SYCL™ Specification

Generic heterogeneous computing for modern C++

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2. Introduction

SYCL (pronounced “sickle”) is a royalty-free, cross-platform abstraction C++ programming model for heterogeneous computing. SYCL builds on the underlying concepts, portability and efficiency of parallel API or standards like OpenCL while adding much of the ease of use and flexibility of single-source C++.

Developers using SYCL are able to write standard modern C++ code, with many of the techniques they are accustomed to, such as inheritance and templates. At the same time developers have access to the full range of capabilities of the underlying implementation (such as OpenCL) both through the features of the SYCL libraries and, where necessary, through interoperation with code written directly using the underneath implementation, via their APIs.

To reduce programming effort and increase the flexibility with which developers can write code, SYCL extends the concepts found in standards like OpenCL model in two ways beyond the general use of C++ features:

- Execution of parallel kernels on a heterogeneous device is made simultaneously convenient and flexible. Common parallel patterns are prioritised with simple syntax, which through a series C++ types allow the programmer to express additional requirements, such as synchronization, if needed.

- Data access in SYCL is separated from data storage. By relying on the C++-style resource acquisition is initialization (RAII) idiom to capture data dependencies between device code blocks, the runtime library can track data movement and provide correct behavior without the complexity of manually managing event dependencies between kernel instances and without the programming having to explicitly move data. This approach enables the data-parallel task-graphs that might be already part of the execution model to be built up easily and safely by SYCL programmers. To primarily assist with porting codes, SYCL provides an alternative way to manage data using Unified Shared Memory (USM).

- The hierarchical parallelism syntax offers a way of expressing the data-parallel similar to the OpenCL device or OpenMP target device execution model in an easy-to-understand modern C++ form. It more cleanly layers parallel loops and synchronization points to avoid fragmentation of code and to more efficiently map to CPU-style architectures.

SYCL retains the execution model, runtime feature set and device capabilities inspired by the OpenCL standard. This standard imposes some limitations on the full range of C++ features that SYCL is able to support. This ensures portability of device code across as wide a range of devices as possible. As a result, while the code can be written in standard C++ syntax with interoperability with standard C++ programs, the entire set of C++ features is not available in SYCL device code. In particular, SYCL device code, as defined by this specification, does not support virtual function calls, function pointers in general, exceptions, runtime type information or the full set of C++ libraries that may depend on these features or on features of a particular host compiler. Nevertheless, these basic restrictions can be relieved by some specific Khronos or vendor extensions.

SYCL implements a single-source multiple compiler-passes (SMCP) design which offers the power of source integration while allowing toolchains to remain flexible. The SMCP design supports embedding of code intended to be compiled for a device, for example a GPU, inline with host code. This embedding of code offers three primary benefits:
**Simplicity** For novice programmers using frameworks like OpenCL, the separation of host and device source code in OpenCL can become complicated to deal with, particularly when similar kernel code is used for multiple different operations on different data types. A single compiler flow and integrated tool chain combined with libraries that perform a lot of simple tasks simplifies initial OpenCL programs to a minimum complexity. This reduces the learning curve for programmers new to heterogeneous programming and allows them to concentrate on parallelization techniques rather than syntax.

**Reuse** C++’s type system allows for complex interactions between different code units and supports efficient abstract interface design and reuse of library code. For example, a *transform* or *map* operation applied to an array of data may allow specialization on both the operation applied to each element of the array and on the type of the data. The SMCP design of SYCL enables this interaction to bridge the host code/device code boundary such that the device code to be specialized on both of these factors directly from the host code.

**Efficiency** Tight integration with the type system and reuse of library code enables a compiler to perform inlining of code and to produce efficient specialized device code based on decisions made in the host code without having to generate kernel source strings dynamically.

The use of C++ features such as generic programming, templated code, functional programming and inheritance on top of existing heterogeneous execution model opens a wide scope for innovation in software design for heterogeneous systems. Clean integration of device and host code within a single C++ type system enables the development of modern, templated generic and adaptable libraries that build simple, yet efficient, interfaces to offer more developers access to heterogeneous computing capabilities and devices. SYCL is intended to serve as a foundation for innovation in programming models for heterogeneous systems, that builds on open and widely implemented standard foundation like OpenCL or Vulkan.

SYCL is designed to be as close to standard C++ as possible. In practice, this means that as long as no dependence is created on SYCL’s integration with the underlying implementation, a standard C++ compiler can compile the SYCL programs and they will run correctly on a host CPU. Any use of specialized low-level features can be masked using the C pre-processor in the same way that compiler-specific intrinsics may be hidden to ensure portability between different host compilers.

SYCL is designed to allow a compilation flow where the source file is passed through multiple different compilers, including a standard C++ host compiler of the developer’s choice, and where the resulting application combines the results of these compilation passes. This is distinct from a single-source flow that might use language extensions that preclude the use of a standard host compiler. The SYCL standard does not preclude the use of a single compiler flow, but is designed to not require it.

The advantages of this design are two-fold. First, it offers better integration with existing tool chains. An application that already builds using a chosen compiler can continue to do so when SYCL code is added. Using the SYCL tools on a source file within a project will both compile for a device and let the same source file be compiled using the same host compiler that the rest of the project is compiled with. Linking and library relationships are unaffected. This design simplifies porting of pre-existing applications to SYCL. Second, the design allows the optimal compiler to be chosen for each device where different vendors may provide optimized tool-chains.

To summarize, SYCL enables computational kernels to be written inside C++ source files as normal C++ code, leading to the concept of “single-source” programming. This means that software developers can develop and use generic algorithms and data structures using standard C++ template techniques, while still supporting multi-platform, multi-device heterogeneous execution. Access to the low level APIs of an underlying implementation (such as OpenCL) is also supported. The specification has been designed to enable implementation across as wide a variety of platforms as possible as well as ease of integration with other platform-specific technologies, thereby letting both users and implementers build on top of SYCL as an open platform for system-wide heterogeneous processing innovation.
3. SYCL architecture

This chapter describes the structure of a SYCL application, and how the SYCL generic programming model lays out on top of a number of SYCL backends.

3.1 Overview

SYCL is an open industry standard for programming a heterogeneous system. The design of SYCL allows standard C++ source code to be written such that it can run on either an heterogeneous device or on the host.

The terminology used for SYCL inherits historically from OpenCL with some SYCL-specific additions. However SYCL is a generic C++ programming model that can be layed out on top of other heterogeneous APIs apart from OpenCL. SYCL implementations can provide SYCL backends for various heterogeneous APIs, implementing the SYCL general specification on top of them. We refer to this heterogeneous API as the SYCL backend API. The SYCL general specification defines the behavior that all SYCL implementations must expose to SYCL users for a SYCL application to behave as expected.

A function object that can execute on a device exposed by a SYCL backend API is called a SYCL kernel function.

To ensure maximum interoperability with different SYCL backend APIs, software developers can access the SYCL backend API alongside the SYCL general API whenever they include the SYCL backend interoperability headers. However, interoperability is a SYCL backend-specific feature. An application that uses interoperability does not conform to the SYCL general application model, since it is not portable across backends.

The target users of SYCL are C++ programmers who want all the performance and portability features of OpenCL, but with the flexibility to use higher-level C++ abstractions across the host/device code boundary. Developers can use most of the abstraction features of C++, such as templates, classes and operator overloading.

However, some C++ language features are not permitted inside kernels, due to the limitations imposed by the capabilities of the underlying heterogeneous platforms. These features include virtual functions, virtual inheritance, throwing/catching exceptions, and run-time type-information. These features are available outside kernels as normal. Within these constraints, developers can use abstractions defined by SYCL, or they can develop their own on top. These capabilities make SYCL ideal for library developers, middleware providers and application developers who want to separate low-level highly-tuned algorithms or data structures that work on heterogeneous systems from higher-level software development. Software developers can produce templated algorithms that are easily usable by developers in other fields.
3.2 Anatomy of a SYCL application

Below is an example of a typical SYCL application which schedules a job to run in parallel on any heterogeneous device available.

```cpp
#include <SYCL/sycl.hpp>
#include <iostream>

int main()
using namespace sycl;

int data[1024]; // Allocate data to be worked on

// By wrapping all the SYCL work in a {} block, we ensure
// all SYCL tasks must complete before exiting the block,
// because the destructor of resultBuf will wait.
{
    // Create a queue to enqueue work to
    queue myQueue;

    // Wrap our data variable in a buffer
    buffer<int, 1> resultBuf { data, range<1> { 1024 } };

    // Create a command group to issue commands to the queue
    myQueue.submit([&](handler& cgh)
        // request access to the buffer
        accessor writeResult { resultBuf, cgh, write_only, noinit };

        // Enqueue a parallel_for task
        cgh.parallel_for(1024, [=](auto idx) {
            writeResult[idx] = idx;
        }); // End of the kernel function

    }); // End of our commands for this queue

    // End of scope, so we wait for work producing resultBuf to complete

    // Print result
    for (int i = 0; i < 1024; i++)
    std::cout << std::endl;
    return 0;
```

At line 1, we “`#include`” the SYCL header files, which provide all of the SYCL features that will be used.

A SYCL application runs on a SYCL Platform (see Section 3.4). The application is structured in three scopes which specify the different sections: application scope, command group scope and kernel scope. The kernel scope specifies a single kernel function that will be, or has been, compiled by a device compiler and executed on a device. In this example kernel scope is defined by lines 25 to 27. The command group scope specifies a unit of work which is comprised of a SYCL kernel function and accessor. In this example command group scope is defined by lines 20 to 28. The application scope specifies all other code outside of a command group scope. These three scopes are used to control the application flow and the construction and lifetimes of the various objects used within SYCL, as explained in Section 3.8.12.
A SYCL kernel function is the scoped block of code that will be compiled using a device compiler. This code may be defined by the body of a lambda function or by the operator() function of a function object. Each instance of the SYCL kernel function will be executed as a single, though not necessarily entirely independent, flow of execution and has to adhere to restrictions on what operations may be allowed to enable device compilers to safely compile it to a range of underlying devices.

The parallel_for function can be templated with a class. This class is used to manually name the kernel when desired, such as to avoid a compiler-generated name when debugging a kernel defined through a lambda, to provide a known name with which to apply build options to a kernel, or to ensure compatibility with multiple compiler-pass implementations.

The parallel_for member function creates an instance of a kernel object. The kernel object is the entity that will be enqueued within a command group. In the case of parallel_for the SYCL kernel function will be executed over the given range from 0 to 1023. The different member functions to execute kernels can be found in Section 4.10.7.

A command group scope is the syntactic scope wrapped by the construction of a command group function object as seen on line 20. The command group function object may invoke only a single SYCL kernel function, and it takes a parameter of type command group handler, which is constructed by the runtime.

All the requirements for a kernel to execute are defined in this command group scope, as described in Section 3.6.1. In this case the constructor used for myQueue on line 14 is the default constructor, which allows the queue to select the best underlying device to execute on, leaving the decision up to the runtime.

In SYCL, data that is required within a SYCL kernel function must be contained within a buffer or image, as described in Section 3.7. We construct a buffer on line 17. Access to the buffer is controlled via an accessor which is constructed on line 22 through the get_access member function of the buffer. The buffer is used to keep track of access to the data and the accessor is used to request access to the data on a queue, as well as to track the dependencies between SYCL kernel function. In this example the accessor is used to write to the data buffer on line 26. All buffers must be constructed in the application-scope, whereas all accessors must be constructed in the command group scope.

### 3.3 Normative references

The documents in the following list are referred to within this SYCL specification, and their content is a requirement for this document.

1. **C++17**: ISO/IEC 14882:2017 Clauses 1-19 [2], referred to in this specification as the C++ core language.
2. **C++2a**: Working Draft, Standard for Programming Language C++ [3], referred to in this specification as the next C++ specification.

### 3.4 The SYCL platform model

The SYCL platform model is based on the OpenCL platform model. The model consists of a host connected to one or more heterogeneous devices, called devices.

A SYCL context is constructed, either directly by the user or implicitly when creating a queue, to hold all the runtime information required by the SYCL runtime and the SYCL backend to operate on a device, or group of devices. When a group of devices can be grouped together on the same context, they have some visibility of each other’s memory objects. The SYCL runtime can assume that memory is visible across all devices in the same
context. Not all devices exposed from the same platform can be grouped together in the same context.

A SYCL application executes on the host as a standard C++ program. Devices are exposed through different SYCL backends to the SYCL application. The SYCL application submits command group function objects to queues. Each queue enables execution on a given device.

The SYCL runtime then extracts operations from the command group function object, e.g. an explicit copy operation or a SYCL kernel function. When the operation is a SYCL kernel function, the SYCL runtime uses a SYCL backend-specific mechanism to extract the device binary from the SYCL application and pass it to the heterogeneous API for execution on the device.

A SYCL device is divided into one or more compute units (CUs) which are each divided into one or more processing elements (PEs). Computations on a device occur within the processing elements. How computation is mapped to PEs is SYCL backend and device specific. Two devices exposed via two different backends can map computations differently to the same device.

When a SYCL application contains SYCL kernel function objects, the SYCL implementation must provide an offline compilation mechanism that enables the integration of the device binaries into the SYCL application. The output of the offline compiler can be an intermediate representation, such as SPIR-V, that will be finalized during execution or a final device ISA.

A device may expose special purpose functionality as a built-in function. The SYCL API exposes functions to query and dispatch said built-in functions. Some SYCL backends and devices may not support programmable kernels, and only support built-in functions.

### 3.5 The SYCL backend model

SYCL is a generic programming model for the C++ language that can target multiple heterogeneous APIs, such as OpenCL.

SYCL implementations enable these target APIs by implementing SYCL backends. For a SYCL implementation to be conformant on said SYCL backend, it must execute the SYCL generic programming model on the backend.

All SYCL implementations must provide a host SYCL backend, which implements the SYCL API and executes SYCL kernel functions on the host platform. Many implementations will provide additional SYCL backends that execute SYCL kernel functions on an accelerator.

The present document covers the SYCL generic interface available to all SYCL backends. How the SYCL generic interface maps to a particular SYCL backend is defined either by a separate SYCL backend specification document, provided by the Khronos SYCL group, or by the SYCL implementation documentation. Whenever there is a SYCL backend specification document, this takes precedence over SYCL implementation documentation.

When a SYCL user builds their SYCL application, she decides which of the SYCL backends will be used to build the SYCL application. This is called the set of active backends. Implementations must ensure that the active backends selected by the user can be used simultaneously by the SYCL implementation at runtime. If two backends are available at compile time but will produce an invalid SYCL application at runtime, the SYCL implementation must emit a compilation error.

A SYCL application built with a number of active backends does not necessarily guarantee that said backends can be executed at runtime. The subset of active backends available at runtime is called available backends. A backend is said to be available if the host platform where the SYCL application is executed exposes support for the heterogeneous API required for the SYCL backend.
It is implementation dependent whether certain backends require third-party libraries to be available in the system. Building with only the host as an active backend guarantees the binary will be executed on any platform without requiring third-party libraries. Failure to have all dependencies required for all active backends at runtime will cause the SYCL application to not run.

Once the application is running, users can query what SYCL platforms are available. SYCL implementations will expose the devices provided by each backend grouped into platforms. A backend must expose at least one platform.

Under the SYCL backend model, SYCL objects can contain one or multiple references to a certain SYCL backend native type. Not all SYCL objects will map directly to a SYCL backend native type. The mapping of SYCL objects to SYCL backend native types is defined by the SYCL backend specification document when available, or by the SYCL implementation otherwise.

To guarantee that multiple SYCL backend objects can interoperate with each other, SYCL memory objects are not bound to a particular SYCL backend. SYCL memory objects can be accessed from any device exposed by an available backend. SYCL Implementations can potentially map SYCL memory objects to multiple native types in different SYCL backends.

Since SYCL memory objects are independent of any particular SYCL backend, SYCL command groups can request access to memory objects allocated by any SYCL backend, and execute it on the backend associated with the queue. This requires the SYCL implementation to be able to transfer memory objects across SYCL backends.

When a SYCL application runs on any number of SYCL backends without relying on any SYCL backend-specific behaviour or interoperability, it is said to be a SYCL general application, and it is expected to run in any SYCL-conformant implementation that supports the required features for the application.

### 3.5.1 Platform mixed version support

The SYCL generic programming model exposes a number of platforms, each of them exposing a number of devices. Each platform is bound to a certain SYCL backend. SYCL devices associated with said platform are associated with that SYCL backend.

Although the APIs in the SYCL generic programming model are defined according to this specification and their version is indicated by the macro `SYCL_LANGUAGE_VERSION`, this does not apply to APIs exposed by the SYCL backends. Each SYCL backend provides its own document that defines its APIs, and that document tells how to query for the device and platform versions.

### 3.6 SYCL execution model

As Section 3.2 describes, a SYCL application is comprised of three scopes: application scope, command group scope, and kernel scope. Code in the application scope and command group scope runs on the host and is governed by the SYCL application execution model. Code in the kernel scope runs on a device and is governed by the SYCL kernel execution model.

A SYCL application can be executed on the host directly without a physical accelerator present when using the SYCL host backend.
3.6 SYCL EXECUTION MODEL

3.6.1 SYCL application execution model

The SYCL application defines the execution order of the kernels by grouping each kernel with its requirements into a command group function object. Command group function objects are submitted to execution via a queue object, which defines the device where the kernel will run. The same command group object can be submitted to different queues. When a command group is submitted to a SYCL queue, the requirements of the kernel execution are captured. The kernels are executed as soon as their requirements have been satisfied.

3.6.1.1 SYCL backend resources managed by the SYCL application

The SYCL runtime integrated with the SYCL application will manage the resources required by the SYCL backend API to manage the heterogeneous devices it is providing access to. This includes, but is not limited to, resource handlers, memory pools, dispatch queues and other temporary handler objects.

The SYCL programming interface represents the lifetime of the resources managed by the SYCL application using RAII rules. Construction of a SYCL object will typically entail the creation of multiple SYCL backend objects, which will be properly released on destruction of said SYCL object. The overall rules for construction and destruction are detailed in the SYCL Programming Interface chapter 4. Those SYCL backends with a SYCL backend document will detail how the resource management from SYCL objects map down to the SYCL backend objects.

In SYCL, the minimum required object for submitting work to devices is the queue, which contains references to a platform, device and a context internally.

The resources managed by SYCL are:

1. **Platforms**: all features of SYCL backend APIs are implemented by platforms. A platform can be viewed as a given vendor’s runtime and the devices accessible through it. Some devices will only be accessible to one vendor’s runtime and hence multiple platforms may be present. SYCL manages the different platforms for the user.

2. **Contexts**: any SYCL backend resource that is acquired by the user is attached to a context. A context contains a collection of devices that the host can use and manages memory objects that can be shared between the devices. Devices belonging to the same context must be able to access each other’s global memory using some implementation-specific mechanism. A given context can only wrap devices owned by a single platform.

3. **Devices**: platforms provide one or more devices for executing SYCL kernels. In SYCL, a device is accessible through a `sycl::device` object.

4. **Kernels**: the SYCL functions that run on SYCL devices are defined as C++ function objects (a named function object type or a lambda function).

   Note that some SYCL backends may expose non-programmable functionality as pre-defined kernels.

5. **Modules**: The SYCL backend API will expose the device binaries in some form of a program-object file or module. This is handled by SYCL using the `module` objects.

6. **Queues**: SYCL kernels execute in command queues. The user must create a queue, which references an associated context, platform and device. The context, platform and device may be chosen automatically, or specified by the user. SYCL queues execute kernels on a particular device of a particular context, but can have dependencies from any device on any available SYCL backend.
The SYCL implementation guarantees the correct initialization and destruction of any resource handled by the underlying SYCL backend API, except for those the user has obtained manually via the SYCL interoperability API.

### 3.6.1.2 SYCL command groups and execution order

By default, SYCL queues execute kernel functions in an out-of-order fashion based on dependency information. Developers only need to specify what data is required to execute a particular kernel. The SYCL runtime will guarantee that kernels are executed in an order that guarantees correctness. By specifying access modes and types of memory, a directed acyclic dependency graph (DAG) of kernels is built at runtime. This is achieved via the usage of command group objects. A SYCL command group object defines a set of requisites ($R$) and a kernel function ($k$). A command group is submitted to a queue when using the `sycl::queue::submit` member function.

A requisite ($r_i$) is a requirement that must be fulfilled for a kernel-function ($k$) to be executed on a particular device. For example, a requirement may be that certain data is available on a device, or that another command group has finished execution. An implementation may evaluate the requirements of a command group at any point after it has been submitted. The processing of a command group is the process by which a SYCL runtime evaluates all the requirements in a given $R$. The SYCL runtime will execute $k$ only when all $r_i$ are satisfied (i.e., when all requirements are satisfied). To simplify the notation, in the specification we refer to the set of requirements of a command group named `foo` as $CG_{foo} = r_1, \ldots, r_n$.

The evaluation of a requisite ($\text{Satisfied}(r_i)$) returns the status of the requisite, which can be True or False. A satisfied requisite implies the requirement is met. Satisfied($r_i$) never alters the requisite, only observes the current status. The implementation may not block to check the requisite, and the same check can be performed multiple times.

An action ($a_i$) is a collection of implementation-defined operations that must be performed in order to satisfy a requisite. The set of actions for a given command group $A$ is permitted to be empty if no operation is required to satisfy the requirement. The notation $a_i$ represents the action required to satisfy $r_i$. Actions of different requisites can be satisfied in any order w.r.t each other without side effects (i.e., given two requirements $r_1$ and $r_2$, $(r_1, r_2) \equiv (r_2, r_1)$). The intersection of two actions is not necessarily empty. Actions can include (but are not limited to): memory copy operations, mapping operations, host side synchronization, or implementation-specific behavior.

Finally, Performing an action ($\text{Perform}(a_i)$) executes the action operations required to satisfy the requisite $r_j$. Note that, after $\text{Perform}(a_i)$, the evaluation $\text{Satisfied}(r_j)$ will return True until the kernel is executed. After the kernel execution, it is not defined whether a different command group with the same requirements needs to perform the action again, where actions of different requisites inside the same command group object can be satisfied in any order w.r.t each other without side effects: Given two requirements $r_j$ and $r_k$, $\text{Perform}(a_j)$ followed by $\text{Perform}(a_k)$ is equivalent to $\text{Perform}(a_k)$ followed by $\text{Perform}(a_j)$.

The requirements of different command groups submitted to the same or different queues are evaluated in the relative order of submission. Command group objects whose intersection of requirement sets is not empty are said to depend on each other. They are executed in order of submission to the queue. If command groups are submitted to different queues or by multiple threads, the order of execution is determined by the SYCL runtime. Note that independent command group objects can be submitted simultaneously without affecting dependencies.

Figure 3.1 illustrates the execution order of three command group objects ($CG_a, CG_b, CG_c$) with certain requirements submitted to the same queue. Both $CG_a$ and $CG_b$ only have one requirement, $r_1$ and $r_2$ respectively. $CG_c$ requires both $r_1$ and $r_2$. This enables the SYCL runtime to potentially execute $CG_a$ and $CG_b$ simultaneously, whereas $CG_c$ cannot be executed until both $CG_a$ and $CG_b$ have been completed. The SYCL runtime evaluates the requisites and performs the actions required (if any) for the $CG_a$ and $CG_b$. When evaluating the requisites of $CG_c$, they will be satisfied once the $CG_a$ and $CG_b$ have finished.
3.6. SYCL EXECUTION MODEL

SYCL Application Enqueue Order

```cpp
sycl::queue syclQueue;
syclQueue.submit(CG_a(r_1));
syclQueue.submit(CG_b(r_2));
syclQueue.submit(CG_c(r_1, r_2));
```

SYCL Kernel Execution Order

```plaintext
CG_a(r_1) -> CG_b(r_2) -> CG_c(r_1, r_2)
```

Figure 3.1: Execution order of three command groups submitted to the same queue.

Figure 3.2 uses three separate SYCL queue objects to submit the same command group objects as before. Regardless of using three different queues, the execution order of the different command group objects is the same. When different threads enqueue to different queues, the execution order of the command group will be the order in which the submit member function is executed. In this case, since the different command group objects execute on different devices, the actions required to satisfy the requirements may be different (e.g., the SYCL runtime may need to copy data to a different device in a separate context).

SYCL Application Enqueue Order

```cpp
sycl::queue syclQueue1; sycl::queue syclQueue2; sycl::queue syclQueue3;
syclQueue1.submit(CG_a(r_1));
syclQueue2.submit(CG_b(r_2));
syclQueue3.submit(CG_c(r_1, r_2));
```

Figure 3.2: Execution order of three command groups submitted to the different queues.

3.6.2 SYCL kernel execution model

When a kernel is submitted for execution, an index space is defined. An instance of the kernel body executes for each point in this index space. This kernel instance is called a work-item and is identified by its point in the index space, which provides a global id for the work-item. Each work-item executes the same code but the specific execution pathway through the code and the data operated upon can vary by using the work-item global id to specialize the computation.

Work-items are organized into work-groups. The work-groups provide a more coarse-grained decomposition of the index space. Each work-group is assigned a unique work-group id with the same dimensionality as the index space used for the work-items. Work-items are each assigned a local id, unique within the work-group, so that a single work-item can be uniquely identified by its global id or by a combination of its local id and work-group id. The work-items in a given work-group execute concurrently on the processing elements of a single compute unit.

The index space supported in SYCL is called an nd-range. An ND-range is an N-dimensional index space, where N is one, two or three. In SYCL, the ND-range is represented via the `nd_range<N>` class. An `nd_range<N>` is
made up of a global range and a local range, each represented via values of type `range<N>` and a global offset, represented via a value of type `id<N>`. The types `range<N>` and `id<N>` are each an N-element array of integers. The iteration space defined via an `nd_range<N>` is an N-dimensional index space starting at the ND-range’s global offset whose size is its global range, split into work-groups of the size of its local range.

Each work-item in the ND-range is identified by a value of type `nd_item<N>`. The type `nd_item<N>` encapsulates a global id, local id and work-group id, all of type `id<N>`, the iteration space offset also of type `id<N>`, as well as global and local ranges and synchronization operations necessary to make work-groups useful. Work-groups are assigned ids using a similar approach to that used for work-item global ids. Work-items are assigned to a work-group and given a local id with components in the range from zero to the size of the work-group in that dimension minus one. Hence, the combination of a work-group id and the local id within a work-group uniquely defines a work-item.

SYCL allows a simplified execution model in which the work-group size is left unspecified. A kernel invoked over a `range<N>`, instead of an `nd_range<N>` is executed within an iteration space of unspecified work-group size. In this case, less information is available to each work-item through the simpler `item<N>` class.

SYCL allows SYCL backends to expose fixed functionality as non-programmable kernels. The behavior of these functions are SYCL backend specific, and do not necessarily follow the SYCL kernel execution model.

## 3.7 Memory model

Since SYCL is a single-source programming model, the memory model affects both the application and the device kernel parts of a program. On the SYCL application, the SYCL runtime will make sure data is available for execution of the kernels. On the SYCL device kernel, the SYCL backend rules describing how the memory behaves on a specific device are mapped to SYCL C++ constructs. Thus it is possible to program kernels efficiently in pure C++.

### 3.7.1 SYCL application memory model

The application running on the host uses SYCL buffer objects using instances of the `sycl::buffer` class to allocate memory in the global address space, or can allocate specialized image memory using the `sycl::unsampled_image` and `sycl::sampled_image` classes.

In the SYCL application, memory objects are bound to all devices in which they are used, regardless of the SYCL context where they reside. SYCL memory objects (namely, buffer and image objects) can encapsulate multiple underlying SYCL backend memory objects together with multiple host memory allocations to enable the same object to be shared between devices in different contexts, platforms or backends.

The order of execution of command group objects ensures a sequentially consistent access to the memory from the different devices to the memory objects.

To access a memory object, the user must create an accessor object which parameterizes the type of access to the memory object that a kernel or the host requires. The accessor object defines a requirement to access a memory object, and this requirement is defined by construction of an accessor, regardless of whether there are any uses in a kernel or by the host. The `cl::sycl::accessor` object specifies whether the access is via global memory, constant memory or image samplers and their associated access functions. The accessor also specifies whether the access is read-only (RO), write-only (WO) or read-write (RW). An optional discard flag can be added to an accessor to tell the system to discard any previous contents of the data the accessor refers to, e.g. discard write-only (DW). For simplicity, when a requisite represents an accessor object in a certain access mode, we represent it as `MemoryObject<AccessMode>`. For example, an accessor that accesses memory object `buf1` in RW mode is represented...
as \textit{buf1}\textsubscript{RW}. A \textbf{command group} object that uses such an accessor is represented as \( CG(bu f1\textsubscript{RW}) \). The \textbf{action} required to satisfy a requisite and the location of the latest copy of a memory object will vary depending on the implementation.

Figure 3.3 illustrates an example where \textbf{command group} objects are enqueued to two separate SYCL queues executing in devices in different contexts. The \textbf{requisites} for the \textbf{command group} execution are the same, but the \textbf{actions} to satisfy them are different. For example, if the data is on the host before execution, \( A(b1\textsubscript{RW}) \) and \( A(b2\textsubscript{RW}) \) can potentially be implemented as copy operations from the host memory to context\textsubscript{1} or context\textsubscript{2} respectively. After \( CG_a \) and \( CG_b \) are executed, \( A'(b1\textsubscript{RW}) \) will likely be an empty operation, since the result of the kernel can stay on the device. On the other hand, the results of \( CG_b \) are now on a different context than \( CG_c \) is executing, therefore \( A'(b2\textsubscript{RW}) \) will need to copy data across two separate OpenCL contexts using an implementation specific mechanism.

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{figure3.3.png}
\caption{Actions performed when three command groups are submitted to two distinct queues, and potential implementation in an OpenCL SYCL \textbf{backend} by a SYCL runtime. Note that in this example, each SYCL buffer \((b1, b2)\) is implemented as separate \texttt{cl\_mem} objects per context.}
\end{figure}

Note that the order of the definition of the accessors within the \textbf{command group} is irrelevant to the requirements they define. All accessors always apply to the entire \textbf{command group} object where they are defined.

When multiple \textbf{accessors} in the same \textbf{command group} define different requisites to the same memory object these requisites must be resolved.

Firstly, any requisites with different access modes but the same access target are resolved into a single requisite with the union of the different access modes according to Table 3.1. The atomic access mode acts as if it was read-
write (RW) when determining the combined requirement. The rules in Table 3.1 are commutative and associative.

<table>
<thead>
<tr>
<th>One access mode</th>
<th>Other access mode</th>
<th>Combined requirement</th>
</tr>
</thead>
<tbody>
<tr>
<td>read (RO)</td>
<td>write (WO)</td>
<td>read-write (RW)</td>
</tr>
<tr>
<td>read (RO)</td>
<td>read-write (RW)</td>
<td>read-write (RW)</td>
</tr>
<tr>
<td>write (WO)</td>
<td>read-write (RW)</td>
<td>read-write (RW)</td>
</tr>
<tr>
<td>discard-write (DW)</td>
<td>discard-read-write (DRW)</td>
<td>discard-read-write (DRW)</td>
</tr>
<tr>
<td>discard-write (DW)</td>
<td>read (RO)</td>
<td>read-write (RW)</td>
</tr>
<tr>
<td>discard-write (DW)</td>
<td>read-write (RW)</td>
<td>read-write (RW)</td>
</tr>
<tr>
<td>discard-read-write (DRW)</td>
<td>write (WO)</td>
<td>write (WO)</td>
</tr>
<tr>
<td>discard-read-write (DRW)</td>
<td>read (RO)</td>
<td>read-write (RW)</td>
</tr>
<tr>
<td>discard-read-write (DRW)</td>
<td>read-write (RW)</td>
<td>read-write (RW)</td>
</tr>
</tbody>
</table>

Table 3.1: Combined requirement from two different accessor access modes within the same command group. The rules are commutative and associative.

The result of this should be that there should not be any requisites with the same access target.

Secondly, the remaining requisites must adhere to the following rule. Only one of the requisites may have write access (W or RW), otherwise the SYCL runtime must throw an exception. All requisites create a requirement for the data they represent to be made available in the specified access target, however only the requisite with write access determines the side effects of the command group, i.e. only the data which that requisite represents will be updated.

For example:

- \( CG(b_1^{G, RW}, b_1^{H}) \) is permitted.
- \( CG(b_1^{G, RW}, b_1^{H}) \) is not be permitted.
- \( CG(b_1^{G, W}, b_1^{C, RW}) \) is not be permitted.

Where \( G, C \) and \( H \) correspond to the global_buffer, constant_buffer and host_buffer respectively.

A buffer created from a range of an existing buffer is called a sub-buffer. A buffer may be overlaid with any number of sub-buffers. Accessors can be created to operate on these sub-buffers. Refer to 4.7.2 for details on sub-buffer creation and restrictions. A requirement to access a sub-buffer is represented by specifying its range, e.g. \( CG(b_1^{G, RW}[0,5]) \) represents the requirement of accessing the range \([0,5]\) buffer \( b_1 \) in read write mode.

If two accessors are constructed to access the same buffer, but both are to non-overlapping sub-buffers of the buffer, then the two accessors are said to not overlap, otherwise the accessors do overlap. Overlapping is the test that is used to determine the scheduling order of command groups. Command-groups with non-overlapping requirements may execute concurrently.

It is permissible for command groups that only read data to not copy that data back to the host or other devices after reading and for the runtime to maintain multiple read-only copies of the data on multiple devices.

A special case of requirement is the one defined by a host accessor. Host accessors are represented with \( H(MemoryObject, accessMode) \), e.g. \( H(b_1^{G, RW}) \) represents a host accessor to \( b_1 \) in read-write mode. Host accessors are a special type of accessor constructed from a memory object outside a command group, and require that the data associated with the given memory object is available on the host in the given pointer. This causes the runtime to block on construction of this object until the requirement has been satisfied. Host accessor objects are effec-
3.7. MEMORY MODEL SYCL 2020 provisional

SYCL Application Enqueue Order

```cpp
sycl::queue q1(context1);
q1.submit(CG_a(b1_RW[0,10]));
q1.submit(CG_b(b1_RW[10,20]));
q1.submit(CG_c(b1_RW[5,15]));
```

SYCL Kernel Execution Order

```cpp
CG_a(b1_RW[0,10])
CG_b(b1_RW[10,20])
CG_c(b1_RW[5,15])
```

Figure 3.4: Requirements on overlapping vs non-overlapping sub-buffer.

SYCL Application Enqueue Order

```cpp
sycl::queue q1; q1.submit(CG_a(b1RW));
q1.submit(CG_b(b2RW));
H(b1RW);
q1.submit(CG_c(b1RW,b2RW));
```

SYCL Kernel Execution Order

```cpp
CG_a(b1RW)
CG_b(b2RW)
CG_c(b1RW,b2RW)
```

Figure 3.5: Execution of command groups when using host accessors.

3.7.2 SYCL device memory model

The memory model for SYCL devices is based on the OpenCL 1.2 memory model. Work-items executing in a kernel have access to four distinct address spaces (memory regions) and a virtual address space overlapping some concrete address spaces:

- **Global memory** is accessible to all work-items in all work-groups. Work-items can read from or write to any element of a global memory object. Reads and writes to global memory may be cached depending on the capabilities of the device. Global memory is persistent across kernel invocations, however there is no guarantee that two concurrently executing kernels can simultaneously write to the same memory object and expect correct results.

- **Constant memory** is a region of global memory that remains constant during the execution of a kernel. The host allocates and initializes memory objects placed into constant memory.
• **Local memory** is accessible to all work-items in a single work-group and inaccessible to work-items in other work-groups. This memory region can be used to allocate variables that are shared by all work-items in a work-group. Work-group-level visibility allows local memory to be implemented as dedicated regions of the device memory where this is appropriate.

• **Private memory** is a region of memory private to a work-item. Variables defined in one work-item’s private memory are not visible to another work-item.

• **Generic memory** is a virtual address space which overlaps the global, local and private address spaces.

3.7.2.1 Access to memory

Accessors in the device kernels provide access to the memory objects, acting as pointers to the corresponding address space.

It is not possible to pass a pointer into host memory directly as a kernel parameter because the devices may be unable to support the same address space as the host.

To allocate local memory within a kernel, the user can either pass a `sycl::local_accessor` object to the kernel as a parameter, or can define a variable in work-group scope inside `sycl::parallel_for_work_group`.

Any variable defined inside a `sycl::parallel_for` scope or `sycl::parallel_for_work_item` scope will be allocated in private memory. Any variable defined inside a `sycl::parallel_for_work_group` scope will be allocated in local memory.

Users can create accessors that reference sub-buffers as well as entire buffers.

Within kernels, the underlying C++ pointer types can be obtained from an accessor. The pointer types will contain a compile-time deduced address space. So, for example, if a C++ pointer is obtained from an accessor to global memory, the C++ pointer type will have a global address space attribute attached to it. The address space attribute will be compile-time propagated to other pointer values when one pointer is initialized to another pointer value using a defined algorithm.

When developers need to explicitly state the address space of a pointer value, one of the explicit pointer classes can be used. There is a different explicit pointer class for each address space: `sycl::raw_local_ptr`, `sycl::raw_global_ptr`, `sycl::raw_private_ptr`, `sycl::raw_constant_ptr`, `sycl::raw_generic_ptr`, `sycl::decorated_local_ptr`, `sycl::decorated_global_ptr`, `sycl::decorated_private_ptr`, `sycl::decorated_constant_ptr`, or `sycl::decorated_generic_ptr`. The classes with the decorated prefix expose pointers that use an implementation defined address space decoration, while the classes with the raw prefix do not. Accessors with an access target `global_buffer`, `constant_buffer`, or `local`, can be converted into explicit pointer classes (multi_ptr). Explicit pointer class values cannot be passed as parameters to kernels or stored in global memory.

For templates that need to adapt to different address spaces, a `sycl::multi_ptr` class is defined which is templated via a compile-time constant enumerator value to specify the address space.

3.7.2.2 Memory consistency inside SYCL kernels

The SYCL memory consistency model is based upon the memory consistency model of the C++ core language. Where SYCL offers extensions to classes and functions that may affect memory consistency (e.g. `sycl::atomic_ref`), the default behavior when these extensions are not used always matches the behavior of standard C++.
A SYCL implementation must guarantee that the same memory consistency model is used across host and device code. Every device compiler must support the memory model defined by the minimum version of C++ described in Section 3.8.1; SYCL implementations supporting additional versions of C++ must also support the corresponding memory models.

The full C++ memory model is not guaranteed to be supported by every device, nor across all combinations of devices within a context. The memory orderings supported by a specific device and context can be queried using functionalities of the `sycl::device` and `sycl::context` classes, respectively.

Within a SYCL kernel, all memory has load/store consistency within a work-item. Ensuring memory consistency across different work-items requires careful usage of group barrier operations, mem-fence operations and atomic operations. On any SYCL device, local and global memory may be made consistent across work-items in a single group through use of a group barrier operation. On SYCL devices supporting acquire-release or sequentially consistent memory orderings, all memory visible to a set of work-items may be made consistent (with a single happens-before relation) across the work-items in that set through the use of mem-fence and atomic operations.

The set of work-items and devices to which the memory ordering constraints of a given atomic operation apply is controlled by a memory scope constraint, which can take one of the following values:

- `memory_scope::work_item` The ordering constraint applies only to the calling work-item. This is only useful for image operations, since all other operations within a work-item are guaranteed to execute in program order.

- `memory_scope::sub_group` The ordering constraint applies only to work-items in the same sub-group as the calling work-item.

- `memory_scope::work_group` The ordering constraint applies only to work-items in the same work-group as the calling work-item. This is the broadest scope that can be applied to atomic operations in work-group local memory. Using any broader scope for atomic operations in work-group local memory is treated as though `memory_scope::work_group` was specified.

- `memory_scope::device` The ordering constraint applies only to work-items executing on the same device as the calling work-item.

- `memory_scope::system` The ordering constraint applies to any work-item or host thread in the system that is currently permitted to access the memory allocation containing the referenced object, as defined by the capabilities of buffers and USM.

[Note for this provisional version: The addition of memory scopes to the C++ memory model modifies the definition of some concepts from the C++ core language. For example: data races, the synchronizes-with relationship and sequential consistency must be defined in a way that accounts for atomic operations with differing (but compatible) scopes, in a manner similar to the OpenCL 2.0 specification [4]. Modified definitions of these concepts will be included in the final version of this specification. — end note]

These memory consistency guarantees are independent of any forward progress guarantees. Communication and synchronization between work-items in different work-groups is unsafe in general, but is supported on devices where all of the following conditions are true:

- Acquire-release or sequentially consistent memory ordering is supported at device scope
- Work-items in different work-groups make independent forward progress
3.7.2.3 Atomic operations

Atomic operations can be performed on memory in buffers and USM. The range of atomic operations available on a specific device is limited by the atomic capabilities of that device. The `sycl::atomic_ref` class must be used to provide safe atomic access to the buffer or USM allocation from device code.

3.8 The SYCL programming model

A SYCL program is written in standard C++. Host code and device code is written in the same C++ source file, enabling instantiation of templated kernels from host code and also enabling kernel source code to be shared between host and device. The device kernels are encapsulated C++ callable types (a function object with `operator()` or a lambda function), which have been designated to be compiled as SYCL kernels.

SYCL programs target heterogeneous systems. The kernels may be compiled and optimized for multiple different processor architectures with very different binary representations.

3.8.1 Minimum version of C++

The C++ features used in SYCL are based on a specific version of C++. Implementations of SYCL must support this minimum C++ version, which defines the C++ constructs that can consequently be used by SYCL feature definitions (for example, lambdas).

The minimum C++ version of this SYCL specification is determined by the normative C++ core language defined in Section 3.3. All implementations of this specification must support at least this core language, and features within this specification are defined using features of the core language. Note that not all core language constructs are supported within SYCL kernel functions or code invoked by a SYCL kernel function, as detailed by Section 5.4.

Implementations may support newer C++ versions than the minimum required by SYCL. Code written using newer features than the SYCL requirement, though, may not be portable to other implementations that don’t support the same C++ version.

3.8.2 Alignment with future versions of C++

Some features of SYCL are aligned with the next C++ specification, as defined in Section 3.3.

The following features are pre-adopted by SYCL 2020 and made available in the `sycl::` namespace: `std::span`, `std::bit_cast`. The implementations of pre-adopted features are compliant with the next C++ specification, and are expected to forward directly to standard C++ features in a future version of SYCL.

The following features of SYCL 2020 use syntax based on the next C++ specification: `sycl::atomic_ref`. These features behave as described in the next C++ specification, barring modifications to ensure compatibility with other SYCL 2020 features and heterogeneous programming. Any such modifications are documented in the corresponding sections of this specification.

3.8.3 Basic data parallel kernels

Data-parallel kernels that execute as multiple work-items and where no local synchronization is required are enqueued with the `sycl::parallel_for` function parameterized by a `sycl::range` parameter. These kernels will execute the kernel function body once for each work-item in the range. The range passed to `sycl::parallel_for`
represents the global size of a SYCL kernel and will be divided into work-groups whose size is chosen by the SYCL runtime. Barrier synchronization is not valid within these work-groups.

Variables with reduction semantics can be added to basic data parallel kernels using the features described in Section 4.10.2.

### 3.8.4 Work-group data parallel kernels

Data parallel kernels can also execute in a mode where the set of work-items is divided into work-groups of user-defined dimensions. The user specifies the global range and local work-group size as parameters to the `sycl::parallel_for` function with a `sycl::nd_range` parameter. In this mode of execution, kernels execute over the `nd_range` in work-groups of the specified size. It is possible to share data among work-items within the same work-group in local or global memory and to synchronize between work-items in the same work-group by calling the `group_barrier` function. All work-groups in a given `parallel_for` will be the same size, and the global size defined in the `nd_range` must be a multiple of the work-group size in each dimension.

Work-groups may be further subdivided into sub-groups. The size and number of sub-groups is implementation-defined and may differ for each kernel, and different devices may make different guarantees with respect to how sub-groups within a work-group are scheduled. The maximum number of work-items in any sub-group in a kernel is based on a combination of the kernel and its dispatch dimensions. The size of any sub-group in the dispatch is between 1 and this maximum sub-group size, and the size of an individual sub-group is invariant for the duration of a kernel’s execution. Similarly to work-groups, the work-items within the same sub-group can be synchronized by calling the `group_barrier` function.

To maximize portability across devices, developers should not assume that work-items within a sub-group execute in any particular order, that work-groups are subdivided into sub-groups in a specific way, nor that two sub-groups within a work-group will make independent forward progress with respect to one another.

Variables with reduction semantics can be added to work-group data parallel kernels using the features described in Section 4.10.2.

### 3.8.5 Hierarchical data parallel kernels

[Note for this provisional version: Based on developer and implementation feedback, the hierarchical data parallel kernel feature described next is undergoing improvements to better align with the frameworks and patterns prevalent in modern programming. As this is a key part of the SYCL API and we expect to make changes to it, we temporarily recommend that new codes refrain from using this feature until the new API is finished in a near-future version of the SYCL specification, when full use of the updated feature will be recommended for use in new code. Existing codes using this feature will of course be supported by conformant implementations of this specification. — end note]

The SYCL compiler provides a way of specifying data parallel kernels that execute within work-groups via a different syntax which highlights the hierarchical nature of the parallelism. This mode is purely a compiler feature and does not change the execution model of the kernel. Instead of calling `sycl::parallel_for` the user calls `sycl::parallel_for_work_group` with a `sycl::range` value representing the number of work-groups to launch and optionally a second `sycl::range` representing the size of each work-group for performance tuning. All code within the `parallel_for_work_group` scope effectively executes once per work-group. Within the `parallel_for_work_group` scope, it is possible to call `parallel_for_work_item` which creates a new scope in which all work-items within the current work-group execute. This enables a programmer to write code that looks like there is an inner work-item loop inside an outer work-group loop, which closely matches the effect of the execution model. All variables declared inside the `parallel_for_work_group` scope are allocated in work-
group local memory, whereas all variables declared inside the `parallel_for_work_item` scope are declared in private memory. All `parallel_for_work_item` calls within a given `parallel_for_work_group` execution must have the same dimensions.

### 3.8.6 Kernels that are not launched over parallel instances

Simple kernels for which only a single instance of the kernel function will be executed are enqueued with the `sycl::single_task` function. The kernel enqueued takes no “work-item id” parameter and will only execute once. The behavior is logically equivalent to executing a kernel on a single compute unit with a single work-group comprising only one work-item. Such kernels may be enqueued on multiple queues and devices and as a result may be executed in task-parallel fashion.

### 3.8.7 Pre-defined kernels

Some SYCL backends may expose pre-defined functionality to users as kernels. These kernels are not programmable, hence they are not bound by the SYCL C++ programming model restrictions, and how they are written is implementation-defined.

### 3.8.8 Synchronization

Synchronization of processing elements executing inside a device is handled by the SYCL device kernel following the SYCL kernel execution model. The synchronization of the different SYCL device kernels executing with the host memory is handled by the SYCL application via the SYCL runtime.

#### 3.8.8.1 Synchronization in the SYCL application

Synchronization points between host and device(s) are exposed through the following operations:

- **Buffer destruction**: The destructors for `sycl::buffer`, `sycl::unsampled_image` and `sycl::sampled_image` objects wait for all submitted work on those objects to complete and to copy the data back to host memory before returning. These destructors only wait if the object was constructed with attached host memory and if data needs to be copied back to the host.

  More complex forms of synchronization on buffer destruction can be specified by the user by constructing buffers with other kinds of references to memory, such as `shared_ptr` and `unique_ptr`.

- **Host Accessors**: The constructor for a host accessor waits for all kernels that modify the same buffer (or image) in any queues to complete and then copies data back to host memory before the constructor returns. Any command groups with requirements to the same memory object cannot execute until the host accessor is destroyed (see 3.5).

- **Command group enqueue**: The SYCL runtime internally ensures that any command groups added to queues have the correct event dependencies added to those queues to ensure correct operation. Adding command groups to queues never blocks. Instead any required synchronization is added to the queue and events of type `sycl::event` are returned by the queue’s submit function that contain event information related to the specific command group.

- **Queue operations**: The user can manually use queue operations, such as `wait` to block execution of the calling thread until all the command groups submitted to the queue have finished execution. Note that this will also affect the dependencies of those command groups in other queues.
• **SYCL event objects**: SYCL provides `sycl::event` objects which can be used for synchronization. If synchronization is required across SYCL contexts from different SYCL backends, then the SYCL runtime ensures that extra host-based synchronization is added to enable the SYCL event objects to operate between contexts correctly.

Note that the destructors of other SYCL objects (`sycl::queue`, `sycl::context`, ...) do not block. Only a `sycl::buffer`, `sycl::sampled_image` or `sycl::unsampled_image` destructor might block. The rationale is that an object without any side effect on the host does not need to block on destruction as it would impact the performance. So it is up to the programmer to use a member function to wait for completion in some cases if this does not fit the goal. See Section 3.8.12 for more information on object life time.

### 3.8.8.2 Synchronization in SYCL kernels

In SYCL, synchronization can be either global or local within a group of work-items. Synchronization between work-items in a single group is achieved using a group barrier.

All the work-items of a group must execute the barrier before any are allowed to continue execution beyond the barrier. Note that the group barrier must be encountered by all work-items of a group executing the kernel or by none at all. In SYCL, work-group barrier and sub-group barrier functionality is exposed via the `group_barrier` function.

There is no mechanism for synchronization between work-items of different work-groups.

### 3.8.9 Error handling

In SYCL, there are two types of errors: synchronous errors that can be detected immediately when an API call is made, and asynchronous errors that can only be detected later after an API call has returned. Synchronous errors, such as failure to construct an object, are reported immediately by the runtime throwing an exception. Asynchronous errors, such as an error occurring during execution of a kernel on a device, are reported via an asynchronous error-handler mechanism.

Asynchronous errors are not reported immediately as they occur. The asynchronous error handler for a context or queue is called with a `sycl::exception_list` object, which contains a list of asynchronously-generated exception objects, on the conditions described by 4.15.1.1 and 4.15.1.2.

Asynchronous errors may be generated regardless of whether the user has specified any asynchronous error handler(s), as described in 4.15.1.2.

Some SYCL backends can report errors that are specific to the platform they are targeting, or that are more concrete than the errors provided by the SYCL API. Any error reported by a SYCL backend must derive from the base `sycl::exception`. When a user wishes to capture specifically an error thrown by a SYCL backend, she must include the SYCL backend-specific headers for said SYCL backend.

### 3.8.10 Fallback mechanism

A command group function object can be submitted either to a single queue to be executed on, or to a secondary queue. If a command group function object fails to be enqueued to the primary queue, then the system will attempt to enqueue it to the secondary queue, if given as a parameter to the submit function. If the command group function object fails to be queued to both of these queues, then a synchronous SYCL exception will be thrown.

It is possible that a command group may be successfully enqueued, but then asynchronously fail to run, for some
reason. In this case, it may be possible for the runtime system to execute the command group function object on the secondary queue, instead of the primary queue. The situations where a SYCL runtime may be able to achieve this asynchronous fall-back is implementation-defined.

### 3.8.11 Scheduling of kernels and data movement

A command group function object takes a reference to a command group handler as a parameter and anything within that scope is immediately executed and takes the handler object as a parameter. The intention is that a user will perform calls to SYCL functions, member functions, destructors and constructors inside that scope. These calls will be non-blocking on the host, but enqueue operations to the queue that the command group is submitted to. All user functions within the command group scope will be called on the host as the command group function object is executed, but any device kernels it invokes will be added to the SYCL queue. All kernels added to the queue will be executed out-of-order from each other, according to their data dependencies.

### 3.8.12 Managing object lifetimes

A SYCL application does not initialize any SYCL backend features until a sycl::context object is created. A user does not need to explicitly create a sycl::context object, but they do need to explicitly create a sycl::queue object, for which a sycl::context object will be implicitly created if not provided by the user.

All SYCL backend objects encapsulated in SYCL objects are reference-counted and will be destroyed once all references have been released. This means that a user needs only create a SYCL queue (which will automatically create an SYCL context) for the lifetime of their application to initialize and release any SYCL backend objects safely.

There is no global state specified to be required in SYCL implementations. This means, for example, that if the user creates two queues without explicitly constructing a common context, then a SYCL implementation does not have to create a shared context for the two queues. Implementations are free to share or cache state globally for performance, but it is not required.

Memory objects can be constructed with or without attached host memory. If no host memory is attached at the point of construction, then destruction of that memory object is non-blocking. The user may use C++ standard pointer classes for sharing the host data with the user application and for defining blocking, or non-blocking behavior of the buffers and images. If host memory is attached by using a raw pointer, then the default behavior is followed, which is that the destructor will block until any command groups operating on the memory object have completed, then, if the contents of the memory object is modified on a device those contents are copied back to host and only then does the destructor return. Instead of a raw pointer, a unique_ptr may be provided, which uses move semantics for initializing and using the associated host memory. In this case, the behavior of the buffer in relation to the user application will be non-blocking on destruction. In the case where host memory is shared between the user application and the SYCL runtime, then the reference counter of the shared_ptr determines whether the buffer needs to copy data back on destruction, and in that case the blocking or non-blocking behavior depends on the user application.

As said in Section 3.8.8, the only blocking operations in SYCL (apart from explicit wait operations) are:

- host accessor constructor, which waits for any kernels enqueued before its creation that write to the corresponding object to finish and be copied back to host memory before it starts processing. The host accessor does not necessarily copy back to the same host memory as initially given by the user;

- memory object destruction, in the case where copies back to host memory have to be done or when the host memory is used as a backing-store.
3.8.13 Device discovery and selection

A user specifies which queue to submit a command group function object and each queue is targeted to run on a specific device (and context). A user can specify the actual device on queue creation, or they can specify a device selector which causes the SYCL runtime to choose a device based on the user’s provided preferences. Specifying a device selector causes the SYCL runtime to perform device discovery. No device discovery is performed until a SYCL device selector is passed to a queue constructor. Device topology may be cached by the SYCL runtime, but this is not required.

Device discovery will return all devices from all platforms exposed by all the supported SYCL backends, including the host backend.

3.8.14 Interfacing with SYCL backend API

There are two styles of developing a SYCL application: (1) Writing a pure SYCL generic application or (2) Writing a SYCL application that relies on some SYCL backend specific behavior.

When users follow (1), there is no assumption about what SYCL backend will be used during compilation or execution of the SYCL application. Therefore, the SYCL backend API is not assumed to be available to the developer. Only standard C++ types and interfaces are assumed to be available, as described in Section 3.8. Users only need to include the `SYCL/sycl.hpp` header to write a SYCL generic application.

On the other hand, when users follow (2), they must know what SYCL backend APIs they are using. In this case, any header required for the normal programmability of the SYCL backend API is assumed to be available to the user. In addition to the `SYCL/sycl.hpp` header, users must also include the SYCL backend-specific header as defined in Section 4.3. The SYCL backend-specific header provides the interoperability interface for the SYCL API to interact with native backend objects. Any type or header from the underlying SYCL backend API is included by the SYCL backend-specific header, under a namespace named after the backend name, e.g. namespace `opencl` would encapsulate the OpenCL headers. This avoids accidental pollution of user space with SYCL backend-specific types.

The interoperability API is defined in Section 4.5.2.

3.9 Memory objects

SYCL memory objects represent data that is handled by the SYCL runtime and can represent allocations in one or multiple devices at any time. Memory objects, both buffers and images, may have one or more underlying native backend objects to ensure that queues objects can use data in any device. A SYCL implementation may have multiple native backend objects for the same device. The SYCL runtime is responsible for ensuring the different copies are up-to-date whenever necessary, using whatever mechanism is available in the system to update the copies of the underlying native backend objects.

[Implementation note: A valid mechanism for this update is to transfer the data from one SYCL backend into the system memory using the SYCL backend-specific mechanism available, and then transfer it to a different device using the mechanism exposed by the new SYCL backend. — end note]

Memory objects in SYCL fall into one of two categories: buffer objects and image objects. A buffer object stores a one-, two- or three-dimensional collection of elements that are stored linearly directly back to back in the same way C or C++ stores arrays. An image object is used to store a one-, two- or three-dimensional texture, frame-buffer or image data that may be stored in an optimized and device-specific format in memory and must be accessed through specialized operations.
Elements of a buffer object can be a scalar data type (such as an `int` or `float`), vector data type, or a user-defined structure. In SYCL, a buffer object is a templated type (`sycl::buffer`), parameterized by the element type and number of dimensions. An image object is stored in one of a limited number of formats. The elements of an image object are selected from a list of predefined image formats which are provided by an underlying SYCL backend implementation. Images are encapsulated in the `sycl::unsampled_image` or `sycl::sampled_image` types, which are templated by the number of dimensions in the image. The minimum number of elements in a memory object is one.

The fundamental differences between a buffer and an image object are:

- Elements in a buffer are stored in an array of 1, 2 or 3 dimensions and can be accessed using an accessor by a kernel executing on a device. The accessors for kernels provide a member function to get C++ pointer types, or the `sycl::global_ptr` or `sycl::constant_ptr` classes.

- Elements of an image are stored in a format that is opaque to the user and cannot be directly accessed using a pointer. SYCL provides image accessors and samplers to allow a kernel to read from or write to an image.

- For a buffer object the data is accessed within a kernel in the same format as it is stored in memory, but in the case of an image object the data is not necessarily accessed within a kernel in the same format as it is stored in memory.

- Image elements are always a 4-component vector (each component can be a float or signed/unsigned integer) in a kernel. The SYCL accessor and sampler member functions to read from an image convert an image element from the format that it is stored into a 4-component vector.

  Similarly, the SYCL accessor member functions provided to write to an image convert the image element from a 4-component vector to the appropriate image format specified such as four 8-bit elements, for example.

Users may want fine-grained control of the synchronization, memory management and storage semantics of SYCL image or buffer objects. For example, a user may wish to specify the host memory for a memory object to use, but may not want the memory object to block on destruction.

Depending on the control and the use cases of the SYCL applications, well established C++ classes and patterns can be used for reference counting and sharing data between user applications and the SYCL runtime. For control over memory allocation on the host and mapping between host and device memory, pre-defined or user-defined C++ `std::allocator` classes are used. For better control of synchronization between a SYCL and a non SYCL application that share data, `std::shared_ptr` and `std::mutex` classes are used.

### 3.10 SYCL device compiler

To enable SYCL to work on a variety of platforms, with different devices, operating systems, build systems and host compilers, SYCL provides a number of options to implementers to enable the compilation of SYCL kernels for devices, while still providing a unified programming model to the user.

#### 3.10.1 Building a SYCL program

A SYCL program runs on a host and one or more SYCL devices. This requires a compilation model that enables compilation for a variety of targets. There is only ever one host for the SYCL program, so the compilation of the source code for the host must happen once and only once. Both kernel and non-kernel source code is compiled for the host.
The design of SYCL enables a single SYCL source file to be passed to multiple, different compilers, using the SMCP technique. This is an implementation option and is not required. What this option enables is for an implementer to provide a device compiler only and not have to provide a host compiler. A programmer who uses such an implementation will compile the same source file twice: once with the host compiler of their choice and once with a device compiler. This approach allows the advantages of having a single source file for both host code and kernels, while still allowing users an independent choice of host and SYCL device compilers.

Only the kernels are compiled for SYCL devices. Therefore, any compiler that compiles only for one or more devices must not compile non-kernel source code. Kernels are contained within C++ source code and may be dependent on lambda capture and template parameters, so compilation of the non-kernel code must determine lambda captures and template parameters, but not generate device code for non-kernel code.

Compilation of a SYCL program may follow either of the following options. The choice of option is made by the implementer:

1. *Separate compilation*: One or more device compilers compile just the SYCL kernels for one or more devices. The device compilers all produce header files for interfacing between the host compiler and the SYCL runtime, which are integrated together with a tool that produces a single header file. The user compiles the source file with a normal C++ host compiler for their platform. The user must ensure that the host compiler is given the correct command-line arguments to ensure that the device compiler output header file is `#include`d from inside the SYCL header files.

2. *Single-source compiler*: In this approach, a single compiler may compile an entire source file for both host and one or more devices. It is the responsibility of the single-source compiler to enable kernels to be compiled correctly for devices and enqueued from the host.

An implementer of SYCL may choose an implementation approach from the options above.

### 3.10.2 Naming of kernels

SYCL kernels are extracted from C++ source files and stored in an implementation-defined format. When the SYCL runtime needs to enqueue a SYCL kernel, it is necessary for the SYCL runtime to load the kernel and pass it to a SYCL backend API. This requires the kernel to have a name that is unique at enclosing namespace scope, to enable an association between the kernel invocation and the kernel itself. The association is achieved using a kernel name, which is a C++ type name.

For a named function object, the kernel name can be the same type as the function object itself, as long as the function object type is unique across the enclosing namespace scopes. For a lambda function the user may optionally provide a name for debugging or other reasons. In SYCL, this optional name is provided as a template parameter to the kernel invocation, e.g. `parallel_for<class kernelName>`, and this name may optionally be forward declared at namespace scope (but must always avoid conflict with another name at enclosing namespace scope).

A device compiler should detect the kernel invocations (e.g. `parallel_for`) in the source code and compile the enclosed kernels, storing them with their associated type name. For details please refer to 5.2.

### 3.11 Language restrictions in kernels

The SYCL kernels are executed on SYCL devices and all of the functions called from a SYCL kernel are going to be compiled for the device by a SYCL device compiler. Due to restrictions of the heterogeneous devices where the SYCL kernel will execute, there are certain restrictions on the base C++ language features that can be used
inside kernel code. For details on language restrictions please refer to 5.4.

SYCL kernels use parameters that are captured by value in the command group scope or are passed from the host to the device using the data management runtime classes of sycl::accessors. Sharing data structures between host and device code imposes certain restrictions, such as use of only user defined classes that are C++ trivially copyable classes for the data structures, and in general, no pointers initialized for the host can be used on the device. The only way of passing pointers to a kernel is through the sycl::accessor class, which supports the sycl::buffer, sycl::unsampled_image and sycl::sampled_image classes. No hierarchical structures of these classes are supported and any other data containers need to be converted to the SYCL data management classes using the SYCL interface. For more details on the rules for kernel parameter passing, please refer to 4.14.4.

3.11.1 SYCL linker

In SYCL only offline linking is supported for SYCL programs and libraries, however the mechanism is optional. In the case of linking C++ functions to a SYCL application, where the definitions are not available in the same translation unit of the compiler, then the macro SYCL_EXTERNAL has to be provided.

3.11.2 Functions and data types available in kernels

Inside kernels, the functions and data types available are restricted by the underlying capabilities of SYCL backend devices.

3.12 Endianness support

SYCL supports both big-endian and little-endian systems as long as it is supported by the used SYCL backends. However SYCL does not support mix-endian systems and does not support specifying the endianness of data within a SYCL kernel function.

Users must be aware of the endianness of the host and the SYCL backend devices they are targeting to ensure kernel arguments are processed correctly when applicable.

3.13 Example SYCL application

Below is a more complex example application, combining some of the features described above.

```c++
#include <SYCL/sycl.hpp>
#include <iostream>

using namespace sycl;

// Size of the matrices
const size_t N = 2000;
const size_t M = 3000;

int main() {
    // Create a queue to work on
    queue myQueue;

    // Create some 2D buffers of float for our matrices
    buffer<float, 2> a { range<2>{N, M }};
}
```
buffer<float, 2> b { range<2>{N, M} };  
buffer<float, 2> c { range<2>{N, M} };  

// Launch an asynchronous kernel to initialize a  
myQueue.submit([&](handler& cgh) {  
    // The kernel writes a, so get a write accessor on it  
    accessor A { a, cgh, write_only };  

    // Enqueue a parallel kernel iterating on a N*M 2D iteration space  
    cgh.parallel_for(range<2> {N, M}, [=](id<2> index) {  
        A[index] = index[0] * 2 + index[1];  
    });  
});  

// Launch an asynchronous kernel to initialize b  
myQueue.submit([&](handler& cgh) {  
    // The kernel writes b, so get a write accessor on it  
    accessor B { b, cgh, write_only };  

    // From the access pattern above, the SYCL runtime detects that this  
    // command_group is independent from the first one and can be  
    // scheduled independently  

    // Enqueue a parallel kernel iterating on a N*M 2D iteration space  
    cgh.parallel_for(range<2> {N, M}, [=](id<2> index) {  
    });  
});  

// Launch an asynchronous kernel to compute matrix addition c = a + b  
myQueue.submit([&](handler& cgh) {  
    // In the kernel a and b are read, but c is written  
    accessor A { a, cgh, read_only };  
    accessor B { b, cgh, read_only };  
    accessor C { c, cgh, write_only };  

    // From these accessors, the SYCL runtime will ensure that when  
    // this kernel is run, the kernels computing a and b have completed  

    // Enqueue a parallel kernel iterating on a N*M 2D iteration space  
    cgh.parallel_for(range<2> {N, M}, [=](id<2> index) {  
    });  
});  

// Ask for an accessor to read c from application scope. The SYCL runtime  
// waits for c to be ready before returning from the constructor  
host_accessor C { c, read_only };  
std::cout << std::endl << "Result:" << std::endl;  
for (size_t i = 0; i < N; i++) {  
    for (size_t j = 0; j < M; j++) {  
        // Compare the result to the analytic value  
        if (C[i][j] != i * (2 + 2014) + j * (1 + 42)) {  
            std::cout << "Wrong value " << C[i][j] << " on element " << i << " "  
                      << j << std::endl;  
        }  
    }  
}
71    exit(-1);
72    }
73 }
74 }
75
76 std::cout << "Good computation!" << std::endl;
77 return 0;
78 }
4. SYCL programming interface

The SYCL programming interface provides a common abstracted feature set to one or more SYCL backend APIs. This section describes the C++ library interface to the SYCL runtime which executes across those SYCL backends.

The entirety of the SYCL interface defined in this section is required to be available for any SYCL backends, with the exception of the interoperability interface, which is described in general terms in this document, not pertaining to any particular SYCL backend.

SYCL guarantees that all the member functions and special member functions of the SYCL classes described are thread safe.

4.1 Backends

The SYCL backends that are available to a SYCL implementation can be identified using the *enum class* `backend`.

```cpp
namespace sycl {
enum class backend {
    <see-below>
};
} // namespace sycl
```

The `enum class` `backend` is implementation defined and must be populated with a unique identifier for each SYCL backend that the SYCL implementation supports, containing at least one SYCL backend that is a SYCL host backend.

Each named SYCL backend enumerated in the `enum class` `backend` must be associated with a SYCL backend specification. Many sections of this specification will refer to the associated SYCL backend specification.

4.1.1 Backend macros

As the identifiers defined in `enum class` `backend` are implementation defined a SYCL implementation must also define a pre-processor macro for each of these identifiers. If the SYCL backend is defined by the Khronos SYCL group, the name of the macro has the form `SYCL_BACKEND_<backend_name>`, where `backend_name` is the associated identifier from backend in all upper-case. See Chapter 6 for the name of the macro if the vendor defines the SYCL backend outside of the Khronos SYCL group.

4.2 Generic vs non-generic SYCL

The SYCL programming API is split into two categories; generic SYCL and non-generic SYCL. Almost everything in the SYCL programming API is considered generic SYCL. However any usage of the `enum class`
backend is considered non-generic SYCL and should only be used for SYCL backend specialized code paths, as the identifiers defined in backend are implementation defined.

In any non-generic SYCL application code where the backend enum class is used, the expression must be guarded with a pre-process #ifdef guard using the associated pre-process macro to ensure that the SYCL application will compile even if the SYCL implementation does not support that SYCL backend being specialized for.

### 4.3 Header files and namespaces

SYCL provides one standard header file: "SYCL/sycl.hpp", which needs to be included in every translation unit which uses the SYCL programming API.

All SYCL classes, constants, types and functions defined by this specification should exist within the ::sycl namespace.

For compatibility with SYCL 1.2.1, SYCL provides another standard header file: "CL/sycl.hpp", which can be included in place of "SYCL/sycl.hpp".

In that case, all SYCL classes, constants, types and functions defined by this specification should exist within the ::cl::sycl C++ namespace.

For consistency, the programming API will only refer to the "sycl.hpp" header and the ::sycl namespace, but this should be considered synonymous with the SYCL 1.2.1 header and namespace.

The sycl::detail namespace is reserved for implementation details.

When a SYCL backend is defined by the Khronos SYCL group, functionality for that SYCL backend is available via the header "SYCL/backend/<backend_name>.hpp", and all SYCL backend-specific functionality is made available in the namespace sycl::<backend_name> where backend_name is the name of the SYCL backend as defined in the SYCL backend specification.

Chapter 6 defines the allowable header files and namespaces for any extensions that a vendor may provide, including any SYCL backend that the vendor may define outside of the Khronos SYCL group.

### 4.4 Class availability

In SYCL some SYCL runtime classes are available to the SYCL application, some are available within a SYCL kernel function and some are available on both and can be passed as parameters to a SYCL kernel function.

Each of the following SYCL runtime classes: buffer, buffer_allocator, context, device, event, exception, handler, id, image_allocator, kernel, marray, module, nd_range, platform, queue, range, sampled_image, sampler, stream, unsampled_image and vec must be available to the host application.

Each of the following SYCL runtime classes: accessor, atomic_ref, device_event, group, h_item, id, item, marray, multi_ptr, nd_item, range, reducer, sampler, stream, sub_group and vec must be available within a SYCL kernel function.

Each of the following SYCL runtime classes: accessor, id, marray, range, reducer, sampler, stream and vec are permitted as parameters to a SYCL kernel function.
4.5 Common interface

When a dimension template parameter is used in SYCL classes, it is defaulted as 1 in most cases.

4.5.1 Param traits class

The class `param_traits` is a C++ type trait for providing an alias to the return type associated with each info parameter. An implementation must provide a specialization of the `param_traits` class for every info parameter with the associated return type as defined in the info parameter tables.

```cpp
namespace sycl {
namespace info {
template <typename T, T param>
class param_traits {
public:

using return_type = __return_type__<T, param>;
};
} // namespace info
} // namespace sycl
```

4.5.2 Backend interoperability

Many of the SYCL runtime classes may be implemented such that they encapsulate an object unique to the SYCL backend that underpins the functionality of that class. Where appropriate, these classes may provide an interface for interoperating between the SYCL runtime object and the native backend object in order to support interoperability within an applications between SYCL and the associated SYCL backend API.

There are two forms of interoperability with SYCL runtime classes; interoperability on the SYCL application with the SYCL backend API and interoperability within a SYCL kernel function with the equivalent kernel language types of the SYCL backend. SYCL application interoperability and SYCL kernel function interoperability are provided via different interfaces and may have different native backend object types.

SYCL application interoperability may be provided for `buffer, context, device, event, kernel, module, platform, queue, sampled_image, image_sampler, stream` and `unsampled_image`.

SYCL kernel function interoperability may be provided for `accessor, stream` and `device_event` inside the SYCL kernel function scope only. SYCL kernel function interoperability is not available inside command group scope.

Support for SYCL backend interoperability is optional and therefore not required to be provided by a SYCL implementation. A SYCL application using SYCL backend interoperability is considered to be non-generic SYCL.

Details on the interoperability for a given SYCL backend are available on the SYCL backend specification document for that SYCL backend.

4.5.2.1 Type traits `backend_traits`

```cpp
namespace sycl {
```

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A series of type traits are provided for SYCL backend interoperability, defined in the `backend_traits` class.

A specialization of `backend_traits` must be provided for each named SYCL backend enumerated in the enum class `backend`.

- For each SYCL runtime class `T` which supports SYCL application interoperability with the SYCL backend, a specialisation of `native_type` must be defined as the type of SYCL application interoperability native backend object associated with `T` for the SYCL backend, specified in the SYCL backend specification.

- For each SYCL runtime class `T` which supports kernel function interoperability with the SYCL backend, a specialisation of `native_type` within `backend_traits` must be defined as the type of the kernel function interoperability native backend object associated with `T` for the SYCL backend, specified in the backend specification.

- A specialization of `errc` must be defined as the SYCL backend error code type.

### 4.5.2.2 Template function `get_native`

```cpp
namespace sycl {
    template<backend Backend, class T>
    backend_traits<Backend>::native_type<T> get_native(const T &syclObject);
} // namespace sycl
```

For each SYCL runtime class `T` which supports SYCL application interoperability, a specialisation of `get_native` must be defined, which takes an instance of `T` and returns a SYCL application interoperability native backend object associated with `syclObject` which can be used for SYCL application interoperability. The lifetime of the object returned are backend-defined and specified in the backend specification.

For each SYCL runtime class `T` which supports kernel function interoperability, a specialisation of `get_native` must be defined, which takes an instance of `T` and returns the kernel function interoperability native backend object associated with `syclObject` which can be used for kernel function interoperability. The lifetime of the object returned are backend-defined and specified in the backend specification.

### 4.5.2.3 Template functions `make_*`

```cpp
namespace sycl {
} // namespace sycl
```

For each SYCL runtime class `T` which supports SYCL application interoperability, a specialisation of `make_*` must be defined, which takes an instance of `T` and returns a SYCL application interoperability native backend object associated with `syclObject` which can be used for SYCL application interoperability. The lifetime of the object returned are backend-defined and specified in the backend specification.

For each SYCL runtime class `T` which supports kernel function interoperability, a specialisation of `make_*` must be defined, which takes an instance of `T` and returns the kernel function interoperability native backend object associated with `syclObject` which can be used for kernel function interoperability. The lifetime of the object returned are backend-defined and specified in the backend specification.
For each SYCL runtime class $T$ which supports SYCL application interoperability, a specialisation of the appropriate template function `make_{sycl_class}` where `{sycl_class}` is the class name of $T$, must be defined, which takes a SYCL application interoperability native backend object and constructs and returns an instance of $T$. The lifetime of the object returned is backend-defined and specified in the backend specification.
4.5.3 Common reference semantics

Each of the following SYCL runtime classes: accessor, buffer, context, device, event, kernel, module, platform, queue, sampled_image, sampler and unsampled_image, must obey the following statements, where T is the runtime class type:

- T must be copy constructible and copy assignable on the host application and within SYCL kernel functions in the case that T is a valid kernel argument. Any instance of T that is constructed as a copy of another instance, via either the copy constructor or copy assignment operator, must behave as-if it were the original instance and as-if any action performed on it were also performed on the original instance and if said instance is not a host object must represent and continue to represent the same underlying native backend object as the original instance where applicable.

- T must be destructible on the host application and within SYCL kernel functions in the case that T is a valid kernel argument. When any instance of T is destroyed, including as a result of the copy assignment operator, any behavior specific to T that is specified as performed on destruction is only performed if this instance is the last remaining host copy, in accordance with the above definition of a copy and the destructor requirements described in 4.5.2 where applicable.

- T must be move constructible and move assignable on the host application and within SYCL kernel functions in the case that T is a valid kernel argument. Any instance of T that is constructed as a move of another instance, via either the move constructor or move assignment operator, must replace the original instance rendering said instance invalid and if said instance is not a host object must represent and continue to represent the same underlying native backend object as the original instance where applicable.

- T must be equality comparable on the host application. Equality between two instances of T (i.e. a == b) must be true if one instance is a copy of the other and non-equality between two instances of T (i.e. a != b) must be true if neither instance is a copy of the other, in accordance with the above definition of a copy, unless either instance has become invalidated by a move operation. By extension of the requirements above, equality on T must guarantee to be reflexive (i.e. a == a), symmetric (i.e. a == b implies b == a and a != b implies b != a) and transitive (i.e. a == b && b == c implies c == a).

- A specialization of std::hash for T must exist on the host application that returns a unique value such that if two instances of T are equal, in accordance with the above definition, then their resulting hash values are also equal and subsequently if two hash values are not equal, then their corresponding instances are also not equal, in accordance with the above definition.

Some SYCL runtime classes will have additional behavior associated with copy, movement, assignment or destruction semantics. If these are specified they are in addition to those specified above unless stated otherwise.

Each of the runtime classes mentioned above must provide a common interface of special member functions in order to fulfill the copy, move, destruction requirements and hidden friend functions in order to fulfill the equality requirements.

A hidden friend function is a function first declared via a friend declaration with no additional out of class or namespace scope declarations. Hidden friend functions are only visible to ADL (Argument Dependent Lookup) and are hidden from qualified and unqualified lookup. Hidden friend functions have the benefits of avoiding accidental implicit conversions and faster compilation.

These common special member functions and hidden friend functions are described in Tables 4.1 and 4.2 respectively.
namespace sycl {
    class T {
        ...
        public:
        T(const T &rhs);
        T(T &&rhs);
        T &operator=(const T &rhs);
        T &operator=(T &&rhs);
        ~T();
        ...
        friend bool operator==(const T &lhs, const T &rhs) { /* ... */ }
        friend bool operator!=(const T &lhs, const T &rhs) { /* ... */ }
    }; // namespace sycl

<table>
<thead>
<tr>
<th>Special member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>T(const T &amp;rhs)</td>
<td>Constructs a T instance as a copy of the RHS SYCL T in accordance with the requirements set out above.</td>
</tr>
<tr>
<td>T(T &amp;&amp;rhs)</td>
<td>Constructs a SYCL T instance as a move of the RHS SYCL T in accordance with the requirements set out above.</td>
</tr>
<tr>
<td>T &amp;operator=(const T &amp;rhs)</td>
<td>Assigns this SYCL T instance with a copy of the RHS SYCL T in accordance with the requirements set out above.</td>
</tr>
<tr>
<td>T &amp;operator=(T &amp;&amp;rhs)</td>
<td>Assigns this SYCL T instance with a move of the RHS SYCL T in accordance with the requirements set out above.</td>
</tr>
<tr>
<td>~T()</td>
<td>Destroys this SYCL T instance in accordance with the requirements set out in 4.5.3. On destruction of the last copy, may perform additional lifetime related operations required for the underlying native backend object specified in the SYCL backend specification, if this SYCL T instance was originally constructed using one of the backend interoperability make_* functions specified in 4.5.2.3.</td>
</tr>
</tbody>
</table>

End of table

Table 4.1: Common special member functions for reference semantics.
### Hidden friend function

<table>
<thead>
<tr>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>bool operator==(const T &amp;lhs, const T &amp;rhs)</td>
</tr>
<tr>
<td>Returns true if this LHS SYCL T is equal to the RHS SYCL T in accordance with the requirements set out above, otherwise returns false.</td>
</tr>
<tr>
<td>bool operator!=(const T &amp;lhs, const T &amp;rhs)</td>
</tr>
<tr>
<td>Returns true if this LHS SYCL T is not equal to the RHS SYCL T in accordance with the requirements set out above, otherwise returns false.</td>
</tr>
</tbody>
</table>

Table 4.2: Common hidden friend functions for reference semantics.

### 4.5.4 Common by-value semantics

Each of the following SYCL runtime classes: `id, range, item, nd_item, h_item, group, sub_group` and `nd_range` must follow the following statements, where `T` is the runtime class type:

- `T` must be default copy constructible and copy assignable on the host application (in the case where `T` is available on the host) and within SYCL kernel functions.
- `T` must be default destructible on the host application (in the case where `T` is available on the host) and within SYCL kernel functions.
- `T` must be default move constructible and default move assignable on the host application (in the case where `T` is available on the host) and within SYCL kernel functions.
- `T` must be equality comparable on the host application (in the case where `T` is available on the host) and within SYCL kernel functions. Equality between two instances of `T` (i.e. `a == b`) must be true if the value of all members are equal and non-equality between two instances of `T` (i.e. `a != b`) must be true if the value of any members are not equal, unless either instance has become invalidated by a move operation. By extension of the requirements above, equality on `T` must guarantee to be reflexive (i.e. `a == a`), symmetric (i.e. `a == b` implies `b == a` and `a != b` implies `b != a`) and transitive (i.e. `a == b && b == c` implies `c == a`).

Some SYCL runtime classes will have additional behavior associated with copy, movement, assignment or destruction semantics. If these are specified they are in addition to those specified above unless stated otherwise.

Each of the runtime classes mentioned above must provide a common interface of special member functions and member functions in order to fulfill the copy, move, destruction and equality requirements, following the rule of five and the rule of zero.

These common special member functions and hidden friend functions are described in Tables 4.3 and 4.4 respectively.

```cpp
namespace sycl {
  class T {
    ...
  public:
```
If any of the following five special member functions are not public, inline or defaulted, then all five of them should be explicitly declared (see rule of five). Otherwise, none of them should be explicitly declared (see rule of zero).

```cpp
T(const T &rhs);
T(T &&rhs);
T &operator=(const T &rhs);
T &operator=(T &&rhs);
~T();
```

```cpp
friend bool operator==(const T &lhs, const T &rhs) { /* ... */ }

friend bool operator!=(const T &lhs, const T &rhs) { /* ... */ }
...}
```
4.5.5 Properties

Each of the following SYCL runtime classes: accessor, context, queue, buffer, unsampled_image and sampled_image provide an optional parameter in each of their constructors to provide a property_list which contains zero or more properties. Each of those properties augments the semantics of the class with a particular feature. Each of those classes must also provide has_property and get_property member functions for querying for a particular property.

The listing below illustrates the usage of various buffer properties, described in 4.7.2.2.

The example illustrates how using properties does not affect the type of the object, thus, does not prevent the usage of SYCL objects in containers.

```cpp
    {
        context myContext;

        std::vector<buffer<int, 1>> bufferList {
            buffer<int, 1>{ptr, rng},
            buffer<int, 1>{ptr, rng, property::use_host_ptr{}},
            buffer<int, 1>{ptr, rng, property::context_bound{myContext}}
        };

        for(auto& buf : bufferList) {
            if (buf.has_property<property::context_bound>()) {
                auto prop = buf.get_property<property::context_bound>();
                assert(myContext == prop.get_context());
            }
        }
    }
```

Each property is represented by a unique class and an instance of a property is an instance of that type. Some properties can be default constructed while others will require an argument on construction. A property may be applicable to more than one class, however some properties may not be compatible with each other. See the requirements for the properties of the SYCL buffer class, SYCL unsampled_image class and SYCL sampled_image class in Table 4.33 and Table 4.40 respectively.

Any property that is provided to a SYCL runtime class via an instance of the SYCL property_list class must become encapsulated by that class and therefore shared between copies of that class. As a result properties must inherit the copy and move semantics of that class as described in 4.5.3.

A SYCL implementation or a SYCL backend may provide additional properties other than those defined here, provided they are defined in accordance with the requirements described in 4.3.

### 4.5.5.1 Properties interface

Each of the runtime classes mentioned above must provide a common interface of member functions in order to fulfill the property interface requirements.

A synopsis of the common properties interface, the SYCL property_list class and the SYCL property classes is provided below. The member functions of the common properties interface are listed in Table 4.6. The constructors of the SYCL property_list class are listed in Table 4.7.
namespace sycl {

  template <typename propertyT>
  struct is_property;

  template <typename propertyT, typename syclObjectT>
  struct is_property_of;

  class T {
    ...
    template <typename propertyT>
    bool has_property() const;
    template <typename propertyT>
    propertyT get_property() const;
    ...
  };

  class property_list {
    public:
      template <typename... propertyTN>
      property_list(propertyTN... props);
  } // namespace sycl

<table>
<thead>
<tr>
<th>Traits</th>
<th>Description</th>
</tr>
</thead>
</table>
| template <typename propertyT>
  struct is_property | An explicit specialization of is_property that inherits from std::true_type must be provided for each property, where propertyT is the class defining the property. This includes both standard properties described in this specification and any additional non-standard properties defined by an implementation. All other specializations of is_property must inherit from std::false_type. |

| template <typename propertyT, syclObjectT>
  struct is_property_of | An explicit specialization of is_property_of that inherits from std::true_type must be provided for each property that can be used in constructing a given SYCL class, where propertyT is the class defining the property and syclObjectT is the SYCL class. This includes both standard properties described in this specification and any additional non-standard properties defined by an implementation. All other specializations of is_property_of must inherit from std::false_type. |

End of table

Table 4.5: Traits for properties.
4.6. SYCL RUNTIME CLASSES

### 4.6.1 Device selection

Since a system can have several SYCL-compatible devices attached, it is useful to have a way to select a specific device or a set of devices to construct a specific object such as a device (see Section 4.6.4) or a queue (see Section 4.6.5), or perform some operations on a device subset.

Device selection is done either by having already a specific instance of a device (see Section 4.6.4) or by providing a device selector which is a ranking function that will give an integer ranking value to all the devices on the system.

#### 4.6.1.1 Device selector

The actual interface for a device selector is a callable taking a const device reference and returning a value implicitly convertible to an int.

At any point where the SYCL runtime needs to select a SYCL device using a device selector, the system will query all available SYCL devices from all SYCL backends in the system including the SYCL host backend, will call the device selector on each device and select the one which returns the highest score. If the highest value is negative no device is selected.

In places where only one device has to be picked and the high score is obtained by more than one device, then one of the tied devices will be returned, but which one is not defined and may depend on enumeration order, for example, outside the control of the SYCL runtime.

---

### Table 4.6: Common member functions of the SYCL property interface.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>template &lt;typename propertyT&gt; bool has_property() const</td>
<td>Returns true if T was constructed with the property specified by propertyT. Returns false if it was not.</td>
</tr>
<tr>
<td>template &lt;typename propertyT&gt; propertyT get_property() const</td>
<td>Returns a copy of the property of type propertyT that T was constructed with. Must throw an exception with the errc::invalid_object_error error code if T was not constructed with the propertyT property.</td>
</tr>
</tbody>
</table>

---

### Table 4.7: Constructors of the SYCL property_list class.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>template &lt;typename... propertyTN&gt; property_list(propertyTN... props)</td>
<td>Available only when: is_property&lt;property&gt;::value evaluates to true where property is each property in propertyTN. Construct a SYCL property_list with zero or more properties.</td>
</tr>
</tbody>
</table>

---

4.6 SYCL runtime classes
Some predefined device selectors are provided by the system as described on Table 4.8 in a header file with some definition similar to the following:

<table>
<thead>
<tr>
<th>SYCL device selectors</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>default_selector_v</td>
<td>Select a SYCL device from any supported SYCL backend based on an implementation-defined heuristic. Must select the host device if no other suitable device can be found.</td>
</tr>
<tr>
<td>gpu_selector_v</td>
<td>Select a SYCL device from any supported SYCL backend for which the device type is info::device::device_type::gpu. The SYCL class constructor using it must throw an exception with the errc::runtime_error error code if no device matching this requirement can be found.</td>
</tr>
<tr>
<td>accelerator_selector_v</td>
<td>Select a SYCL device from any supported SYCL backend for which the device type is info::device::device_type::accelerator. The SYCL class constructor using it must throw an exception with the errc::runtime_error error code if no device matching this requirement can be found.</td>
</tr>
<tr>
<td>cpu_selector_v</td>
<td>Select a SYCL device from any supported SYCL backend for which the device type is info::device::device_type::cpu. The SYCL class constructor using it must throw an exception with the errc::runtime_error error code if no device matching this requirement can be found.</td>
</tr>
<tr>
<td>host_selector_v</td>
<td>Select the SYCL host device from the SYCL host backend. This must always return a valid SYCL device.</td>
</tr>
</tbody>
</table>

Table 4.8: Standard device selectors included with all SYCL implementations.

```cpp
namespace sycl {

namespace sycl {

// Predefined device selectors
__unspecified__ default_selector_v;
__unspecified__ host_selector_v;
__unspecified__ cpu_selector_v;
__unspecified__ gpu_selector_v;
__unspecified__ accelerator_selector_v;

// Predefined types for compatibility with old SYCL 1.2.1 device selectors
using default_selector = __unspecified__;
using host_selector = __unspecified__;

```
4.6. SYCL RUNTIME CLASSES

Typical examples of default and user-provided device selectors could be:

```cpp
sycl::device my_gpu { sycl::gpu_selector_v };
sycl::queue my_accelerator { sycl::accelerator_selector_v };
...
int prefer_my_vendor(const sycl::device & d) {
    // Return 1 if the vendor name is "MyVendor" or 0 else.
    // 0 does not prevent another device to be picked as a second choice
    return d.get_info<info::device::vendor>() == "MyVendor";
}
// Get the preferred device or another one if not available
sycl::device preferred_device { prefer_my_vendor };
// This throws if there is no such device in the system
sycl::queue half_precision_controller {
    // Can use a lambda as a device ranking function.
    // Returns a negative number to fail in the case there is no such device
    [] (auto &d) { return d.has(aspect::fp16) ? 1 : -1; }
};
// To ease porting SYCL 1.2.1 code, there are types whose
// construction leads to the equivalent predefined device selector
sycl::queue my_old_style_gpu { sycl::gpu_selector {} };
```

[Note: in SYCL 1.2.1 the predefined device selectors were actually types that had to be instantiated to be used. Now they are just instances. To simplify porting code using the old type instantiations, a backward-compatible API is still provided, such as `sycl::default_selector`. The new predefined device selectors have their new names appended with `_v` to avoid conflicts, thus following the naming style used by traits in the C++ standard library. There is no requirement for the implementation to have for example `sycl::gpu_selector_v` being an instance of `sycl::gpu_selector`. — end note]

Implementation note: the SYCL API might rely on SFINAE or C++20 concepts to resolve some ambiguity in constructors with default parameters.

4.6.2 Platform class

The SYCL platform class encapsulates a single SYCL platform on which SYCL kernel functions may be executed. A SYCL platform must be associated with a single SYCL backend and may encapsulate a native backend object.

A SYCL platform is also associated with one or more SYCL devices associated with the same SYCL backend.

All member functions of the platform class are synchronous and errors are handled by throwing synchronous SYCL exceptions.
The default constructor of the SYCL `platform` class will construct a platform associated with the SYCL host backend.

The explicit constructor of the SYCL `platform` class which takes a device selector will construct a platform that is associated with the SYCL backend that is associated with the device that the device selector would construct, according to Sections 4.6.1.1 and 4.6.4.

The SYCL `platform` class provides the common reference semantics (see Section 4.5.3).

### 4.6.2.1 Platform interface

A synopsis of the SYCL `platform` class is provided below. The constructors, member functions and static member functions of the SYCL `platform` class are listed in Tables 4.9, 4.10 and 4.11 respectively. The additional common special member functions and common member functions are listed in 4.5.3 in Tables 4.1 and 4.2 respectively.

```cpp
namespace sycl {
    class platform {
        public:
            platform();
            template <typename DeviceSelector>
            explicit platform(const DeviceSelector &deviceSelector);
            /* -- common interface members -- */
            backend get_backend() const;
            std::vector<device> get_devices(
                info::device_type = info::device_type::all) const;
            template <info::platform param>
            typename info::param_traits<info::platform, param>::return_type get_info() const;
            template <typename BackendEnum, BackendEnum param>
            typename info::param_traits<BackendEnum, param>::return_type
            get_backend_info() const;
            bool has(aspect asp) const;
            bool has_extension(const std::string &extension) const; // Deprecated
            bool is_host() const;
            static std::vector<platform> getPlatforms();
    } // namespace sycl
```

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>platform()</td>
<td>Constructs a SYCL <code>platform</code> instance as a host platform.</td>
</tr>
</tbody>
</table>

Table 4.9: Constructors of the SYCL `platform` class.
### 4.6. SYCL RUNTIME CLASSES

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename DeviceSelector&gt; explicit platform(const DeviceSelector &amp;)</code></td>
<td>Constructs a SYCL platform instance using the device selector parameter. One of the SYCL devices that is associated with the constructed SYCL platform instance must be the SYCL device that is produced from the provided device ranking function.</td>
</tr>
</tbody>
</table>

End of table

Table 4.9: Constructors of the SYCL platform class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>backend get_backend() const</code></td>
<td>Returns a backend identifying the SYCL backend associated with this platform.</td>
</tr>
<tr>
<td><code>template &lt;info::platform param&gt; typename info::param_traits&lt;info::platform, param&gt;::return_type get_info() const</code></td>
<td>Queries this SYCL platform for information requested by the template parameter param. Specializations of info::param_traits must be defined in accordance with the info parameters in Table 4.19 to facilitate returning the type associated with the param parameter.</td>
</tr>
<tr>
<td><code>template &lt;typename BackendEnum, BackendEnum param&gt; typename info::param_traits&lt;BackendEnum, param&gt;::return_type get_backend_info() const</code></td>
<td>Queries this SYCL platform for SYCL backend-specific information requested by the template parameter param. BackendEnum can be any enum class type specified by the SYCL backend specification of a supported SYCL backend named according to the convention info::&lt;backend_name&gt;::platform and param must be a valid enumeration of that enum class. Specializations of info::param_traits must be defined for BackendEnum in accordance with the SYCL backend specification. Must throw an exception with the errc::invalid_object_error if the SYCL backend that corresponds with BackendEnum is different from the SYCL backend that is associated with this platform.</td>
</tr>
<tr>
<td><code>bool has(aspect asp) const</code></td>
<td>Returns true if all of the SYCL devices associated with this SYCL platform have the given aspect.</td>
</tr>
<tr>
<td><code>bool has_extension(const std::string &amp; extension) const</code></td>
<td>Deprecated, use has() instead. Returns true if this SYCL platform supports the extension queried by the extension parameter. A SYCL platform can only support an extension if all associated SYCL devices support that extension.</td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.10: Member functions of the SYCL platform class.
### 4.6. SYCL RUNTIME CLASSES

#### 4.6.2.2 Platform information descriptors

A `platform` can be queried for information using the `get_info` member function of the `platform` class, specifying one of the info parameters enumerated in `info::platform`. Every `platform` (including a host `platform`) must produce a valid value for each info parameter. The possible values for each info parameter and any restrictions are defined in the specification of the SYCL backend associated with the `platform`. All info parameters in `info::platform` are specified in Table 4.12 and the synopsis for `info::platform` is described in appendix A.1.

<table>
<thead>
<tr>
<th>Platform descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>info::platform::version</code></td>
<td><code>std::string</code></td>
<td>Returns the software driver version of the <code>device</code>.</td>
</tr>
<tr>
<td><code>info::platform::name</code></td>
<td><code>std::string</code></td>
<td>Returns the name of the <code>platform</code>.</td>
</tr>
<tr>
<td><code>info::platform::vendor</code></td>
<td><code>std::string</code></td>
<td>Returns the name of the vendor providing the <code>platform</code>.</td>
</tr>
</tbody>
</table>
| `info::platform::extensions` | `std::vector<
  std::string>` | Deprecated, use `device::get_info()` with `info::device::aspects` instead. Returns the extensions supported by the `platform`. |

Table 4.12: Platform information descriptors.
4.6.3 Context class

The context class represents a SYCL context. A context represents the runtime data structures and state required by a SYCL backend API to interact with a group of devices associated with a platform.

The SYCL context class provides the common reference semantics (see Section 4.5.3).

4.6.3.1 Context interface

The constructors and member functions of the SYCL context class are listed in Tables 4.13 and 4.14, respectively. The additional common special member functions and common member functions are listed in 4.5.3 in Tables 4.1 and 4.2, respectively.

All member functions of the context class are synchronous and errors are handled by throwing synchronous SYCL exceptions.

All constructors of the SYCL context class will construct an instance associated with a particular SYCL backend, determined by the constructor parameters or, in the case of the default constructor, the SYCL device produced by the default_selector_v.

A SYCL context can optionally be constructed with an async_handler parameter. In this case the async_handler is used to report asynchronous SYCL exceptions, as described in 4.15.

Information about a SYCL context may be queried through the get_info() member function.

```cpp
namespace sycl {
    class context {
        public:
            explicit context(const property_list &propList = {});
            explicit context(async_handler asyncHandler,
                             const property_list &propList = {});
            explicit context(const device &dev, const property_list &propList = {});
            explicit context(const device &dev, async_handler asyncHandler,
                             const property_list &propList = {});
            explicit context(const std::vector<device> &deviceList,
                             const property_list &propList = {});
            explicit context(const std::vector<device> &deviceList,
                             async_handler asyncHandler,
                             const property_list &propList = {});
            /* -- property interface members -- */
            /* -- common interface members -- */
            backend get_backend() const;
            bool is_host() const;
            platform get_platform() const;
    }
}
```
```cpp
std::vector<device> get_devices() const;

template <info::context param>
typename info::param_traits<info::context, param>::return_type get_info() const;

template <typename BackendEnum, BackendEnum param>
typename info::param_traits<BackendEnum, param>::return_type
get_backend_info() const;
};
```
### Member function

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;info::context param&gt; typename info::param_traits&lt;info::context, param&gt;::return_type get_info() const</code></td>
<td>Queries this SYCL context for information requested by the template parameter param using the param_traits class template to facilitate returning the appropriate type associated with the param parameter.</td>
</tr>
<tr>
<td><code>template &lt;typename BackendEnum, BackendEnum param&gt; typename info::param_traits&lt;BackendEnum, param&gt;::return_type get_backend_info() const</code></td>
<td>Queries this SYCL context for SYCL backend-specific information requested by the template parameter param. BackendEnum can be any enum class type specified by the SYCL backend specification of a supported SYCL backend named according to the convention info::&lt;backend_name&gt;::context and param must be a valid enumeration of that enum class. Specializations of info::param_traits must be defined for BackendEnum in accordance with the SYCL backend specification. Must throw an exception with the errc::invalid_object_error error code if the SYCL backend that corresponds with BackendEnum is different from the SYCL backend that is associated with this context.</td>
</tr>
<tr>
<td><code>platform get_platform() const</code></td>
<td>Returns the SYCL platform that is associated with this SYCL context. The value returned must be equal to that returned by get_info<a href="">info::context::platform</a>().</td>
</tr>
<tr>
<td><code>std::vector&lt;device&gt; get_devices() const</code></td>
<td>Returns a std::vector containing all SYCL devices that are associated with this SYCL context. The value returned must be equal to that returned by get_info<a href="">info::context::devices</a>().</td>
</tr>
</tbody>
</table>

End of table

### 4.6.3.2 Context information descriptors

A context can be queried for information using the get_info member function of the context class, specifying one of the info parameters enumerated in info::context. Every context (including a host context) must produce a valid value for each info parameter. The possible values for each info parameter and any restrictions are defined in the specification of the SYCL backend associated with the context. All info parameters in info::context are specified in Table 4.15 and the synopsis for info::context is described in appendix A.2.

<table>
<thead>
<tr>
<th>Context Descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::context::platform</td>
<td>platform</td>
<td>Returns the platform associated with the context.</td>
</tr>
<tr>
<td>info::context::devices</td>
<td>std::vector&lt;device&gt;</td>
<td>Returns all of the devices associated with the context.</td>
</tr>
</tbody>
</table>

Continued on next page
### Context Descriptors

<table>
<thead>
<tr>
<th>Context Descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::context::atomic_memory_order_capabilities</td>
<td>std::vector&lt;memory_order&gt;</td>
<td>Returns the set of memory orderings supported by atomic operations on all devices in the context, which is guaranteed to include relaxed. The memory ordering of the context determines the behavior of atomic operations applied to any memory that can be concurrently accessed by multiple devices in the context.</td>
</tr>
<tr>
<td>info::context::atomic_fence_order_capabilities</td>
<td>std::vector&lt;memory_order&gt;</td>
<td>Returns the set of memory orderings supported by atomic_fence on all devices in the context, which is guaranteed to include relaxed. The memory ordering of the context determines the behavior of fence operations applied to any memory that can be concurrently accessed by multiple devices in the context.</td>
</tr>
<tr>
<td>info::context::atomic_memory_scope_capabilities</td>
<td>std::vector&lt;memory_scope&gt;</td>
<td>Returns the set of memory scopes supported by atomic operations on all devices in the context, which is guaranteed to include work_group.</td>
</tr>
<tr>
<td>info::context::atomic_fence_scope_capabilities</td>
<td>std::vector&lt;memory_scope&gt;</td>
<td>Returns the set of memory orderings supported by atomic_fence on all devices in the context, which is guaranteed to include work_group.</td>
</tr>
</tbody>
</table>

Table 4.15: Context information descriptors.

#### 4.6.3.3 Context properties

The `property_list` constructor parameters are present for extensibility.

#### 4.6.4 Device class

The SYCL `device` class encapsulates a single SYCL device on which kernels can be executed. A SYCL device object can map to a native backend object.

All member functions of the `device` class are synchronous and errors are handled by throwing synchronous SYCL exceptions.

The default constructor of the SYCL `device` class will construct a host device from the host SYCL backend.

The explicit constructor of the SYCL `device` class which takes a `device selector` will construct a device selected by the `device selector` according to Section 4.6.1.1.

A SYCL `device` can be partitioned into multiple SYCL devices, by calling the `create_sub_devices()` member function template. The resulting SYCL `devices` are considered sub devices, and it is valid to partition these sub
4.6 SYCL RUNTIME CLASSES

The range of support for this feature is SYCL backend and device specific and can be queried for through `get_info()`.

The SYCL `device` class provides the common reference semantics (see Section 4.5.3).

### 4.6.4.1 Device interface

A synopsis of the SYCL `device` class is provided below. The constructors, member functions and static member functions of the SYCL `device` class are listed in Tables 4.16, 4.17 and 4.18 respectively. The additional common special member functions and common member functions are listed in 4.5.3 in Tables 4.1 and 4.2, respectively.

```cpp
namespace sycl {
    class device {
        public:
            device();

            template <typename DeviceSelector>
            explicit device(const DeviceSelector &deviceSelector);

            /* -- common interface members -- */

            backend get_backend() const;

            bool is_host() const;

            bool is_cpu() const;

            bool is_gpu() const;

            bool is_accelerator() const;

            platform get_platform() const;

            template <info::device param>
            typename info::param_traits<info::device, param>::return_type
            get_info() const;

            template <typename BackendEnum, BackendEnum param>
            typename info::param_traits<BackendEnum, param>::return_type
            get_backend_info() const;

            bool has(aspect asp) const;

            bool has_extension(const std::string &extension) const; // Deprecated

            // Available only when prop == info::partition_property::partition_equally
            template <info::partition_property prop>
            std::vector<device> create_sub_devices(size_t nbSubDev) const;

            // Available only when prop == info::partition_property::partition_by_counts
            template <info::partition_property prop>
            std::vector<device> create_sub_devices(const std::vector<size_t> &counts) const;

            // Available only when prop == info::partition_property::partition_by_affinity_domain
```
template <info::partition_property prop>
std::vector<device> create_sub_devices(info::affinity_domain affinityDomain) const;

static std::vector<device> get_devices(
    info::device_type deviceType = info::device_type::all);

} // namespace sycl

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>device()</td>
<td>Constructs a SYCL device instance as a host device.</td>
</tr>
</tbody>
</table>
| template <typename DeviceSelector>
    explicit device(const DeviceSelector & deviceSelector) | Constructs a SYCL device instance using the device selected by the device selector provided. |

Table 4.16: Constructors of the SYCL device class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>backend get_backend() const</td>
<td>Returns the a backend identifying the SYCL backend associated with this device.</td>
</tr>
<tr>
<td>platform get_platform() const</td>
<td>Returns the associated SYCL platform. The value returned must be equal to that returned by get_info<a href="">info::device::platform</a>()</td>
</tr>
<tr>
<td>bool is_host() const</td>
<td>Returns the same value as has(aspect::host). See Table 4.20.</td>
</tr>
<tr>
<td>bool is_cpu() const</td>
<td>Returns the same value as has(aspect::cpu). See Table 4.20.</td>
</tr>
<tr>
<td>bool is_gpu() const</td>
<td>Returns the same value as has(aspect::gpu). See Table 4.20.</td>
</tr>
<tr>
<td>bool is_accelerator() const</td>
<td>Returns the same value as has(aspect::accelerator). See Table 4.20.</td>
</tr>
<tr>
<td>template &lt;info::device param&gt; typename info::param_traits&lt;info::device, param&gt;::return_type get_info() const</td>
<td>Queries this SYCL device for information requested by the template parameter param. Specializations of info::param_traits must be defined in accordance with the info parameters in Table 4.19 to facilitate returning the type associated with the param parameter.</td>
</tr>
</tbody>
</table>

Table 4.17: Member functions of the SYCL device class.
### Member function | Description
--- | ---
```
template <typename BackendEnum, BackendEnum param>  
typename info::param_traits<BackendEnum, param>::return_type  
get_backend_info() const
```
Queries this SYCL device for SYCL backend-specific information requested by the template parameter `param`. `BackendEnum` can be any enum class type specified by the SYCL backend specification of a supported SYCL backend named according to the convention `info::<backend_name>::device` and `param` must be a valid enumeration of that enum class. Specializations of `info::param_traits` must be defined for `BackendEnum` in accordance with the SYCL backend specification. Must throw an exception with the `errc::invalid_object_error` error code if the SYCL backend that corresponds with `BackendEnum` is different from the SYCL backend that is associated with this device.

```
bool has(aspect asp) const
```
Returns true if this SYCL device has the given `aspect`. SYCL applications can use this member function to determine which optional features this device supports (if any).

```
bool has_extension (const std::string &extension) const
```
Deprecated, use `has()` instead. Returns true if this SYCL device supports the extension queried by the extension parameter.

```
template <info::partition_property prop>  
std::vector<device> create_sub_devices(  
    size_t nbSubDev) const
```
Available only when `prop` is `info::partition_property::partition_equally`. Returns a `std::vector` of sub devices partitioned from this SYCL device equally based on the `nbSubDev` parameter. If this SYCL device does not support `info::partition_property::partition_equally` an exception with the `errc::feature_not_supported` error code must be thrown.

```
template <info::partition_property prop>  
std::vector<device> create_sub_devices(  
    const std::vector<size_t> &counts) const
```
Available only when `prop` is `info::partition_property::partition_by_count`. Returns a `std::vector` of sub devices partitioned from this SYCL device by count sizes based on the `counts` parameter. If the SYCL device does not support `info::partition_property::partition_by_count` an exception with the `errc::feature_not_supported` error code must be thrown.

Table 4.17: Member functions of the SYCL `device` class.
Member function | Description
--- | ---
`template <info::partition_property prop> std::vector<device> create_sub_devices(info::affinity_domain affinityDomain) const` | Available only when prop is `info::partition_property::partition_by_affinity_domain`. Returns a `std::vector` of sub devices partitioned from this SYCL `device` by affinity domain based on the `affinityDomain` parameter. Partitions the device into sub devices based upon the affinity domain. If the SYCL `device` does not support `info::partition_property::partition_by_affinity_domain` or the SYCL `device` does not support the `info::affinity_domain` provided, an exception with the `errc::feature_not_supported` error code must be thrown.

| Static member function | Description
--- | ---
`static std::vector<device> get_devices(info::device_type deviceType = info::device_type::all)` | Returns a `std::vector` containing all SYCL `devices` from all SYCL backends available in the system of the device type specified by the parameter `deviceType`. Note that when the `device_type` is `info::device_type::all` or `info::device_type::host`, the `std::vector` returned must contain at least one host device from the SYCL host backend.

4.6.4.2 Device information descriptors

A `device` can be queried for information using the `get_info` member function of the `device` class, specifying one of the info parameters enumerated in `info::device`. Every `device` (including a host `device`) must produce a valid value for each info parameter. The possible values for each info parameter and any restriction are defined in the specification of the SYCL backend associated with the `device`. All info parameters in `info::device` are specified in Table 4.19 and the synopsis for `info::device` is described in appendix A.3.

| Device descriptors | Return type | Description
--- | --- | ---
`info::device::device_type` | `info::device_type` | Returns the device type associated with the `device`. May not return `info::device_type::all`.
`info::device::vendor_id` | `uint32_t` | Returns a unique vendor device identifier.

Table 4.17: Member functions of the SYCL `device` class.

Table 4.18: Static member functions of the SYCL `device` class.

Table 4.19: Device information descriptors.
<table>
<thead>
<tr>
<th>Device descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::device::max_compute_units</td>
<td>uint32_t</td>
<td>Returns the number of parallel compute units available to the device. The minimum value is 1.</td>
</tr>
<tr>
<td>info::device::max_work_item_dimensions</td>
<td>uint32_t</td>
<td>Returns the maximum dimensions that specify the global and local work-item IDs used by the data parallel execution model. The minimum value is 3 if this SYCL device is not of device type info::device_type::custom.</td>
</tr>
<tr>
<td>info::device::max_work_item_sizes</td>
<td>id&lt;3&gt;</td>
<td>Returns the maximum number of work-items that are permitted in each dimension of the work-group of the nd_range. The minimum value is (1,1,1) for devices that are not of device type info::device_type::custom.</td>
</tr>
<tr>
<td>info::device::max_work_group_size</td>
<td>size_t</td>
<td>Returns the maximum number of work-items that are permitted in a work-group executing a kernel on a single compute unit. The minimum value is 1.</td>
</tr>
<tr>
<td>info::device::max_num_sub_groups</td>
<td>uint32_t</td>
<td>Returns the maximum number of sub-groups in a work-group for any kernel executed on the device. The minimum value is 1.</td>
</tr>
<tr>
<td>info::device::sub_group_independent_forward_progress</td>
<td>bool</td>
<td>Returns true if the device supports independent forward progress of sub-groups with respect to other sub-groups in the same work-group.</td>
</tr>
<tr>
<td>info::device::sub_group_sizes</td>
<td>std::vector&lt; size_t&gt;</td>
<td>Returns a std::vector of size_t containing the set of sub-group sizes supported by the device.</td>
</tr>
<tr>
<td>info::device::preferred_vector_width_char</td>
<td>uint32_t</td>
<td>Returns the preferred native vector width size for built-in scalar types that can be put into vectors. The vector width is defined as the number of scalar elements that can be stored in the vector. Must return 0 for info::device::preferred_vector_width_double if the device does not have aspect::fp64 and must return 0 for info::device::preferred_vector_width_half if the device does not have aspect::fp16.</td>
</tr>
</tbody>
</table>

Table 4.19: Device information descriptors.
### Device descriptors

<table>
<thead>
<tr>
<th>Device descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::device::native_vector_width_char</td>
<td>uint32_t</td>
<td>Returns the native ISA vector width. The vector width is defined as the number of scalar elements that can be stored in the vector. Must return 0 for info::device::preferred_vector_width_double if the device does not have aspect::fp64 and must return 0 for info::device::preferred_vector_width_half if the device does not have aspect::fp16.</td>
</tr>
<tr>
<td>info::device::native_vector_width_short</td>
<td>uint32_t</td>
<td></td>
</tr>
<tr>
<td>info::device::native_vector_width_int</td>
<td>uint32_t</td>
<td></td>
</tr>
<tr>
<td>info::device::native_vector_width_long</td>
<td>uint32_t</td>
<td></td>
</tr>
<tr>
<td>info::device::native_vector_width_float</td>
<td>uint32_t</td>
<td></td>
</tr>
<tr>
<td>info::device::native_vector_width_double</td>
<td>uint32_t</td>
<td></td>
</tr>
<tr>
<td>info::device::native_vector_width_half</td>
<td>uint32_t</td>
<td></td>
</tr>
<tr>
<td>info::device::max_clock_frequency</td>
<td>uint32_t</td>
<td>Returns the maximum configured clock frequency of this SYCL device in MHz.</td>
</tr>
<tr>
<td>info::device::address_bits</td>
<td>uint32_t</td>
<td>Returns the default compute device address space size specified as an unsigned integer value in bits. Must return either 32 or 64.</td>
</tr>
<tr>
<td>info::device::max_mem_alloc_size</td>
<td>uint64_t</td>
<td>Returns the maximum size of memory object allocation in bytes. The minimum value is max (1/4th of info::device::global_mem_size, 128<em>1024</em>1024) if this SYCL device is not of device type info::device_type::custom.</td>
</tr>
<tr>
<td>info::device::image_support</td>
<td>bool</td>
<td>Deprecated. Returns the same value as device::has(aspect::image).</td>
</tr>
<tr>
<td>info::device::max_read_image_args</td>
<td>uint32_t</td>
<td>Returns the maximum number of simultaneous image objects that can be read from by a kernel. The minimum value is 128 if the SYCL device has aspect::image.</td>
</tr>
<tr>
<td>info::device::max_write_image_args</td>
<td>uint32_t</td>
<td>Returns the maximum number of simultaneous image objects that can be written to by a kernel. The minimum value is 8 if the SYCL device has aspect::image.</td>
</tr>
<tr>
<td>info::device::image2d_max_width</td>
<td>size_t</td>
<td>Returns the maximum width of a 2D image or 1D image in pixels. The minimum value is 8192 if the SYCL device has aspect::image.</td>
</tr>
<tr>
<td>info::device::image2d_max_height</td>
<td>size_t</td>
<td>Returns the maximum height of a 2D image in pixels. The minimum value is 8192 if the SYCL device has aspect::image.</td>
</tr>
<tr>
<td>info::device::image3d_max_width</td>
<td>size_t</td>
<td>Returns the maximum width of a 3D image in pixels. The minimum value is 2048 if the SYCL device has aspect::image.</td>
</tr>
<tr>
<td>info::device::image3d_max_height</td>
<td>size_t</td>
<td>Returns the maximum height of a 3D image in pixels. The minimum value is 2048 if the SYCL device has aspect::image.</td>
</tr>
</tbody>
</table>

Table 4.19: Device information descriptors.
### Device descriptors

<table>
<thead>
<tr>
<th>Device descriptor</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::device::image3d_max_depth</td>
<td>size_t</td>
<td>Returns the maximum depth of a 3D image in pixels. The minimum value is 2048 if the SYCL device has aspect::image.</td>
</tr>
<tr>
<td>info::device::image_max_buffer_size</td>
<td>size_t</td>
<td>Returns the number of pixels for a 1D image created from a buffer object. The minimum value is 65536 if the SYCL device has aspect::image. Note that this information is intended for OpenCL interoperability only as this feature is not supported in SYCL.</td>
</tr>
<tr>
<td>info::device::image_max_array_size</td>
<td>size_t</td>
<td>Returns the maximum number of images in a 1D or 2D image array. The minimum value is 2048 if the SYCL device has aspect::image.</td>
</tr>
<tr>
<td>info::device::max_samplers</td>
<td>uint32_t</td>
<td>Returns the maximum number of samplers that can be used in a kernel. The minimum value is 16 if the SYCL device has aspect::image.</td>
</tr>
<tr>
<td>info::device::max_parameter_size</td>
<td>size_t</td>
<td>Returns the maximum size in bytes of the arguments that can be passed to a kernel. The minimum value is 1024 if this SYCL device is not of device type info::device_type::custom. For this minimum value, only a maximum of 128 arguments can be passed to a kernel.</td>
</tr>
<tr>
<td>info::device::mem_base_addr_align</td>
<td>uint32_t</td>
<td>Returns the minimum value in bits of the largest supported SYCL built-in data type if this SYCL device is not of device type info::device_type::custom.</td>
</tr>
</tbody>
</table>

Table 4.19: Device information descriptors.
### Device descriptors

<table>
<thead>
<tr>
<th>Device descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
</table>
| `info::device::half_fp_config` | `std::vector<
  info::fp_config>` | Returns a `std::vector` of `info::fp_config` describing the half precision floating-point capability of this SYCL device. The `std::vector` may contain zero or more of the following values:

- `info::fp_config::denorm`: denoms are supported.
- `info::fp_config::inf_nan`: INF and quiet NaNs are supported.
- `info::fp_config::round_to_nearest`: round to nearest even rounding mode is supported.
- `info::fp_config::round_to_zero`: round to zero rounding mode is supported.
- `info::fp_config::round_to_inf`: round to positive and negative infinity rounding modes are supported.
- `info::fp_config::fma`: IEEE754-2008 fused multiply add is supported.
- `info::fp_config::correctly_rounded_divide_sqrt`: divide and sqrt are correctly rounded as defined by the IEEE754 specification.
- `info::fp_config::soft_float`: basic floating-point operations (such as addition, subtraction, multiplication) are implemented in software.

If half precision is supported by this SYCL device (i.e. the `device` has `aspect::fp16`) there is no minimum floating-point capability. If half support is not supported the returned `std::vector` must be empty. |

Table 4.19: Device information descriptors.
### Device descriptors

<table>
<thead>
<tr>
<th>Description</th>
<th>Return type</th>
</tr>
</thead>
</table>
| Returns a `std::vector<info::fp_config>` describing the single precision floating-point capability of this SYCL device. The `std::vector` must contain one or more of the following values: | \[ \begin{align*} 
  \cdot \text{info::fp_config::denorm}: & \text{denoms are supported.} \\
  \cdot \text{info::fp_config::inf_nan}: & \text{INF and quiet NaNs are supported.} \\
  \cdot \text{info::fp_config::round_to_nearest}: & \text{round to nearest even rounding mode is supported.} \\
  \cdot \text{info::fp_config::round_to_zero}: & \text{round to zero rounding mode is supported.} \\
  \cdot \text{info::fp_config::round_to_inf}: & \text{round to positive and negative infinity rounding modes are supported.} \\
  \cdot \text{info::fp_config::fma}: & \text{IEEE754-2008 fused multiply add is supported.} \\
  \cdot \text{info::fp_config::correctly_rounded_divide_sqrt}: & \text{divide and sqrt are correctly rounded as defined by the IEEE754 specification.} \\
  \cdot \text{info::fp_config::soft_float}: & \text{basic floating-point operations (such as addition, subtraction, multiplication) are implemented in software.} \\
\end{align*} \] |

If this SYCL device is not of type `info::device_type::custom` then the minimum floating-point capability must be: `info::fp_config::round_to_nearest` and `info::fp_config::inf_nan`.

| Table 4.19: Device information descriptors. |
### Device descriptors

<table>
<thead>
<tr>
<th>Device descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
</table>
| `info::device::double_fp_config` | `std::vector<info::fp_config>` | Returns a `std::vector` of `info::fp_config` describing the double precision floating-point capability of this SYCL device. The `std::vector` may contain zero or more of the following values:  
  - `info::fp_config::denorm`: denorms are supported.  
  - `info::fp_config::inf_nan`: INF and NaNs are supported.  
  - `info::fp_config::round_to_nearest`: round to nearest even rounding mode is supported.  
  - `info::fp_config::round_to_zero`: round to zero rounding mode is supported.  
  - `info::fp_config::round_to_inf`: round to positive and negative infinity rounding modes are supported.  
  - `info::fp_config::fma`: IEEE754-2008 fused multiply-add is supported.  
  - `info::fp_config::soft_float`: basic floating-point operations (such as addition, subtraction, multiplication) are implemented in software.  
  If double precision is supported by this SYCL device (i.e. the device has aspect `::fp64`) and this SYCL device is not of type `info::device_type::custom` then the minimum floating-point capability must be: `info::fp_config::fma`, `info::fp_config::round_to_nearest`, `info::fp_config::round_to_zero`, `info::fp_config::round_to_inf`, `info::fp_config::inf_nan` and `info::fp_config::denorm`. If double support is not supported the returned `std::vector` must be empty. |
| `info::device::global_mem_cache_type` | `info::global_mem_cache_type` | Returns the type of global memory cache supported.                                                                                     |
| `info::device::global_mem_cache_line_size` | `uint32_t` | Returns the size of global memory cache line in bytes.                                                                                      |
| `info::device::global_mem_cache_size` | `uint64_t` | Returns the size of global memory cache in bytes.                                                                                           |
| `info::device::global_mem_size` | `uint64_t` | Returns the size of global device memory in bytes.                                                                                          |

Table 4.19: Device information descriptors.
### Device descriptors

<table>
<thead>
<tr>
<th>Device descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::device::</td>
<td>max_constant_buffer_size</td>
<td>uint64_t Returns the maximum size in bytes of a constant buffer allocation. The minimum value is 64 KB if this SYCL device is not of type info::device_type::custom.</td>
</tr>
<tr>
<td>info::device::</td>
<td>max_constant_args</td>
<td>uint32_t Returns the maximum number of constant arguments that can be declared in a kernel. The minimum value is 8 if this SYCL device is not of type info::device_type::custom.</td>
</tr>
<tr>
<td>info::device::local_mem_type</td>
<td>info::local_mem_type</td>
<td>Returns the type of local memory supported. This can be info::local_mem_type::local implying dedicated local memory storage such as SRAM, or info::local_mem_type::global. If this SYCL device is of type info::device_type::custom this can also be info::local_mem_type::none, indicating local memory is not supported.</td>
</tr>
<tr>
<td>info::device::local_mem_size</td>
<td>uint64_t</td>
<td>Returns the size of local memory arena in bytes. The minimum value is 32 KB if this SYCL device is not of type info::device_type::custom.</td>
</tr>
<tr>
<td>info::device::</td>
<td>error_correction_support</td>
<td>bool Returns true if the device implements error correction for all accesses to compute device memory (global and constant). Returns false if the device does not implement such error correction.</td>
</tr>
<tr>
<td>info::device::</td>
<td>host_unified_memory</td>
<td>bool Deprecated, use device::has() with one of the aspect::usm_* aspects instead. Returns true if the device and the host have a unified memory subsystem and returns false otherwise.</td>
</tr>
<tr>
<td>info::device::</td>
<td>atomic_memory_order_capabilities</td>
<td>std::vector&lt;memory_order&gt; Returns the set of memory orderings supported by atomic operations on the device, which is guaranteed to include relaxed. If this device is a host device, the set must include all values of the memory_order enum class: relaxed, acquire, release, acq_rel and seq_cst.</td>
</tr>
<tr>
<td>info::device::</td>
<td>atomic_fence_order_capabilities</td>
<td>std::vector&lt;memory_order&gt; Returns the set of memory orderings supported by atomic_fence on the device, which is guaranteed to include relaxed. If this device is a host device, the set must include all values of the memory_order enum class: relaxed, acquire, release, acq_rel and seq_cst.</td>
</tr>
</tbody>
</table>

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### Device descriptors

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</tr>
</thead>
<tbody>
<tr>
<td>info::device::atomic_memory_scope_capabilities</td>
<td>std::vector&lt;memory_scope&gt;</td>
<td>Returns the set of memory scopes supported by atomic operations on the device, which is guaranteed to include work_group. If this device is a host device, the set must include all values of the memory_scope enum class: work_item, sub_group, work_group, device and system.</td>
</tr>
<tr>
<td>info::device::atomic_fence_scope_capabilities</td>
<td>std::vector&lt;memory_scope&gt;</td>
<td>Returns the set of memory scopes supported by atomic_fence on the device, which is guaranteed to include work_group. If this device is a host device, the set must include all values of the memory_scope enum class: work_item, sub_group, work_group, device and system.</td>
</tr>
<tr>
<td>info::device::profiling_timer_resolution</td>
<td>size_t</td>
<td>Returns the resolution of device timer in nanoseconds.</td>
</tr>
<tr>
<td>info::device::is_endian_little</td>
<td>bool</td>
<td>Returns true if this SYCL device is a little endian device and returns false otherwise.</td>
</tr>
<tr>
<td>info::device::is_available</td>
<td>bool</td>
<td>Returns true if the SYCL device is available and returns false if the device is not available.</td>
</tr>
<tr>
<td>info::device::is_compiler_available</td>
<td>bool</td>
<td>Deprecated. Returns the same value as device::has(aspect::online_compiler).</td>
</tr>
<tr>
<td>info::device::is_linker_available</td>
<td>bool</td>
<td>Deprecated. Returns the same value as device::has(aspect::online_linker).</td>
</tr>
<tr>
<td>info::device::execution_capabilities</td>
<td>std::vector<a href="">info::execution_capability</a></td>
<td>Returns a std::vector of the info::execution_capability describing the supported execution capabilities. Note that this information is intended for OpenCL interoperability only as SYCL only supports info::execution_capability::exec_kernel.</td>
</tr>
<tr>
<td>info::device::queue_profiling</td>
<td>bool</td>
<td>Deprecated. Returns the same value as device::has(aspect::queue_profiling).</td>
</tr>
<tr>
<td>info::device::built_in_kernels</td>
<td>std::vector<a href="">std::string</a></td>
<td>Returns a std::vector of built-in OpenCL kernels supported by this SYCL device.</td>
</tr>
<tr>
<td>info::device::platform</td>
<td>platform</td>
<td>Returns the SYCL platform associated with this SYCL device.</td>
</tr>
<tr>
<td>info::device::name</td>
<td>std::string</td>
<td>Returns the device name of this SYCL device.</td>
</tr>
<tr>
<td>info::device::vendor</td>
<td>std::string</td>
<td>Returns the vendor of this SYCL device.</td>
</tr>
<tr>
<td>info::device::driver_version</td>
<td>std::string</td>
<td>Returns the OpenCL software driver version as a std::string in the form: major_number.minor_number, if this SYCL device is an OpenCL device. Must return a std::string with the value “1.2” if this SYCL device is a host device.</td>
</tr>
</tbody>
</table>

Table 4.19: Device information descriptors.
### Device descriptors

<table>
<thead>
<tr>
<th>Device descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
</table>
| info::device::profile | std::string | Returns the OpenCL profile as a std::string, if this SYCL device is an OpenCL device. The value returned can be one of the following strings:  
  - FULL_PROFILE - if the device supports the OpenCL specification (functionality defined as part of the core specification and does not require any extensions to be supported).  
  - EMBEDDED_PROFILE - if the device supports the OpenCL embedded profile. Must return a std::string with the value "FULL PROFILE" if this is a host device. |
| info::device::version | std::string | Returns the SYCL version as a std::string in the form: `<major_version>.<minor_version>`. If this SYCL device is a host device, the `<major_version>.<minor_version>` value returned must be "1.2". |
| info::device::backend_version | std::string | Returns a string describing the version of the SYCL backend associated with the device. The possible values are specified in the SYCL backend specification of the SYCL backend associated with the device. |
| info::device::aspects | std::vector<aspect> | Returns a std::vector of aspect values supported by this SYCL device. |

Table 4.19: Device information descriptors.
### Device descriptors

| info::device::extensions | std::vector<std::string> | Deprecated, use info::device::aspects instead.
|-------------------------|--------------------------|-------------------------------------------------
|                         |                          | Returns a std::vector of extension names (the extension names do not contain any spaces) supported by this SYCL device. The extension names returned can be vendor supported extension names and one or more of the following Khronos approved extension names:
|                         |                          | • cl_khr_int64_base_atomics
|                         |                          | • cl_khr_int64_extended_atomics
|                         |                          | • cl_khr_3d_image_writes
|                         |                          | • cl_khr_fp16
|                         |                          | • cl_khr_gl_sharing
|                         |                          | • cl_khr_gl_event
|                         |                          | • cl_khr_d3d10_sharing
|                         |                          | • cl_khr_d3d11_sharing
|                         |                          | • cl_khr_depth_images
|                         |                          | • cl_khr_gl_depth_images
|                         |                          | • cl_khr_gl_msaa_sharing
|                         |                          | • cl_khr_image2d_from_buffer
|                         |                          | • cl_khr_initialize_memory
|                         |                          | • cl_khr_context_abort
|                         |                          | • cl_khr_spir
|                         |                          | If this SYCL device is an OpenCL device then following approved Khronos extension names must be returned by all device that support OpenCL C 1.2:
|                         |                          | • cl_khr_global_int32_base_atomics
|                         |                          | • cl_khr_global_int32_extended_atomics
|                         |                          | • cl_khr_local_int32_base_atomics
|                         |                          | • cl_khr_local_int32_extended_atomics
|                         |                          | • cl_khr_byte_addressable_store
|                         |                          | • cl_khr_fp64 (for backward compatibility if double precision is supported)
|                         |                          | Please refer to the OpenCL 1.2 Extension Specification for a detailed description of these extensions.
| info::device::printf_buffer_size | size_t | Returns the maximum size of the internal buffer that holds the output of printf calls from a kernel. The minimum value is 1 MB if info::device::profile returns true for this SYCL device.

<table>
<thead>
<tr>
<th>info::device::printf_buffer_size</th>
<th>size_t</th>
</tr>
</thead>
</table>

Table 4.19: Device information descriptors.
<table>
<thead>
<tr>
<th>Device descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::device::preferred_interop_user_sync</td>
<td>bool</td>
<td>Returns true if the preference for this SYCL device is for the user to be responsible for synchronization, when sharing memory objects between OpenCL and other APIs such as DirectX, false if the device/implementation has a performant path for performing synchronization of memory object shared between OpenCL and other APIs such as DirectX.</td>
</tr>
<tr>
<td>info::device::parent_device</td>
<td>device</td>
<td>Returns the parent SYCL device to which this sub-device is a child if this is a sub-device. Must throw an exception with the errc::invalid_object_error error code if this SYCL device is not a sub device.</td>
</tr>
<tr>
<td>info::device::partition_max_sub_devices</td>
<td>uint32_t</td>
<td>Returns the maximum number of sub-devices that can be created when this SYCL device is partitioned. The value returned cannot exceed the value returned by info::device::device_max_compute_units.</td>
</tr>
<tr>
<td>info::device::partition_properties</td>
<td>std::vector<a href="">info::partition_property</a></td>
<td>Returns the partition properties supported by this SYCL device; a vector of info::partition_property. If this SYCL device cannot be partitioned into at least two sub devices then the returned vector must be empty.</td>
</tr>
<tr>
<td>info::device::partition_affinity_domains</td>
<td>std::vector<a href="">info::partition_affinity_domain</a></td>
<td>Returns a std::vector of the partition affinity domains supported by this SYCL device when partitioning with info::partition_property::partition_by_affinity_domain.</td>
</tr>
</tbody>
</table>
| info::device::partition_type_property      | info::partition_property | Returns the partition property of this SYCL device. If this SYCL device is not a sub device then the the return value must be info::partition_property::no_partition, otherwise it must be one of the following values:  
  • info::partition_property::partition_equally  
  • info::partition_property::partition_by_counts  
  • info::partition_property::partition_by_affinity_domain |

Table 4.19: Device information descriptors.
### Device descriptors

<table>
<thead>
<tr>
<th>Device descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::device::</td>
<td>info::</td>
<td>Returns the partition affinity domain of this SYCL device. If this SYCL device is not a sub device or the sub device was not partitioned with info::partition_type::partition_by_affinity_domain then the return value must be info::partition_affinity_domain::not_applicable, otherwise it must be one of the following values:</td>
</tr>
<tr>
<td>partition_type_affinity_domain</td>
<td>partition_affinity_domain</td>
<td></td>
</tr>
<tr>
<td></td>
<td>uint32_t</td>
<td>Returns the device reference count. If the device is not a sub-device the value returned must be 1.</td>
</tr>
</tbody>
</table>

Table 4.19: Device information descriptors.

#### 4.6.4.3 Device aspects

Every SYCL device has an associated set of “aspects” which identify characteristics of the device. Aspects are defined via the enum class aspect enumeration:

```cpp
namespace sycl {
enum class aspect {
    host,
    cpu,
    gpu,
    accelerator,
    custom,
    fp16,
    fp64,
    int64_base_atomics,
    int64_extended_atomics,
    image,
    online_compiler,
    online_linker,
    queue_profiling,
    usm_device_allocations,
    usm_host_allocations,
};
```

CHAPTER 4. SYCL PROGRAMMING INTERFACE
4.6. SYCL RUNTIME CLASSES

```cpp
usm_shared_allocations,
usm_restricted_shared_allocations,
usm_system_allocator
```

```cpp
};
```

Table 4.20 lists the aspects that are defined in the core SYCL specification. However, a SYCL backend or extension may provide additional aspects. If so, the SYCL backend specification document or the extension document describes them. If a SYCL backend defined by the Khronos SYCL group provides aspects, their enumerated values are defined in the backend’s namespace. For example, an aspect specific to the OpenCL backend could be defined like this:

```cpp
namespace sycl {
    namespace opencl {
        namespace aspect {
            static constexpr auto bar = static_cast<sycl::aspect>(-1);
        } // namespace aspect
    } // namespace opencl
} // namespace sycl
```

Aspects provided by an extension or a vendor’s SYCL backend are defined as described in Chapter 6.

SYCL applications can query the aspects for a device via `device::has()` in order to determine whether the device supports any optional features. Table 4.20 tells which optional features are enabled by aspects in the core SYCL specification, but backends and extensions may provide optional features also. If so, the SYCL backend specification document or the extension document describes which features are enabled by each aspect.

A SYCL application can also use the `is_aspect_active<aspect>::value` trait to test whether an aspect is “active” at compile time. An aspect is active if the compilation environment supports any device which has that aspect. For example, if the implementation supports no devices with `aspect::custom`, the trait `is_aspect_active<aspect>::value` will be false. The set of active aspects could also be affected by command line options passed to the compiler. For example, if an implementation provides a command line option that disables `aspect::accelerator` devices, that trait will be false when the option is passed to the compiler.

[Note: Like any type trait, the value of `is_aspect_active<aspect>::value` has a uniform value across all parts of a SYCL application. If an implementation uses SMCP, all compiler passes define a particular aspect’s `is_aspect_active` type trait with the same value, regardless of whether that compiler pass’s device supports the aspect. Thus, `is_aspect_active` cannot be used to determine whether any particular device supports an aspect. Instead, applications must use `device::has()` or `platform::has()` for this. — end note]

The trait `sycl::is_aspect_active<aspect>::value` must be defined as either true or false for all the core SYCL aspects listed in Table 4.20, all aspects from Khronos ratified extensions, and all of a vendor’s own extension aspects.
<table>
<thead>
<tr>
<th>Aspect</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>aspect::host</td>
<td>A device that runs on the host CPU and exposes the host SYCL backend. Devices with this aspect have device type info::device_type::host.</td>
</tr>
<tr>
<td>aspect::cpu</td>
<td>A device that runs on a CPU, but doesn’t use the host SYCL backend. Devices with this aspect have device type info::device_type::cpu.</td>
</tr>
<tr>
<td>aspect::gpu</td>
<td>A device that can also be used to accelerate a 3D graphics API. Devices with this aspect have device type info::device_type::gpu.</td>
</tr>
<tr>
<td>aspect::accelerator</td>
<td>A dedicated accelerator device, usually using a peripheral interconnect for communication. Devices with this aspect have device type info::device_type::accelerator.</td>
</tr>
<tr>
<td>aspect::custom</td>
<td>A dedicated accelerator that can use the SYCL API, but programmable kernels cannot be dispatched to the device, only fixed functionality is available. See Section 3.8.7. Devices with this aspect have device type info::device_type::custom.</td>
</tr>
<tr>
<td>aspect::fp16</td>
<td>Indicates that the device supports half precision floating point operations.</td>
</tr>
<tr>
<td>aspect::fp64</td>
<td>Indicates that the device supports 64-bit precision floating point operations.</td>
</tr>
<tr>
<td>aspect::int64_base_atomics</td>
<td>Indicates that the device supports the following atomic operations on 64-bit values: atomic::load, atomic::store, atomic::fetch_add, atomic::fetch_sub, atomic::exchange, atomic::compare_exchange_strong and atomic::compare_exchange_weak.</td>
</tr>
<tr>
<td>aspect::int64_extended_atomics</td>
<td>Indicates that the device supports the following atomic operations on 64-bit values: atomic::fetch_min, atomic::fetch_max, atomic::fetch_and, atomic::fetch_or, and atomic::fetch_xor.</td>
</tr>
<tr>
<td>aspect::image</td>
<td>Indicates that the device supports images (Section 4.7.3). Devices of type info::device_type::host always have this support.</td>
</tr>
<tr>
<td>aspect::online_compiler</td>
<td>Indicates that the device supports online compilation of device code. Devices that have this aspect support the build() and compile() functions defined in Section 4.13.7.</td>
</tr>
</tbody>
</table>

Table 4.20: Device aspects defined by the core SYCL specification.
4.6. SYCL RUNTIME CLASSES

<table>
<thead>
<tr>
<th>Aspect</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>aspect::online_linker</td>
<td>Indicates that the device supports online linking of device code. Devices that have this aspect support the link() functions defined in Section 4.13.7. All devices that have this aspect also have aspect::online_compiler.</td>
</tr>
<tr>
<td>aspect::queue_profiling</td>
<td>Indicates that the device supports queue profiling via property::queue::enable_profiling.</td>
</tr>
<tr>
<td>aspect::usm_device_allocations</td>
<td>Indicates that the device supports explicit USM allocations as described in Section 4.8.</td>
</tr>
<tr>
<td>aspect::usm_host_allocations</td>
<td>Indicates that the device can access USM memory allocated via usm::alloc::host. (See Section 4.8.)</td>
</tr>
<tr>
<td>aspect::usm_shared_allocations</td>
<td>Indicates that the device supports USM memory allocated via usm::alloc::shared as restricted USM, concurrent USM, or both. (See Section 4.8.)</td>
</tr>
<tr>
<td>aspect::usm_restricted_shared_allocations</td>
<td>Indicates that the device supports USM memory allocated via usm::alloc::shared as restricted USM. Any device with this aspect will also have aspect::usm_shared_allocations. (See Section 4.8.)</td>
</tr>
<tr>
<td>aspect::usm_system_allocator</td>
<td>Indicates that the system allocator may be used instead of SYCL USM allocation mechanisms for usm::alloc::shared allocations on this device. (See Section 4.8.)</td>
</tr>
</tbody>
</table>

Table 4.20: Device aspects defined by the core SYCL specification.

4.6.5 Queue class

The SYCL queue class encapsulates a single SYCL queue which schedules kernels on a SYCL device. The SYCL queue can encapsulate to one or multiple native backend objects.

A SYCL queue can be used to submit command groups to be executed by the SYCL runtime using the submit member function.

All member functions of the queue class are synchronous and errors are handled by throwing synchronous SYCL exceptions. The submit member function schedules command groups asynchronously, so any errors in the submission of a command group are handled by throwing synchronous SYCL exceptions. Any exceptions from the command group after it has been submitted are handled by passing asynchronous errors at specific times to an async_handler, as described in 4.15.

A SYCL queue can wait for all command groups that it has submitted by calling wait or wait_and_throw.

The default constructor of the SYCL queue class will construct a queue based on the SYCL device returned from the default_selector_v (see Section 4.6.1.1).
All other constructors construct a host or device queue, determined by the parameters provided. All constructors will implicitly construct a SYCL platform, device and context in order to facilitate the construction of the queue.

Each constructor takes as the last parameter an optional SYCL property_list to provide properties to the SYCL queue.

The SYCL queue class provides the common reference semantics (see Section 4.5.3).

### 4.6.5.1 Queue interface

A synopsis of the SYCL queue class is provided below. The constructors and member functions of the SYCL queue class are listed in Tables 4.21 and 4.22 respectively. The additional common special member functions and common member functions are listed in 4.5.3 in Tables 4.1 and 4.2, respectively.

```cpp
namespace sycl {
    class queue {
        public:
            explicit queue(const property_list &propList = {});
            explicit queue(const async_handler &asyncHandler,
                            const property_list &propList = {});
            template <typename DeviceSelector>
            explicit queue(const DeviceSelector &deviceSelector,
                            const property_list &propList = {});
            template <typename DeviceSelector>
            explicit queue(const DeviceSelector &deviceSelector,
                            const async_handler &asyncHandler,
                            const property_list &propList = {});
            explicit queue(const device &syclDevice, const property_list &propList = {});
            explicit queue(const device &syclDevice, const async_handler &asyncHandler,
                            const property_list &propList = {});
            template <typename DeviceSelector>
            explicit queue(const context &syclContext,
                            const DeviceSelector &deviceSelector,
                            const property_list &propList = {});
            template <typename DeviceSelector>
            explicit queue(const context &syclContext,
                            const async_handler &asyncHandler,
                            const DeviceSelector &deviceSelector,
                            const property_list &propList = {});
            explicit queue(const context &syclContext, const device &syclDevice,
                            const property_list &propList = {});
            explicit queue(const context &syclContext, const device &syclDevice,
                            const async_handler &asyncHandler,
                            const property_list &propList = {});

```
4.6. SYCL RUNTIME CLASSES

/* -- common interface members -- */

/* -- property interface members -- */

backend get_backend() const;
context get_context() const;
device get_device() const;
bool is_host() const;
bool is_in_order() const;

template <info::queue param>
typename info::param_traits<info::queue, param>::return_type get_info() const;

template <typename BackendEnum, BackendEnum param>
typename info::param_traits<BackendEnum, param>::return_type
get_backend_info() const;

template <typename T>
event submit(T cgf);

template <typename T>
event submit(T cgf, const queue &secondaryQueue);

void wait();
void wait_and_throw();
void throw_asynchronous();

/* -- convenience shortcuts -- */

template <typename KernelName, typename KernelType>
event single_task(const KernelType &KernelFunc);

template <typename KernelName, typename KernelType>
event single_task(event DepEvent, const KernelType &KernelFunc);

template <typename KernelName, typename KernelType>
event single_task(const std::vector<event> &DepEvents,

    const KernelType &KernelFunc);

template <typename KernelName, typename KernelType, int Dims>
event parallel_for(range<Dims> NumWorkItems, const KernelType &KernelFunc);

...
### Constructors of the \texttt{queue} class.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>\texttt{explicit queue(const property_list &amp;propList = {})}</td>
<td>Constructs a SYCL \texttt{queue} instance using the device constructed from the \texttt{default_selector.v}. Zero or more properties can be provided to the constructed SYCL \texttt{queue} via an instance of \texttt{property_list}.</td>
</tr>
<tr>
<td>\texttt{explicit queue(const async_handler &amp;asyncHandler, const property_list &amp;propList = {})}</td>
<td>Constructs a SYCL \texttt{queue} instance with an \texttt{async_handler} using the device constructed from the \texttt{default_selector.v}. Zero or more properties can be provided to the constructed SYCL \texttt{queue} via an instance of \texttt{property_list}.</td>
</tr>
<tr>
<td>\texttt{template &lt;typename DeviceSelector&gt; explicit queue(const DeviceSelector &amp;deviceSelector, const property_list &amp;propList = {})}</td>
<td>Constructs a SYCL \texttt{queue} instance using the device returned by the \texttt{device selector} provided. Zero or more properties can be provided to the constructed SYCL \texttt{queue} via an instance of \texttt{property_list}.</td>
</tr>
</tbody>
</table>

Continued on next page.
### Constructors of the queue class

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
</table>
| template <typename DeviceSelector>
  explicit queue(const DeviceSelector & deviceSelector,  
  const async_handler &asyncHandler,  
  const property_list &propList = {})                                                                 | Constructs a SYCL queue instance with an async_handler using the device returned by the device selector provided. Zero or more properties can be provided to the constructed SYCL queue via an instance of property_list. |
| explicit queue(const device &syclDevice,  
  const property_list &propList = {})                                                                 | Constructs a SYCL queue instance using the syclDevice provided. Zero or more properties can be provided to the constructed SYCL queue via an instance of property_list. |
| explicit queue(const device &syclDevice,  
  const async_handler &asyncHandler,  
  const property_list &propList = {})                                                                 | Constructs a SYCL queue instance with an async_handler using the syclDevice provided. Zero or more properties can be provided to the constructed SYCL queue via an instance of property_list. |
| template <typename DeviceSelector>
  explicit queue(const context &syclContext,  
  const DeviceSelector &deviceSelector,  
  const property_list &propList = {})                                                                 | Constructs a SYCL queue instance that is associated with the syclContext provided, using the device returned by the device selector provided. Must throw an exception with the errc::invalid_object_error error code if syclContext does not encapsulate the SYCL device returned by deviceSelector. Zero or more properties can be provided to the constructed SYCL queue via an instance of property_list. |
| template <typename DeviceSelector>
  explicit queue(const context &syclContext,  
  const DeviceSelector &deviceSelector,  
  const async_handler &asyncHandler,  
  const property_list &propList = {})                                                                 | Constructs a SYCL queue instance with an async_handler that is associated with the syclContext provided, using the device returned by the device selector provided. Must throw an exception with the errc::invalid_object_error error code if syclContext does not encapsulate the SYCL device returned by deviceSelector. Zero or more properties can be provided to the constructed SYCL queue via an instance of property_list. |
| explicit queue(const context &syclContext,  
  const device &syclDevice,  
  const property_list &propList = {})                                                                 | Constructs a SYCL queue instance using the syclDevice provided, and associated with the syclContext provided. Must throw an exception with the errc::invalid_object_error error code if syclContext does not encapsulate the SYCL device syclDevice. Zero or more properties can be provided to the constructed SYCL queue via an instance of property_list. |

Table 4.21: Constructors of the queue class.

Continued on next page
### Constructor

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>explicit queue(const context &amp;syclContext, const device &amp;syclDevice, const async_handler &amp;asyncHandler, const property_list &amp;propList = {})</code></td>
<td>Constructs a SYCL queue instance with an async_handler using the syclDevice provided, and associated with the syclContext provided. Must throw an exception with the errc::invalid_object_error error code if syclContext does not encapsulate the SYCL device syclDevice. Zero or more properties can be provided to the constructed SYCL queue via an instance of property_list.</td>
</tr>
</tbody>
</table>

Table 4.21: Constructors of the `queue` class.

### Member function

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>backend get_backend()const</code></td>
<td>Returns the a backend identifying the SYCL backend associated with this queue.</td>
</tr>
<tr>
<td><code>context get_context ()const</code></td>
<td>Returns the SYCL queue’s context. Reports errors using SYCL exception classes. The value returned must be equal to that returned by <code>get_info&lt;info::queue::context&gt;()</code>.</td>
</tr>
<tr>
<td><code>device get_device ()const</code></td>
<td>Returns the SYCL device the queue is associated with. Reports errors using SYCL exception classes. The value returned must be equal to that returned by <code>get_info&lt;info::queue::devices&gt;()</code>.</td>
</tr>
<tr>
<td><code>bool is_host()const</code></td>
<td>Returns true if the backend associated with this SYCL queue is a SYCL host backend.</td>
</tr>
<tr>
<td><code>bool is_in_order()const</code></td>
<td>Returns true if the SYCL queue was created with the in_order property. Equivalent to <code>has_property&lt;property::queue::in_order&gt;()</code>.</td>
</tr>
<tr>
<td><code>void wait()</code></td>
<td>Performs a blocking wait for the completion of all enqueued tasks in the queue. Synchronous errors will be reported through SYCL exceptions.</td>
</tr>
</tbody>
</table>

Table 4.22: Member functions for `queue` class.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>void wait_and_throw ()</code></td>
<td>Performs a blocking wait for the completion of all enqueued tasks in the queue. Synchronous errors will be reported through SYCL exceptions. Any unconsumed asynchronous errors will be passed to the async_handler associated with the queue or enclosing context. If no user defined async_handler is associated with the queue or enclosing context, then an implementation defined default async_handler is called to handle any errors, as described in 4.15.1.2.</td>
</tr>
<tr>
<td><code>void throw_asynchronous ()</code></td>
<td>Checks to see if any unconsumed asynchronous errors have been produced by the queue and if so reports them by passing them to the async_handler associated with the queue or enclosing context. If no user defined async_handler is associated with the queue or enclosing context, then an implementation defined default async_handler is called to handle any errors, as described in 4.15.1.2.</td>
</tr>
<tr>
<td><code>template &lt;info::queue param&gt; typename info::param_traits &lt;info::queue, param&gt;::return_type get_info () const</code></td>
<td>Queries this SYCL queue for information requested by the template parameter param. Specializations of info::param_traits must be defined in accordance with the info parameters in Table 4.23 to facilitate returning the type associated with the param parameter.</td>
</tr>
<tr>
<td><code>template &lt;typename T&gt; event submit(T cgf)</code></td>
<td>Submit a command group function object to the queue, in order to be scheduled for execution on the device.</td>
</tr>
<tr>
<td><code>template &lt;typename T&gt; event submit(T cgf, queue &amp; secondaryQueue)</code></td>
<td>Submit a command group function object to the queue, in order to be scheduled for execution on the device. On a kernel error, this command group function object, is then scheduled for execution on the secondary queue. Returns an event, which corresponds to the queue the command group function object is being enqueued on.</td>
</tr>
</tbody>
</table>

Table 4.22: Member functions for `queue` class.
### Member function | Description
--- | ---
```
template <typename BackendEnum, BackendEnum param>
  typename info::param_traits<BackendEnum, param>::
  return_type
  get_backend_info() const
```
Queries this SYCL queue for SYCL backend-specific information requested by the template parameter `param`. `BackendEnum` can be any enum class type specified by the SYCL backend specification of a supported SYCL backend named according to the convention `info::<backend_name>::queue` and `param` must be a valid enumeration of that enum class. Specializations of `info::param_traits` must be defined for `BackendEnum` in accordance with the SYCL backend specification. Must throw an `exception` with the `errc::invalid_object_error` error code if the SYCL backend that corresponds with `BackendEnum` is different from the SYCL backend that is associated with this `queue`.

```
template <typename KernelName, typename KernelType>
  event single_task(const KernelType &kernelFunc)
```
Defines and invokes a SYCL kernel function as a lambda function or a named function object type. The kernel function is submitted to the queue, in order to be scheduled for execution on the device.

```
template <typename KernelName, typename KernelType>
  event single_task(
    event DepEvent, const KernelType &kernelFunc)
```
Defines and invokes a SYCL kernel function as a lambda function or a named function object type. The kernel function is submitted to the queue, in order to be scheduled for execution on the device once the `event` specified by `DepEvent` has completed.

```
template <typename KernelName, typename KernelType>
  event single_task(
    const std::vector<event> &DepEvents, 
    const KernelType &kernelFunc)
```
Defines and invokes a SYCL kernel function as a lambda function or a named function object type. The kernel function is submitted to the queue, in order to be scheduled for execution on the device once every `event` specified by `DepEvents` has completed.

```
template <typename KernelName, typename KernelType, int dimensions>
  event parallel_for(
    range<dimensions> numWorkItems, 
    const KernelType &kernelFunc)
```
Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified range and given an id or item for indexing in the indexing space defined by range. The kernel function is submitted to the queue, in order to be scheduled for execution on the device.

| Continued on next page

Table 4.22: Member functions for `queue` class.
### 4.6. SYCL RUNTIME CLASSES

#### Table 4.22: Member functions for `queue` class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `template <typename KernelName, typename KernelType, int dimensions>  
  event parallel_for(  
  range<dimensions> numWorkItems, event DepEvent,  
  const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified range and given an id or item for indexing in the indexing space defined by range. The kernel function is submitted to the queue, in order to be scheduled for execution on the device once the `event` specified by `DepEvent` has completed. |
| `template <typename KernelName, typename KernelType, int dimensions>  
  event parallel_for(  
  range<dimensions> numWorkItems,  
  const std::vector<event> &DepEvents,  
  const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified range and given an id or item for indexing in the indexing space defined by range. The kernel function is submitted to the queue, in order to be scheduled for execution on the device once every `event` specified by `DepEvents` has completed. |
| `template <typename KernelName, typename KernelType, int dimensions>  
  event parallel_for(  
  range<dimensions> numWorkItems,  
  id<dimensions> workItemOffset,  
  const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified range and given an id or item for indexing in the indexing space defined by range. The kernel function is submitted to the queue, in order to be scheduled for execution. |
| `template <typename KernelName, typename KernelType, int dimensions>  
  event parallel_for(  
  range<dimensions> numWorkItems,  
  id<dimensions> workItemOffset,  
  event DepEvent,  
  const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified range and given an id or item for indexing in the indexing space defined by range. The kernel function is submitted to the queue, in order to be scheduled for execution on the device once the `event` specified by `DepEvent` has completed. |
| `template <typename KernelName, typename KernelType, int dimensions>  
  event parallel_for(  
  range<dimensions> numWorkItems,  
  id<dimensions> workItemOffset,  
  const std::vector<event> &DepEvents,  
  const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified range and given an id or item for indexing in the indexing space defined by range. The kernel function is submitted to the queue, in order to be scheduled for execution on the device once every `event` specified by `DepEvents` has completed. |

Continued on next page
### Member function

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `template <typename KernelName, typename KernelType, int dimensions>
  event parallel_for(
  nd_range<dimensions> executionRange,
  const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified `nd-range` and given an `nd-item` for indexing in the indexing space defined by the `nd-range`. The kernel function is submitted to the queue, in order to be scheduled for execution. |
| `template <typename KernelName, typename KernelType, int dimensions>
  event parallel_for(
  nd_range<dimensions> executionRange,
  event DepEvent, const &KernelType kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified `nd-range` and given an `nd-item` for indexing in the indexing space defined by the `nd-range`. The kernel function is submitted to the queue, in order to be scheduled for execution on the device once the `event` specified by `DepEvent` has completed. |
| `template <typename KernelName, typename KernelType, int dimensions>
  event parallel_for(
  nd_range<dimensions> executionRange,
  const std::vector<event> &DepEvents,
  const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified `nd-range` and given an `nd-item` for indexing in the indexing space defined by the `nd-range`. The kernel function is submitted to the queue, in order to be scheduled for execution on the device once every `event` specified by `DepEvents` has completed. |

End of table

Table 4.22: Member functions for `queue` class.

### 4.6.5.2 Queue information descriptors

A queue can be queried for information using the `get_info` member function of the `queue` class, specifying one of the info parameters enumerated in `info::queue`. Every `queue` (including a host `queue`) must produce a valid value for each info parameter. The possible values for each info parameter and any restriction are defined in the specification of the SYCL backend associated with the `queue`. All info parameters in `info::queue` are specified in Table 4.23 and the synopsis for `info::queue` is described in appendix A.4.

<table>
<thead>
<tr>
<th>Queue Descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>info::queue::context</code></td>
<td><code>context</code></td>
<td>Returns the SYCL <code>context</code> associated with this SYCL <code>queue</code>.</td>
</tr>
<tr>
<td><code>info::queue::device</code></td>
<td><code>device</code></td>
<td>Returns the SYCL <code>device</code> associated with this SYCL <code>queue</code>.</td>
</tr>
</tbody>
</table>

End of table

Table 4.23: Queue information descriptors.

### 4.6.5.3 Queue properties

The properties that can be provided when constructing the SYCL `queue` class are describe in Table 4.24.
### 4.6. SYCL RUNTIME CLASSES

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>property::queue::enable_profiling</td>
<td>The enable_profiling property adds the requirement that the SYCL runtime must capture profiling information for the command groups that are submitted from this SYCL queue and provide said information via the SYCL event class get_profiling_info member function, if the associated SYCL device has aspect::queue_profiling.</td>
</tr>
<tr>
<td>property::queue::in_order</td>
<td>The in_order property adds the requirement that the SYCL queue provides in-order semantics where tasks are executed in the order in which they are submitted. Tasks submitted in this fashion can be viewed as having an implicit dependence on the previously submitted operation.</td>
</tr>
</tbody>
</table>

End of table

Table 4.24: Properties supported by the SYCL queue class.

The constructors of the queue property classes are listed in Table 4.25.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>property::queue::enable_profiling::enable_profiling()</td>
<td>Constructs a SYCL enable_profiling property instance.</td>
</tr>
<tr>
<td>property::queue::in_order::in_order()</td>
<td>Constructs a SYCL in_order property instance.</td>
</tr>
</tbody>
</table>

End of table

Table 4.25: Constructors of the queue property classes.

#### 4.6.5.4 Queue error handling

Queue errors come in two forms:

- **Synchronous Errors** are those that we would expect to be reported directly at the point of waiting on an event, and hence waiting for a queue to complete, as well as any immediate errors reported by enqueuing work onto a queue. Such errors are reported through C++ exceptions.

- **Asynchronous errors** are those that are produced or detected after associated host API calls have returned (so can’t be thrown as exceptions by the API call), and that are handled by an async_handler through which the errors are reported. Handling of asynchronous errors from a queue occurs at specific times, as described by 4.15.

Note that if there are asynchronous errors to be processed when a queue is destructed, the handler is called and this might delay or block the destruction, according to the behavior of the handler.
4.6.6 Event class

An event in SYCL is an object that represents the status of an operation that is being executed by the SYCL runtime.

Typically in SYCL, data dependency and execution order is handled implicitly by the SYCL runtime. However, in some circumstances developers want fine grain control of the execution, or want to retrieve properties of a command that is running.

A SYCL event maps to a single SYCL backend object when available. Note that, although an event represents the status of a particular operation, the dependencies of a certain event can be used to keep track of multiple steps required to synchronize said operation.

A SYCL event is returned by the submission of a command group. The dependencies of the event returned via the submission of the command group are the implementation-defined commands associated with the command group execution.

The SYCL event class provides the common reference semantics (see Section 4.5.3).

The constructors and member functions of the SYCL event class are listed in Tables 4.26 and 4.27, respectively. The additional common special member functions and common member functions are listed in Tables 4.1 and 4.2, respectively.

```cpp
namespace sycl {
    class event {
        public:
            event();
           .backend get_backend() const;
            bool is_host() const;
            std::vector<event> get_wait_list();
            void wait();
            static void wait(const std::vector<event> &eventList);
            void wait_and_throw();
            static void wait_and_throw(const std::vector<event> &eventList);
            template <info::event param>
            typename info::param_traits<info::event, param>::return_type
            get_info() const;
            template <typename BackendEnum, BackendEnum param>
            typename info::param_traits<BackendEnum, param>::return_type
            get_backend_info() const;
            template <info::event_profiling param>
```
4.6. SYCL RUNTIME CLASSES

```cpp
32    typename info::param_traits<info::event_profiling, param>::return_type
33    get_profiling_info() const;
34
35 } // namespace sycl
```

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>event ()</td>
<td>Constructs a host event that is immediately ready. The event has no dependencies and no associated commands. Waiting on this event will return immediately.</td>
</tr>
</tbody>
</table>

End of table

Table 4.26: Constructors of the `event` class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>backend get_backend()const</td>
<td>Returns the a backend identifying the SYCL backend associated with this event.</td>
</tr>
<tr>
<td>bool is_host()const</td>
<td>Returns true if this SYCL event is a host event.</td>
</tr>
<tr>
<td><code>std::vector&lt;event&gt;</code> get_wait_list()</td>
<td>Return the list of events that this event waits for in the dependence graph. Only direct dependencies are returned, and not transitive dependencies that direct dependencies wait on. Whether already completed events are included in the returned list is implementation defined.</td>
</tr>
<tr>
<td>void wait()</td>
<td>Wait for the event and the command associated with it to complete.</td>
</tr>
<tr>
<td>void wait_and_throw()</td>
<td>Wait for the event and the command associated with it to complete. Any unconsumed asynchronous errors from any context that the event was waiting on executions from will be passed to the async_handler associated with the context. If no user defined async_handler is associated with the context, then an implementation defined default async_handler is called to handle any errors, as described in 4.15.1.2.</td>
</tr>
<tr>
<td>static void wait(</td>
<td>Synchronously wait on a list of events.</td>
</tr>
<tr>
<td><code>const std::vector&lt;event&gt;</code> &amp;eventList)</td>
<td></td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.27: Member functions for the `event` class.
## 4.6. SYCL RUNTIME CLASSES

### Table 4.27: Member functions for the `event` class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>static void wait_and_throw(</strong>&lt;br&gt;const std::vector&lt;event&gt; &amp;eventList**);**</td>
<td>Synchronously wait on a list of events. Any unconsumed asynchronous errors from any context that the event was waiting on executions from will be passed to the <code>async_handler</code> associated with the context. If no user defined <code>async_handler</code> is associated with the context, then an implementation defined default <code>async_handler</code> is called to handle any errors, as described in 4.15.1.2.</td>
</tr>
<tr>
<td><strong>template &lt;info::event param&gt;</strong>&lt;br&gt;typename info::param_traits&lt;br&gt;ret&lt;info::event, param&gt;::return_type&lt;br&gt;get_info();<strong>const</strong></td>
<td>Queries this SYCL <code>event</code> for information requested by the template parameter <code>param</code>. Specializations of <code>info::param_traits</code> must be defined in accordance with the info parameter names in Table 4.28 to facilitate returning the type associated with the <code>param</code> parameter.</td>
</tr>
<tr>
<td><strong>template &lt;typename BackendEnum, BackendEnum param&gt;</strong>&lt;br&gt;typename info::param_traits&lt;BackendEnum, param&gt;::return_type&lt;br&gt;get_backend_info();<strong>const</strong></td>
<td>Queries this SYCL <code>event</code> for SYCL backend-specific information requested by the template parameter <code>param</code>. <code>BackendEnum</code> can be any enum class type specified by the SYCL backend specification of a supported SYCL backend named according to the convention <code>info::&lt;backend_name&gt;::event</code> and <code>param</code> must be a valid enumeration of that enum class. Specializations of <code>info::param_traits</code> must be defined for <code>BackendEnum</code> in accordance with the SYCL backend specification. Must throw an exception with the <code>errc::invalid_object_error</code> error code if the SYCL backend that corresponds with <code>BackendEnum</code> is different from the SYCL backend that is associated with this <code>event</code>.</td>
</tr>
</tbody>
</table>

Continued on next page
### 4.6. SYCL RUNTIME CLASSES

#### 4.6.6.1 Event information and profiling descriptors

An `event` can be queried for information using the `get_info` member function of the `event` class, specifying one of the info parameters enumerated in `info::event`. Every `event` (including a host `event`) must produce a valid value for each info parameter. The possible values for each info parameter and any restrictions are defined in the specification of the SYCL backend associated with the `event`. All info parameters in `info::event` are specified in Table 4.28 and the synopsis for `info::event` is described in appendix A.6.

<table>
<thead>
<tr>
<th>Event Descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>info::event::command_execution_status</code></td>
<td><code>info::event_command_status</code></td>
<td>Returns the event status of the command group associated with this SYCL <code>event</code>.</td>
</tr>
</tbody>
</table>

End of table

An `event` can be queried for profiling information using the `get_profiling_info` member function of the `event` class, specifying one of the profiling info parameters enumerated in `info::event_profiling`. Every `event` (including a host `event`) must produce a valid value for each info parameter. The possible values for each info parameter and any restrictions are defined in the specification of the SYCL backend associated with the `event`. All info parameters in `info::event_profiling` are specified in Table 4.29 and the synopsis for `info::event_profiling` is described in appendix A.6.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;info::event_profiling p&gt;</code></td>
<td>Queries this SYCL <code>event</code> for profiling information requested by the parameter <code>p</code>. If the requested profiling information is unavailable when <code>get_profiling_info</code> is called due to incompletion of <code>command groups</code> associated with the <code>event</code>, then the call to <code>get_profiling_info</code> will block until the requested profiling information is available. An example is asking for <code>info::event_profiling::command_end</code> when the associated <code>command group</code> has yet to finish execution. Calls to <code>get_profiling_info</code> must throw an <code>exception</code> with the <code>errc::invalid_object_error</code> error code if the SYCL <code>queue</code> which submitted the <code>command group</code> this SYCL <code>event</code> is associated with was not constructed with the <code>property::queue::enable_profiling</code> property. Specializations of <code>info::param_traits</code> must be defined in accordance with the info parameters in Table 4.29 to facilitate returning the type associated with the <code>p</code> parameter.</td>
</tr>
</tbody>
</table>
### Event information profiling descriptor

<table>
<thead>
<tr>
<th>Descriptor</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::event_profiling::command_submit</td>
<td>uint64_t</td>
<td>Returns an implementation defined 64-bit value describing the time in nanoseconds when the associated command group was submitted.</td>
</tr>
<tr>
<td>info::event_profiling::command_start</td>
<td>uint64_t</td>
<td>Returns an implementation defined 64-bit value describing the time in nanoseconds when the associated command group started executing.</td>
</tr>
<tr>
<td>info::event_profiling::command_end</td>
<td>uint64_t</td>
<td>Returns an implementation defined 64-bit value describing the time in nanoseconds when the associated command group finished executing.</td>
</tr>
</tbody>
</table>

Table 4.29: Profiling information descriptors for the SYCL event class.

## 4.7 Data access and storage in SYCL

In SYCL, data storage and access are handled by separate classes. Buffers and images handle storage and ownership of the data, whereas accessors handle access to the data. Buffers and images in SYCL can be bound to more than one device or context, including across different SYCL backends. They also handle ownership of the data, while allowing exception handling for blocking and non-blocking data transfers. Accessors manage data transfers between the host and all of the devices in the system, as well as tracking of data dependencies.

### 4.7.1 Host allocation

A SYCL runtime may need to allocate temporary objects on the host to handle some operations (such as copying data from one context to another). Allocation on the host is managed using an allocator object, following the standard C++ allocator class definition. The default allocator for memory objects is implementation defined, but the user can supply their own allocator class.

```cpp
1 {  
2     buffer<int, 1, UserDefinedAllocator<int> > b(d);  
3 }
```

When an allocator returns a `nullptr`, the runtime cannot allocate data on the host. Note that in this case the runtime will raise an error if it requires host memory but it is not available (e.g. when moving data across SYCL backend contexts).

The definition of allocators extends the current functionality of SYCL, ensuring that users can define allocator functions for specific hardware or certain complex shared memory mechanisms (e.g. NUMA), and improves interoperability with STL-based libraries (e.g. Intel’s TBB provides an allocator).

### 4.7.1.1 Default allocators

A default allocator is always defined by the implementation, and it is guaranteed to return non-`nullptr` and new memory positions every call. The default allocator for const buffers will remove the const-ness of the type (therefore, the default allocator for a buffer of type “const int” will be an `Allocator<int>`). This implies that...
host accessors will not synchronize with the pointer given by the user in the buffer/image constructor, but will use the memory returned by the Allocator itself for that purpose. The user can implement an allocator that returns the same address as the one passed in the buffer constructor, but it is the responsibility of the user to handle the potential race conditions.

<table>
<thead>
<tr>
<th>Allocators</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>buffer_allocator</td>
<td>It is the default buffer allocator used by the runtime, when no allocator is defined by the user.</td>
</tr>
<tr>
<td>image_allocator</td>
<td>It is the default allocator used by the runtime for the SYCL unsampled_image and sampled_image classes when no allocator is provided by the user. The image_allocator is required allocate in elements of std::byte.</td>
</tr>
</tbody>
</table>

Table 4.30: SYCL Default Allocators.

See Section 4.7.5 for details on manual host-device synchronization.

4.7.2 Buffers

The buffer class defines a shared array of one, two or three dimensions that can be used by the SYCL kernel and has to be accessed using accessor classes. Buffers are templated on both the type of their data, and the number of dimensions that the data is stored and accessed through.

A buffer does not map to only one underlying backend object, and all SYCL backend memory objects may be temporary for use within a command group on a specific device. Note that if no source data is provided for a buffer, the buffer uses uninitialized memory for performance reasons. So it is up to the programmer to explicitly construct the objects in this case if required.

More generally, since the value type of a buffer is required to be trivially copyable, there is no constructor or destructor called in any case.

A SYCL buffer can construct an instance of a SYCL buffer that reinterprets the original SYCL buffer with a different type, dimensionality and range using the member function reinterpret. The reinterpreted SYCL buffer that is constructed must behave as though it were a copy of the SYCL buffer that constructed it (see sec 4.5.3) with the exception that the type, dimensionality and range of the reinterpreted SYCL buffer must reflect the type, dimensionality and range specified when calling the reinterpret member function. By extension of this, the class member types value_type, reference and const_reference, and the member functions get_range and get_count of the reinterpreted SYCL buffer must reflect the new type, dimensionality and range. The data that the original SYCL buffer and the reinterpreted SYCL buffer manage remains unaffected, though the representation of the data when accessed through the reinterpreted SYCL buffer may alter to reflect the new type, dimensionality and range. It is important to note that a reinterpreted SYCL buffer is a copy of the original SYCL buffer only, and not a new SYCL buffer. Constructing more than one SYCL buffer managing the same host pointer is still undefined behavior.

The SYCL buffer class template provides the common reference semantics (see Section 4.5.3).
4.7.2.1 Buffer interface

The constructors and member functions of the SYCL buffer class template are listed in Tables 4.31 and 4.32, respectively. The additional common special member functions and common member functions are listed in Tables 4.1 and 4.2, respectively.

Each constructor takes as the last parameter an optional SYCL property_list to provide properties to the SYCL buffer.

The SYCL buffer class template takes a template parameter AllocatorT for specifying an allocator which is used by the SYCL runtime when allocating temporary memory on the host. If no template argument is provided, then the default allocator for the SYCL buffer class buffer_allocator will be used (see 4.7.1.1).

```cpp
namespace sycl {
    namespace property {
        namespace buffer {
            class use_host_ptr {
                public:
                use_host_ptr() = default;
            };

            class use_mutex {
                public:
                use_mutex(std::mutex &mutexRef);
                std::mutex *get_mutex_ptr() const;
            };

            class context_bound {
                public:
                context_bound(context boundContext);
                context get_context() const;
            };
        } // namespace buffer
    } // namespace property

    template <typename T, int dimensions = 1,
              typename AllocatorT = sycl::buffer_allocator>
    class buffer {
    public:
        using value_type = T;
        using reference = value_type &;
        using const_reference = const value_type &;
        using allocator_type = AllocatorT;

        buffer(const range<dimensions> &bufferRange,
               const property_list &propList = {});
        buffer(const range<dimensions> &bufferRange, AllocatorT allocator,
               const property_list &propList = {});
        buffer(T *hostData, const range<dimensions> &bufferRange,
               const property_list &propList = {});
    }
}
```


4.7. DATA ACCESS AND STORAGE IN SYCL

```cpp
buffer<T *hostData, const range<dimensions> &bufferRange, 
    AllocatorT allocator, const property_list &propList = {});

buffer(const T *hostData, const range<dimensions> &bufferRange, 
    const property_list &propList = {});

buffer(const T *hostData, const range<dimensions> &bufferRange, 
    AllocatorT allocator, const property_list &propList = {});

buffer(const std::shared_ptr<T> &hostData, 
    const range<dimensions> &bufferRange, AllocatorT allocator, 
    const property_list &propList = {});

buffer(const std::shared_ptr<T> &hostData, 
    const range<dimensions> &bufferRange, 
    const property_list &propList = {});

template <class InputIterator>
buffer<T, 1>(InputIterator first, InputIterator last, AllocatorT allocator, 
    const property_list &propList = {});

template <class InputIterator>
buffer<T, 1>(InputIterator first, InputIterator last, 
    const property_list &propList = {});

buffer(buffer<T, dimensions, AllocatorT> b, const id<dimensions> &baseIndex, 
    const range<dimensions> &subRange);

/* -- common interface members -- */

/* -- property interface members -- */

range<dimensions> get_range() const;

size_t get_count() const;

size_t get_size() const;

AllocatorT get_allocator() const;

template <access::mode mode, access::target target = access::target::global_buffer>
accessor<T, dimensions, mode, target> get_access(
    handler &commandGroupHandler);

template <access::mode mode>
accessor<T, dimensions, mode, access::target::host_buffer> get_access();

template <access::mode mode, access::target target = access::target::global_buffer>
accessor<T, dimensions, mode, target> get_access(
    handler &commandGroupHandler, range<dimensions> accessRange, 
    id<dimensions> accessOffset = {});

template <access::mode mode>
accessor<T, dimensions, mode, access::target::host_buffer> get_access(
    range<dimensions> accessRange, id<dimensions> accessOffset = {});
```
template<typename... Ts>
auto get_access(Ts...);

template<typename... Ts>
auto get_host_access(Ts...);

template <typename Destination = std::nullptr_t>
void set_final_data(Destination finalData = nullptr);

bool is_sub_buffer() const;

template <typename ReinterpretT, int ReinterpretDim>
buffer<ReinterpretT, ReinterpretDim, AllocatorT>
reinterpret(range<ReinterpretDim> reinterpretRange) const;

// Only available when ReinterpretDim == 1
// or when (ReinterpretDim == dimensions) &&
// (sizeof(ReinterpretT) == sizeof(T))
template <typename ReinterpretT, int ReinterpretDim = dimensions>
buffer<ReinterpretT, ReinterpretDim, AllocatorT>
reinterpret() const;

// Deduction guides
// AUTOMATIC: reinterpreting with the same dimensions.
// The following template instantiations are deduced automatically:

template <class InputIterator, class AllocatorT>
buffer(InputIterator, InputIterator, AllocatorT, const property_list & = {})
-> buffer<typename std::iterator_traits<InputIterator>::value_type, 1,
AllocatorT>;

template <class InputIterator>
buffer(InputIterator, InputIterator, const property_list & = {})
-> buffer<typename std::iterator_traits<InputIterator>::value_type, 1>;

template <class T, int dimensions, class AllocatorT>
buffer(const T *, const range<dimensions> &, AllocatorT,
const property_list & = {})
-> buffer<T, dimensions, AllocatorT>;

buffer(const T *, const range<dimensions> &,
const property_list & = {})
-> buffer<T, dimensions>;

} // namespace sycl
### Constructors of the `buffer` class

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>buffer(const range&lt;dimensions&gt; &amp; bufferRange, const property_list &amp; propList = { })</code></td>
<td>Construct a SYCL buffer instance with uninitialized memory. The constructed SYCL buffer will use a default constructed AllocatorT when allocating memory on the host. The range of the constructed SYCL buffer is specified by the bufferRange parameter provided. Data is not written back to the host on destruction of the buffer unless the buffer has a valid non-null pointer specified via the member function <code>set_final_data()</code>. Zero or more properties can be provided to the constructed SYCL buffer via an instance of <code>property_list</code>.</td>
</tr>
<tr>
<td><code>buffer(const range&lt;dimensions&gt; &amp; bufferRange, AllocatorT allocator, const property_list &amp; propList = { })</code></td>
<td>Construct a SYCL buffer instance with uninitialized memory. The constructed SYCL buffer will use the allocator parameter provided when allocating memory on the host. The range of the constructed SYCL buffer is specified by the bufferRange parameter provided. Data is not written back to the host on destruction of the buffer unless the buffer has a valid non-null pointer specified via the member function <code>set_final_data()</code>. Zero or more properties can be provided to the constructed SYCL buffer via an instance of <code>property_list</code>.</td>
</tr>
<tr>
<td><code>buffer(T* hostData, const range&lt;dimensions&gt; &amp; bufferRange, const property_list &amp; propList = { })</code></td>
<td>Construct a SYCL buffer instance with the hostData parameter provided. The buffer is initialized with the memory specified by hostData. The ownership of this memory is given to the constructed SYCL buffer for the duration of its lifetime. The constructed SYCL buffer will use a default constructed AllocatorT when allocating memory on the host. The range of the constructed SYCL buffer is specified by the bufferRange parameter provided. Zero or more properties can be provided to the constructed SYCL buffer via an instance of <code>property_list</code>.</td>
</tr>
</tbody>
</table>

Continued on next page
### Constructor Description

<table>
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<tr>
<td><code>buffer(T* hostData, const range&lt;dimensions&gt; &amp; bufferRange, AllocatorT allocator, const property_list &amp;propList = {})</code></td>
<td>Construct a SYCL buffer instance with the hostData parameter provided. The buffer is initialized with the memory specified by hostData. The ownership of this memory is given to the constructed SYCL buffer for the duration of its lifetime. The constructed SYCL buffer will use the allocator parameter provided when allocating memory on the host. The range of the constructed SYCL buffer is specified by the bufferRange parameter provided. Zero or more properties can be provided to the constructed SYCL buffer via an instance of property_list.</td>
</tr>
<tr>
<td><code>buffer(const T* hostData, const range&lt;dimensions&gt; &amp; bufferRange, const property_list &amp;propList = {})</code></td>
<td>Construct a SYCL buffer instance with the hostData parameter provided. The ownership of this memory is given to the constructed SYCL buffer for the duration of its lifetime. The constructed SYCL buffer will use a default constructed AllocatorT when allocating memory on the host. The host address is const T, so the host accesses can be read-only. However, the typename T is not const so the device accesses can be both read and write accesses. Since the hostData is const, this buffer is only initialized with this memory and there is no write back after its destruction, unless the buffer has another valid non-null final data address specified via the member function set_final_data() after construction of the buffer. The range of the constructed SYCL buffer is specified by the bufferRange parameter provided. Zero or more properties can be provided to the constructed SYCL buffer via an instance of property_list.</td>
</tr>
</tbody>
</table>

Table 4.31: Constructors of the buffer class.
### Constructor

<table>
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<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>buffer(const T* hostData, const range&lt;dimensions&gt; &amp; bufferRange, AllocatorT allocator, const property_list &amp;propList = {}}</code></td>
<td>Construct a SYCL buffer instance with the hostData parameter provided. The ownership of this memory is given to the constructed SYCL buffer for the duration of its lifetime. The constructed SYCL buffer will use the allocator parameter provided when allocating memory on the host. The host address is <code>const T</code>, so the host accesses can be read-only. However, the typename T is not const so the device accesses can be both read and write accesses. Since, the hostData is const, this buffer is only initialized with this memory and there is no write back after its destruction, unless the buffer has another valid non-null final data address specified via the member function <code>set_final_data()</code> after construction of the buffer. The range of the constructed SYCL buffer is specified by the bufferRange parameter provided. Zero or more properties can be provided to the constructed SYCL buffer via an instance of property_list.</td>
</tr>
<tr>
<td><code>buffer(const std::shared_ptr&lt;T&gt; &amp;hostData, const range&lt;dimensions&gt; &amp; bufferRange, const property_list &amp;propList = {}}</code></td>
<td>Construct a SYCL buffer instance with the hostData parameter provided. The ownership of this memory is given to the constructed SYCL buffer for the duration of its lifetime. The constructed SYCL buffer will use a default constructed AllocatorT when allocating memory on the host. The range of the constructed SYCL buffer is specified by the bufferRange parameter provided. Zero or more properties can be provided to the constructed SYCL buffer via an instance of property_list.</td>
</tr>
</tbody>
</table>

Table 4.31: Constructors of the `buffer` class.

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**Continued on next page**
### Constructor Description

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>buffer(const std::shared_ptr&lt;void&gt; &amp;hostData, const range&lt;dimensions&gt; &amp; bufferRange, AllocatorT allocator, const property_list &amp;propList = {})</code></td>
<td>Construct a SYCL buffer instance with the hostData parameter provided. The ownership of this memory is given to the constructed SYCL buffer for the duration of its lifetime. The constructed SYCL buffer will use the allocator parameter provided when allocating memory on the host. The range of the constructed SYCL buffer is specified by the bufferRange parameter provided. Zero or more properties can be provided to the constructed SYCL buffer via an instance of property_list.</td>
</tr>
<tr>
<td><code>template &lt;typename InputIterator&gt; buffer(InputIterator first, InputIterator last, const property_list &amp;propList = {})</code></td>
<td>Create a new allocated 1D buffer initialized from the given elements ranging from first up to one before last. The data is copied to an intermediate memory position by the runtime. Data is not written back to the same iterator set provided. However, if the buffer has a valid non-const iterator specified via the member function set_final_data(), data will be copied back to that iterator. The constructed SYCL buffer will use a default constructed AllocatorT when allocating memory on the host. Zero or more properties can be provided to the constructed SYCL buffer via an instance of property_list.</td>
</tr>
<tr>
<td><code>template &lt;typename InputIterator&gt; buffer(InputIterator first, InputIterator last, AllocatorT allocator = {}, const property_list &amp;propList = {})</code></td>
<td>Create a new allocated 1D buffer initialized from the given elements ranging from first up to one before last. The data is copied to an intermediate memory position by the runtime. Data is not written back to the same iterator set provided. However, if the buffer has a valid non-const iterator specified via the member function set_final_data(), data will be copied back to that iterator. The constructed SYCL buffer will use the allocator parameter provided when allocating memory on the host. Zero or more properties can be provided to the constructed SYCL buffer via an instance of property_list.</td>
</tr>
</tbody>
</table>

Table 4.31: Constructors of the buffer class.
4.7. DATA ACCESS AND STORAGE IN SYCL

**Constructor**

```
buffer(buffer<T, dimensions, AllocatorT> &b,
const id<dimensions> & baseIndex,
const range<dimensions> & subRange)
```

**Description**

Create a new sub-buffer without allocation to have separate accessors later. `b` is the buffer with the real data, which must not be a sub-buffer. `baseIndex` specifies the origin of the sub-buffer inside the buffer `b`. `subRange` specifies the size of the sub-buffer. The sum of `baseIndex` and `subRange` in any dimension must not exceed the parent buffer (`b`) size (`bufferRange`) in that dimension, and an **exception** with the errc::invalid_object_error error code must be thrown if violated.

The offset and range specified by `baseIndex` and `subRange` together must represent a contiguous region of the original SYCL buffer. If a non-contiguous region of a buffer is requested when constructing a sub-buffer, then an **exception** with the errc::invalid_object_error error code must be thrown.

The origin (based on `baseIndex`) of the sub-buffer being constructed must be a multiple of the memory base address alignment of each SYCL device that is executed on, otherwise the SYCL runtime must throw an asynchronous **exception** with the errc::invalid_object_error error code. This value is retrievable via the SYCL device class info query `info::device::mem_base_addr_align` and must throw **exception** with the errc::invalid_object_error error code if `b` is already a sub-buffer.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>buffer(buffer&lt;T, dimensions, AllocatorT&gt; &amp;b, const id&lt;dimensions&gt; &amp; baseIndex, const range&lt;dimensions&gt; &amp; subRange)</code></td>
<td>Create a new sub-buffer without allocation to have separate accessors later. <code>b</code> is the buffer with the real data, which must not be a sub-buffer. <code>baseIndex</code> specifies the origin of the sub-buffer inside the buffer <code>b</code>. <code>subRange</code> specifies the size of the sub-buffer. The sum of <code>baseIndex</code> and <code>subRange</code> in any dimension must not exceed the parent buffer (<code>b</code>) size (<code>bufferRange</code>) in that dimension, and an <strong>exception</strong> with the errc::invalid_object_error error code must be thrown if violated.</td>
</tr>
</tbody>
</table>

The offset and range specified by `baseIndex` and `subRange` together must represent a contiguous region of the original SYCL buffer. If a non-contiguous region of a buffer is requested when constructing a sub-buffer, then an **exception** with the errc::invalid_object_error error code must be thrown.

The origin (based on `baseIndex`) of the sub-buffer being constructed must be a multiple of the memory base address alignment of each SYCL device that is executed on, otherwise the SYCL runtime must throw an asynchronous **exception** with the errc::invalid_object_error error code. This value is retrievable via the SYCL device class info query `info::device::mem_base_addr_align` and must throw **exception** with the errc::invalid_object_error error code if `b` is already a sub-buffer.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>range&lt;dimensions&gt; get_range() const</code></td>
<td>Return a range object representing the size of the buffer in terms of number of elements in each dimension as passed to the constructor.</td>
</tr>
<tr>
<td><code>size_t get_count() const</code></td>
<td>Returns the total number of elements in the buffer. Equal to <code>get_range()[0] * ... * get_range()[dimensions-1]</code>.</td>
</tr>
<tr>
<td><code>size_t get_size() const</code></td>
<td>Returns the size of the buffer storage in bytes. Equal to <code>get_count() * sizeof(T)</code>.</td>
</tr>
</tbody>
</table>

Table 4.31: Constructors of the `buffer` class.

Table 4.32: Member functions for the `buffer` class.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>AllocatorT get_allocator() const</td>
<td>Returns the allocator provided to the buffer.</td>
</tr>
<tr>
<td>template&lt;access::mode mode, access::target target = access::target::global_buffer&gt; accessor&lt;T, dimensions, mode, target&gt; get_access(handler &amp;commandGroupHandler)</td>
<td>Returns a valid accessor to the buffer with the specified access mode and target in the command group buffer. The value of target can be access::target::global_buffer or access::constant_buffer.</td>
</tr>
<tr>
<td>template&lt;access::mode mode&gt; accessor&lt;T, dimensions, mode, access::target::host_buffer&gt; get_access()</td>
<td>Returns a valid host accessor to the buffer with the specified access mode and target.</td>
</tr>
<tr>
<td>template&lt;access::mode mode, access::target target = access::target::global_buffer&gt; accessor&lt;T, dimensions, mode, target&gt; get_access(handler &amp;commandGroupHandler, range&lt;dimensions&gt; accessRange, id&lt;dimensions&gt; accessOffset = {})</td>
<td>Returns a valid accessor to the buffer with the specified access mode and target in the command group buffer. Only the values starting from the given offset and up to the given range are guaranteed to be updated. The value of target can be access::target::global_buffer or access::constant_buffer.</td>
</tr>
<tr>
<td>template&lt;access::mode mode&gt; accessor&lt;T, dimensions, mode, access::target::host_buffer&gt; get_access(range&lt;dimensions&gt; accessRange, id&lt;dimensions&gt; accessOffset = {})</td>
<td>Returns a valid host accessor to the buffer with the specified access mode and target. Only the values starting from the given offset and up to the given range are guaranteed to be updated. The value of target can only be access::target::host_buffer.</td>
</tr>
<tr>
<td>template&lt;typename... Ts&gt; auto get_access(Ts... args)</td>
<td>Returns a valid accessor as if constructed via passing the buffer and all provided arguments to the SYCL accessor. Possible implementation: return accessor { *this, args... };</td>
</tr>
<tr>
<td>template&lt;typename... Ts&gt; auto get_host_access(Ts... args)</td>
<td>Returns a valid host accessor as if constructed via passing the buffer and all provided arguments to the SYCL host_accessor. Possible implementation: return host_accessor { *this, args... };</td>
</tr>
</tbody>
</table>

Table 4.32: Member functions for the buffer class.
### Table 4.32: Member functions for the `buffer` class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename Destination = std::nullptr_t&gt; void set_final_data(Destination finalData = nullptr)</code></td>
<td>The <code>finalData</code> points to where the outcome of all the buffer processing is going to be copied to at destruction time, if the buffer was involved with a write accessor. Destination can be either an output iterator or a <code>std::weak_ptr&lt;T&gt;</code>. Note that a raw pointer is a special case of output iterator and thus defines the host memory to which the result is to be copied. In the case of a weak pointer, the output is not updated if the weak pointer has expired. If <code>Destination</code> is <code>std::nullptr_t</code>, then the copy back will not happen.</td>
</tr>
<tr>
<td><code>void set_write_back(bool flag = true)</code></td>
<td>This member function allows dynamically forcing or canceling the write-back of the data of a buffer on destruction according to the value of <code>flag</code>. Forcing the write-back is similar to what happens during a normal write-back as described in Section 4.7.2.3 and 4.7.4. If there is nowhere to write-back, using this function does not have any effect.</td>
</tr>
<tr>
<td><code>bool is_sub_buffer() const</code></td>
<td>Returns true if this SYCL buffer is a sub-buffer, otherwise returns false.</td>
</tr>
<tr>
<td><code>template &lt;typename ReinterpretT, int ReinterpretDim&gt; buffer&lt;ReinterpretT, ReinterpretDim, AllocatorT&gt; reinterpret(range&lt;ReinterpretDim&gt; reinterpretRange) const</code></td>
<td>Creates and returns a reinterpreted SYCL buffer with the type specified by <code>ReinterpretT</code>, dimensions specified by <code>ReinterpretDim</code> and range specified by <code>reinterpretRange</code>. The buffer object being reinterpreted can be a SYCL sub-buffer that was created from a SYCL buffer and must throw <code>exception</code> with the <code>errc::invalid_object_error</code> error code if the total size in bytes represented by the type and range of the reinterpreted SYCL buffer (or sub-buffer) does not equal the total size in bytes represented by the type and range of this SYCL buffer (or sub-buffer). Reinterpreting a sub-buffer provides a reinterpreted view of the sub-buffer only, and does not change the offset or size of the sub-buffer view (in bytes) relative to the parent buffer.</td>
</tr>
</tbody>
</table>

Continued on next page
### Member function

```cpp
template <typename ReinterpretT, int ReinterpretDim = dimensions>
buffer<ReinterpretT, ReinterpretDim, AllocatorT>
reinterpret() const
```

**Description**

Creates and returns a reinterpreted SYCL buffer with the type specified by `ReinterpretT` and dimensions specified by `ReinterpretDim`. Only valid when `(ReinterpretDim == 1)` or when `((ReinterpretDim == dimensions) && (sizeof(ReinterpretT) == sizeof(T)))`. The buffer object being reinterpreted can be a SYCL sub-buffer that was created from a SYCL buffer and must throw an exception with the `errc::invalid_object_error` error code if the total size in bytes represented by the type and range of the reinterpreted SYCL buffer (or sub-buffer) does not equal the total size in bytes represented by the type and range of this SYCL buffer (or sub-buffer). Reinterpreting a sub-buffer provides a reinterpreted view of the sub-buffer only, and does not change the offset or size of the sub-buffer view (in bytes) relative to the parent buffer.

End of table

Table 4.32: Member functions for the `buffer` class.

### 4.7.2.2 Buffer properties

The properties that can be provided when constructing the SYCL `buffer` class are described in Table 4.33.

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>property::buffer::use_host_ptr</code></td>
<td>The <code>use_host_ptr</code> property adds the requirement that the SYCL runtime must not allocate any memory for the SYCL buffer and instead uses the provided host pointer directly. This prevents the SYCL runtime from allocating additional temporary storage on the host.</td>
</tr>
<tr>
<td><code>property::buffer::use_mutex</code></td>
<td>The <code>use_mutex</code> property is valid for the SYCL <code>buffer</code>, <code>unsampled_image</code> and <code>sampled_image</code> classes. The property adds the requirement that the memory which is owned by the SYCL buffer can be shared with the application via a <code>std::mutex</code> provided to the property. The mutex <code>m</code> is locked by the runtime whenever the data is in use and unlocked otherwise. Data is synchronized with <code>hostData</code>, when the mutex is unlocked by the runtime.</td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.33: Properties supported by the SYCL `buffer` class.
The context_bound property adds the requirement that the SYCL buffer can only be associated with a single SYCL context that is provided to the property.

Table 4.33: Properties supported by the SYCL buffer class.

The constructors and special member functions of the buffer property classes are listed in Tables 4.34 and 4.35 respectively.

Table 4.34: Constructors of the buffer property classes.

Buffers are reference-counted. When a buffer value is constructed from another buffer, the two values reference the same buffer and a reference count is incremented. When a buffer value is destroyed, the reference count is decremented. Only when there are no more buffer values that reference a specific buffer is the actual buffer destroyed and the buffer destruction behavior defined below is followed.

If any error occurs on buffer destruction, it is reported via the associated queue’s asynchronous error handling mechanism.

The basic rule for the blocking behavior of a buffer destructor is that it blocks if there is some data to write back because a write accessor on it has been created, or if the buffer was constructed with attached host memory and is still in use.

4.7.2.3 Buffer synchronization rules

Buffers are reference-counted. When a buffer value is constructed from another buffer, the two values reference the same buffer and a reference count is incremented. When a buffer value is destroyed, the reference count is decremented. Only when there are no more buffer values that reference a specific buffer is the actual buffer destroyed and the buffer destruction behavior defined below is followed.

If any error occurs on buffer destruction, it is reported via the associated queue’s asynchronous error handling mechanism.

The basic rule for the blocking behavior of a buffer destructor is that it blocks if there is some data to write back because a write accessor on it has been created, or if the buffer was constructed with attached host memory and is still in use.
More precisely:

1. A buffer can be constructed with just a size and using the default buffer allocator. The memory management for this type of buffer is entirely handled by the SYCL system. The destructor for this type of buffer does not need to block, even if work on the buffer has not completed. Instead, the SYCL system frees any storage required for the buffer asynchronously when it is no longer in use in queues. The initial contents of the buffer are unspecified.

2. A buffer can be constructed with associated host memory and a default buffer allocator. The buffer will use this host memory for its full lifetime, but the contents of this host memory are unspecified for the lifetime of the buffer. If the host memory is modified by the host, or mapped to another buffer or image during the lifetime of this buffer, then the results are undefined. The initial contents of the buffer will be the contents of the host memory at the time of construction.

When the buffer is destroyed, the destructor will block until all work in queues on the buffer have completed, then copy the contents of the buffer back to the host memory (if required) and then return.

(a) If the type of the host data is `const`, then the buffer is read-only; only read accessors are allowed on the buffer and no-copy-back to host memory is performed (although the host memory must still be kept available for use by SYCL). When using the default buffer allocator, the const-ness of the type will be removed in order to allow host allocation of memory, which will allow temporary host copies of the data by the SYCL runtime, for example for speeding up host accesses.

When the buffer is destroyed, the destructor will block until all work in queues on the buffer have completed and then return, as there is no copy of data back to host.

(b) If the type of the host data is not `const` but the pointer to host data is `const`, then the read-only restriction applies only on host and not on device accesses.

When the buffer is destroyed, the destructor will block until all work in queues on the buffer have completed.

3. A buffer can be constructed using a `shared_ptr` to host data. This pointer is shared between the SYCL application and the runtime. In order to allow synchronization between the application and the runtime a mutex is used which will be locked by the runtime whenever the data is in use, and unlocked when it is no longer needed.

The `shared_ptr` reference counting is used in order to prevent destroying the buffer host data prematurely. If the `shared_ptr` is deleted from the user application before buffer destruction, the buffer can continue securely because the pointer hasn’t been destroyed yet. It will not copy data back to the host before destruction, however, as the application side has already deleted its copy.

Note that since there is an implicit conversion of a `std::unique_ptr` to a `std::shared_ptr`, a `std::unique_ptr` can also be used to pass the ownership to the SYCL runtime.

4. A buffer can be constructed from a pair of iterator values. In this case, the buffer construction will copy the data from the data range defined by the iterator pair. The destructor will not copy back any data and does not need to block.

If `set_final_data()` is used to change where to write the data back to, then the destructor of the buffer will block if a write accessor on it has been created.

A sub-buffer object can be created which is a sub-range reference to a base buffer. This sub-buffer can be used
to create accessors to the base buffer, which have access to the range specified at time of construction of the
sub-buffer. Sub-buffers cannot be created from sub-buffers, but only from a base buffer which is not already a
sub-buffer.

Sub-buffers must be constructed from a contiguous region of memory in a buffer. This requirement is potentially
non-intuitive when working with buffers that have dimensionality larger than one, but maps to one-dimensional
SYCL backend native allocations without performance cost due to index mapping computation. For example:

```cpp
// Example of creating a sub-buffer from a base buffer
buffer<int, 2> parent_buffer { range<2>{ 8, 8 } }; // Create 2-d buffer with 8x8 ints
buffer<int, 2> sub_buf1 { parent_buffer, /*offset*/ range<2>{ 2, 0 }, /*size*/ range<2>{ 2, 8 } }; // OK: Contiguous region from middle of buffer
buffer<int, 2> sub_buf2 { parent_buffer, /*offset*/ range<2>{ 2, 0 }, /*size*/ range<2>{ 2, 2 } }; // invalid_object_error exception: Non-contiguous regions of 2-d buffer
buffer<int, 2> sub_buf3 { parent_buffer, /*offset*/ range<2>{ 2, 2 }, /*size*/ range<2>{ 2, 6 } }; // invalid_object_error exception: Out-of-bounds size
```

### 4.7.3 Images

The classes `unsampled_image` (Table 4.36) and `sampled_image` (Table 4.38) define shared image data of one, two
or three dimensions, that can be used by kernels in queues and have to be accessed using accessor classes with
image accessor modes.

The constructors and member functions of the SYCL `unsampled_image` and `sampled_image` class templates are
listed in Tables 4.36, 4.37, 4.38 and 4.39, respectively. The additional common special member functions and
common member functions are listed in Tables 4.1 and 4.2, respectively.

Where relevant, it is the responsibility of the user to ensure that the format of the data matches the format described
by `image_format`.

The allocator template parameter of the SYCL `unsampled_image` and `sampled_image` classes can be any allocator
type including a custom allocator, however it must allocate in units of `std::byte`.

For any image that is constructed with the range \((r1, r2, r3)\) with an element type size in bytes of \(s\), the image row
pitch and image slice pitch should be calculated as follows:

\[
\begin{align*}
    r1 \cdot s & \quad (4.1) \\
    r1 \cdot r2 \cdot s & \quad (4.2)
\end{align*}
\]

The SYCL `unsampled_image` and `sampled_image` class templates provide the common reference semantics (see
Section 4.5.3).

### 4.7.3.1 Unsampler image interface

Each constructor of the `unsampled_image` takes an `image_format` to describe the data layout of the image data.
Each constructor additionally takes as the last parameter an optional SYCL property_list to provide properties to the SYCL unsampled_image.

The SYCL unsampled_image class template takes a template parameter AllocatorT for specifying an allocator which is used by the SYCL runtime when allocating temporary memory on the host. If no template argument is provided, the default allocator for the SYCL unsampled_image class image_allocator is used (see Section 4.7.1.1).

```cpp
namespace sycl {
enum class image_format : unsigned int {
    r8g8b8a8_unorm,
    r16g16b16a16_unorm,
    r8g8b8a8_sint,
    r16g16b16a16_sint,
    r32b32g32a32_sint,
    r8g8b8a8_uint,
    r16g16b16a16_uint,
    r32b32g32a32_uint,
    r16b16g16a16_sfloat,
    r32g32b32a32_sfloat,
    b8g8r8a8_unorm,
};

template <int dimensions = 1, typename AllocatorT = sycl::image_allocator>
class unsampled_image {
public:
    unsampled_image(image_format format, const range<dimensions> &rangeRef,
        const property_list &propList = {});

    unsampled_image(image_format format, const range<dimensions> &rangeRef,
        AllocatorT allocator, const property_list &propList = {});

    /* Available only when: dimensions > 1 */
    unsampled_image(image_format format, const range<dimensions> &rangeRef,
        const range<dimensions -1> &pitch,
        const property_list &propList = {});

    unsampled_image(void *hostPointer, image_format format,
        const range<dimensions> &rangeRef,
        const property_list &propList = {});

    unsampled_image(void *hostPointer, image_format format,
        const range<dimensions> &rangeRef, AllocatorT allocator,
        const property_list &propList = {});

    /* Available only when: dimensions > 1 */
    unsampled_image(void *hostPointer, image_format format,
        const range<dimensions> &rangeRef,
        const range<dimensions -1> &pitch,
```
4.7. DATA ACCESS AND STORAGE IN SYCL

const property_list &propList = {};

/* Available only when: dimensions > 1 */
unsampled_image(void *hostPointer, image_format format,
const range<dimensions> &rangeRef,
const range<dimensions -1> &pitch, AllocatorT allocator,
const property_list &propList = {});

unsampled_image(std::shared_ptr<void> &hostPointer, image_format format,
const range<dimensions> &rangeRef,
const property_list &propList = {});

unsampled_image(std::shared_ptr<void> &hostPointer, image_format format,
const range<dimensions> &rangeRef, AllocatorT allocator,
const property_list &propList = {});

/* Available only when: dimensions > 1 */
unsampled_image(std::shared_ptr<void> &hostPointer, image_format format,
const range<dimensions> &rangeRef,
const range<dimensions -1> &pitch,
const property_list &propList = {});

/* Available only when: dimensions > 1 */
unsampled_image(std::shared_ptr<void> &hostPointer, image_format format,
const range<dimensions> &rangeRef, AllocatorT allocator,
const property_list &propList = {});

/* -- common interface members -- */

/* -- property interface members -- */
range<dimensions> get_range() const;

/* Available only when: dimensions > 1 */
range<dimensions - 1> get_pitch() const;

size_t get_count() const;

size_t get_size() const;

AllocatorT get_allocator() const;

template <typename dataT, access::mode accessMode>
accessor<dataT, dimensions, accessMode, access::target::unsampled_image>
get_access(handler & commandGroupHandler);

template <typename dataT, access::mode accessMode>
accessor<dataT, dimensions, accessMode, access::target::host_unsampled_image>
get_access();

template <typename Destination = std::nullptr_t>
void set_final_data(Destination finalData = std::nullptr);

void set_write_back(bool flag = true);
```cpp
};
} // namespace sycl
```

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>`unsampled_image(image_format format, const range&lt;dimensions&gt; &amp;rangeRef,</td>
<td>Construct a SYCL <code>unsampled_image</code> instance with uninitialized memory. The constructed SYCL <code>unsampled_image</code> will use a default constructed <code>AllocatorT</code> when allocating memory on the host. The element size of the constructed SYCL <code>unsampled_image</code> will be derived from the <code>format</code> parameter. The range of the constructed SYCL <code>unsampled_image</code> is specified by the <code>rangeRef</code> parameter provided. The pitch of the constructed SYCL <code>unsampled_image</code> will be the default size determined by the SYCL runtime. Unless the member function <code>set_final_data()</code> is called with a valid non-null pointer, there will be no write back on destruction. Zero or more properties can be provided to the constructed SYCL <code>unsampled_image</code> via an instance of <code>property_list</code>.</td>
</tr>
<tr>
<td>const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`unsampled_image(image_format format, const range&lt;dimensions&gt; &amp;rangeRef,</td>
<td>Construct a SYCL <code>unsampled_image</code> instance with uninitialized memory. The constructed SYCL <code>unsampled_image</code> will use the <code>allocator</code> parameter provided when allocating memory on the host. The element size of the constructed SYCL <code>unsampled_image</code> will be derived from the <code>format</code> parameter. The range of the constructed SYCL <code>unsampled_image</code> is specified by the <code>rangeRef</code> parameter provided. The pitch of the constructed SYCL <code>unsampled_image</code> will be the default size determined by the SYCL runtime. Unless the member function <code>set_final_data()</code> is called with a valid non-null pointer, there will be no write back on destruction. Zero or more properties can be provided to the constructed SYCL <code>unsampled_image</code> via an instance of <code>property_list</code>.</td>
</tr>
<tr>
<td>AllocatorT allocator,</td>
<td></td>
</tr>
<tr>
<td>const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Table 4.36: Constructors of the <code>unsampled_image</code> class template.</td>
</tr>
</tbody>
</table>
### Constructor Description

```
unsampled_image(image_format format,  
const range<dimensions> &rangeRef,  
const range<dimensions-1> &pitch,  
const property_list &propList = {})
```

Available only when: dimensions > 1. Construct a SYCL `unsampled_image` instance with uninitialized memory. The constructed SYCL `unsampled_image` will use a default constructed `AllocatorT` when allocating memory on the host. The element size of the constructed SYCL `unsampled_image` will be derived from the `format` parameter. The range of the constructed SYCL `unsampled_image` is specified by the `rangeRef` parameter provided. The pitch of the constructed SYCL `unsampled_image` will be the `pitch` parameter provided. Unless the member function `set_final_data()` is called with a valid non-null pointer, there will be no write back on destruction. Zero or more properties can be provided to the constructed SYCL `unsampled_image` via an instance of `property_list`.

```
unsampled_image(image_format format,  
const range<dimensions> &rangeRef,  
const range<dimensions-1> &pitch,  
AllocatorT allocator,  
const property_list &propList = {})
```

Available only when: dimensions > 1. Construct a SYCL `unsampled_image` instance with uninitialized memory. The constructed SYCL `unsampled_image` will use the `allocator` parameter provided when allocating memory on the host. The element size of the constructed SYCL `unsampled_image` will be derived from the `format` parameter. The range of the constructed SYCL `unsampled_image` is specified by the `rangeRef` parameter provided. The pitch of the constructed SYCL `unsampled_image` will be the `pitch` parameter provided. Unless the member function `set_final_data()` is called with a valid non-null pointer, there will be no write back on destruction. Zero or more properties can be provided to the constructed SYCL `unsampled_image` via an instance of `property_list`.

Table 4.36: Constructors of the `unsampled_image` class template.
**Table 4.36:** Constructors of the `unsampled_image` class template.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>unsampled_image(void *hostPointer, image_format format, const range&lt;dimensions&gt; &amp;rangeRef, const property_list &amp;propList = {})</code></td>
<td>Construct a SYCL <code>unsampled_image</code> instance with the <code>hostPointer</code> parameter provided. The ownership of this memory is given to the constructed SYCL <code>unsampled_image</code> for the duration of its lifetime. The constructed SYCL <code>unsampled_image</code> will use a default constructed <code>AllocatorT</code> when allocating memory on the host. The element size of the constructed SYCL <code>unsampled_image</code> will be derived from the <code>format</code> parameter. The range of the constructed SYCL <code>unsampled_image</code> is specified by the <code>rangeRef</code> parameter provided. The pitch of the constructed SYCL <code>unsampled_image</code> will be the default size determined by the SYCL runtime. Unless the member function <code>set_final_data()</code> is called with a valid non-null pointer, any memory allocated by the SYCL runtime is written back to <code>hostPointer</code>. Zero or more properties can be provided to the constructed SYCL <code>unsampled_image</code> via an instance of <code>property_list</code>.</td>
</tr>
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</table>
Table 4.36: Constructors of the `unsampled_image` class template.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
</table>
| `unsampled_image(void *hostPointer,             | Available only when: `dimensions > 1`
| image_format format,                            | Construct a SYCL `unsampled_image` instance with the `hostPointer` parameter provided. The ownership of this memory is given to the constructed SYCL `unsampled_image` for the duration of its lifetime. The constructed SYCL `unsampled_image` will use a default constructed `AllocatorT` when allocating memory on the host. The element size of the constructed SYCL `unsampled_image` will be derived from the `format` parameter. The range of the constructed SYCL `unsampled_image` is specified by the `rangeRef` parameter provided. The pitch of the constructed SYCL `unsampled_image` will be the `pitch` parameter provided. Unless the member function `set_final_data()` is called with a valid non-null pointer, any memory allocated by the SYCL runtime is written back to `hostPointer`. Zero or more properties can be provided to the constructed SYCL `unsampled_image` via an instance of `property_list`. |
| const range<dimensions> &rangeRef,              |             |
| const range<dimensions-1> &pitch,               |             |
| const property_list &propList = {}              |             |

| `unsampled_image(void *hostPointer,             | Available only when: `dimensions > 1`
| image_format format,                            | Construct a SYCL `unsampled_image` instance with the `hostPointer` parameter provided. The ownership of this memory is given to the constructed SYCL `unsampled_image` for the duration of its lifetime. The constructed SYCL `unsampled_image` will use the `allocator` parameter provided when allocating memory on the host. The element size of the constructed SYCL `unsampled_image` will be derived from the `format` parameter. The range of the constructed SYCL `unsampled_image` is specified by the `rangeRef` parameter provided. The pitch of the constructed SYCL `unsampled_image` will be the `pitch` parameter provided. Unless the member function `set_final_data()` is called with a valid non-null pointer, any memory allocated by the SYCL runtime is written back to `hostPointer`. Zero or more properties can be provided to the constructed SYCL `unsampled_image` via an instance of `property_list`. |
| const range<dimensions> &rangeRef,              |             |
| const range<dimensions-1> &pitch,               |             |
| AllocatorT allocator,                            |             |
| const property_list &propList = {}              |             |
**Table 4.36: Constructors of the `unsampled_image` class template.**

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
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<tbody>
<tr>
<td><code>unsampled_image(std::shared_ptr&lt;void&gt;&amp; hostPointer, image_format format, const range&lt;dimensions&gt;&amp; rangeRef, const property_list &amp;propList = {})</code></td>
<td>Construct a SYCL <code>unsampled_image</code> instance with the <code>hostPointer</code> parameter provided. The ownership of this memory is given to the constructed SYCL <code>unsampled_image</code> for the duration of its lifetime. The constructed SYCL <code>unsampled_image</code> will use a default constructed <code>AllocatorT</code> when allocating memory on the host. The element size of the constructed SYCL <code>unsampled_image</code> will be derived from the <code>format</code> parameter. The range of the constructed SYCL <code>unsampled_image</code> is specified by the <code>rangeRef</code> parameter provided. The pitch of the constructed SYCL <code>unsampled_image</code> will be the default size determined by the SYCL runtime. Unless the member function <code>set_final_data()</code> is called with a valid non-null pointer, any memory allocated by the SYCL runtime is written back to <code>hostPointer</code>. Zero or more properties can be provided to the constructed SYCL <code>unsampled_image</code> via an instance of <code>property_list</code>.</td>
</tr>
</tbody>
</table>

Continued on next page
### Constructors of the `unsampled_image` class template

<table>
<thead>
<tr>
<th>Constructor</th>
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<tbody>
<tr>
<td><code>unsampled_image(std::shared_ptr&lt;void&gt;&amp; hostPointer, image_format format, const range&lt;dimensions&gt;&amp; rangeRef, const range&lt;dimensions-1&gt;&amp; pitch, const property_list&amp; propList = {})</code></td>
</tr>
<tr>
<td>Construct a SYCL <code>unsampled_image</code> instance with the <code>hostPointer</code> parameter provided. The ownership of this memory is given to the constructed SYCL <code>unsampled_image</code> for the duration of its lifetime. The constructed SYCL <code>unsampled_image</code> will use a default constructed <code>AllocatorT</code> when allocating memory on the host. The element size of the constructed SYCL <code>unsampled_image</code> will be derived from the <code>format</code> parameter. The range of the constructed SYCL <code>unsampled_image</code> is specified by the <code>rangeRef</code> parameter provided. The pitch of the constructed SYCL <code>unsampled_image</code> will be the <code>pitch</code> parameter provided. Unless the member function <code>set_final_data()</code> is called with a valid non-null pointer, any memory allocated by the SYCL runtime is written back to <code>hostPointer</code>. Zero or more properties can be provided to the constructed SYCL <code>unsampled_image</code> via an instance of <code>property_list</code>.</td>
</tr>
</tbody>
</table>

### End of table

Table 4.36: Constructors of the `unsampled_image` class template.
### Table 4.37: Member functions of the `unsampled_image` class template.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>range&lt;dimensions&gt; get_range() const</code></td>
<td>Return a range object representing the size of the image in terms of the number of elements in each dimension as passed to the constructor.</td>
</tr>
<tr>
<td><code>range&lt;dimensions-1&gt; get_pitch() const</code></td>
<td>Available only when: dimensions &gt; 1. Return a range object representing the pitch of the image in bytes.</td>
</tr>
<tr>
<td><code>size_t get_count() const</code></td>
<td>Returns the total number of elements in the image. Equal to <code>get_range()[0] * ... * get_range()[dimensions-1]</code>.</td>
</tr>
<tr>
<td><code>size_t get_size() const</code></td>
<td>Returns the size of the image storage in bytes. The number of bytes may be greater than <code>get_count() * element size</code> due to padding of elements, rows and slices of the image for efficient access.</td>
</tr>
<tr>
<td><code>AllocatorT get_allocator() const</code></td>
<td>Returns the allocator provided to the image.</td>
</tr>
<tr>
<td><code>template&lt;typename dataT, access::mode accessMode&gt; accessor&lt;dataT, dimensions, accessMode, access::target::unsampled_image&gt;</code></td>
<td>Returns a valid accessor to the image with the specified access mode and target. The only valid types for <code>dataT</code> are <code>int4</code>, <code>uint4</code>, <code>float4</code> and <code>half4</code>.</td>
</tr>
<tr>
<td><code>template&lt;typename dataT, access::mode accessMode&gt; accessor&lt;dataT, dimensions, accessMode, access::target::host_unsampled_image&gt;</code></td>
<td>Returns a valid accessor to the image with the specified access mode and target. The only valid types for <code>dataT</code> are <code>int4</code>, <code>uint4</code>, <code>float4</code> and <code>half4</code>.</td>
</tr>
<tr>
<td><code>template &lt;typename Destination = std::nullptr_t&gt; void set_final_data(Destination finalData = nullptr)</code></td>
<td>The <code>finalData</code> point to where the output of all the image processing is going to be copied to at destruction time, if the image was involved with a write accessor. Destination can be either an output iterator, a <code>std::weak_ptr&lt;T&gt;</code>. Note that a raw pointer is a special case of output iterator and thus defines the host memory to which the result is to be copied. In the case of a weak pointer, the output is not copied if the weak pointer has expired. If <code>Destination</code> is <code>std::nullptr_t</code>, then the copy back will not happen.</td>
</tr>
<tr>
<td><code>void set_write_back(bool flag = true)</code></td>
<td>This member function allows dynamically forcing or canceling the write-back of the data of an image on destruction according to the value of <code>flag</code>. Forcing the write-back is similar to what happens during a normal write-back as described in Section 4.7.3.4 and 4.7.4. If there is nowhere to write-back, using this function does not have any effect.</td>
</tr>
</tbody>
</table>
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4.7.3.2 Sampled image interface

Each constructor of the `sampled_image` class requires a pointer to the host data the image will sample, an `image_format` to describe the data layout and an `image_sampler` (Section 4.7.8) to describe how to sample the image data.

Each constructor additionally takes as the last parameter an optional SYCL `property_list` to provide properties to the SYCL `sampled_image`.

```cpp
namespace sycl {
enum class image_format : unsigned int {
    r8g8b8a8_unorm,
    r16g16b16a16_unorm,
    r8g8b8a8_sint,
    r16g16b16a16_sint,
    r32b32g32a32_sint,
    r8g8b8a8_uint,
    r16g16b16a16_uint,
    r32b32g32a32_uint,
    r16b16g16a16_sfloat,
    r32g32b32a32_sfloat,
    b8g8r8a8_unorm,
};

template <int dimensions = 1, typename AllocatorT = sycl::image_allocator>
class sampled_image {
public:
    sampled_image(const void *hostPointer, image_format format,
                   image_sampler sampler, const range<dimensions> &rangeRef,
                   const property_list &propList = {});
    /* Available only when: dimensions > 1 */
    sampled_image(const void *hostPointer, image_format format,
                   image_sampler sampler, const range<dimensions - 1> &pitch,
                   const property_list &propList = {});
    sampled_image(std::shared_ptr<const void> &hostPointer, image_format format,
                   image_sampler sampler, const range<dimensions> &rangeRef,
                   const property_list &propList = {});
    /* Available only when: dimensions > 1 */
    sampled_image(std::shared_ptr<const void> &hostPointer, image_format format,
                   image_sampler sampler, const range<dimensions - 1> &pitch,
                   const property_list &propList = {});

    /* -- common interface members -- */
    range<dimensions> get_range() const;
    /* -- property interface members -- */
    range<dimensions - 1> get_pitch() const;
};
```

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size_t get_count() const;
size_t get_size() const;

template<typename dataT, access::mode accessMode>
accessor<dataT, dimensions, accessMode, access::target::sampled_image>
get_access(handler & commandGroupHandler);

template<typename dataT, access::mode accessMode>
accessor<dataT, dimensions, accessMode, access::target::host_sampled_image>
get_access();

} // namespace sycl

### Constructor

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>sampled_image(const void *hostPointer, image_format format, image_sampler sampler, const range&lt;dimensions&gt; &amp;rangeRef, const property_list &amp;propList = {})</code></td>
<td>Construct a SYCL <code>sampled_image</code> instance with the <code>hostPointer</code> parameter provided. The ownership of this memory is given to the constructed SYCL <code>sampled_image</code> for the duration of its lifetime. The host address is <code>const</code>, so the host accesses must be read-only. Since, the <code>hostPointer</code> is <code>const</code>, this image is only initialized with this memory and there is no write after its destruction. The element size of the constructed SYCL <code>sampled_image</code> will be derived from the <code>format</code> parameter. The sampling member function of the constructed SYCL <code>sampled_image</code> will be derived from the <code>sampler</code> parameter. The range of the constructed SYCL <code>sampled_image</code> is specified by the <code>rangeRef</code> parameter provided. The pitch of the constructed SYCL <code>sampled_image</code> will be the default size determined by the SYCL runtime. Zero or more properties can be provided to the constructed SYCL <code>sampled_image</code> via an instance of <code>property_list</code>.</td>
</tr>
</tbody>
</table>

Table 4.38: Constructors of the `sampled_image` class template.

Continued on next page
4.7. DATA ACCESS AND STORAGE IN SYCL

Constructor | Description |
--- | --- |
`sampled_image(const void *hostPointer, image_format format, image_sampler sampler, const range<dimensions> &rangeRef, const range<dimensions-1> &pitch, const property_list &propList = {})` | Available only when: dimensions > 1. Construct a SYCL sampled_image instance with the hostPointer parameter provided. The ownership of this memory is given to the constructed SYCL sampled_image for the duration of its lifetime. The host address is `const`, so the host accesses must be read-only. Since, the hostPointer is `const`, this image is only initialized with this memory and there is no write after destruction. The element size of the constructed SYCL sampled_image will be derived from the format parameter. The sampling member function of the constructed SYCL sampled_image will be derived from the sampler parameter. The range of the constructed SYCL sampled_image is specified by the rangeRef parameter provided. The pitch of the constructed SYCL sampled_image will be the pitch parameter provided. Zero or more properties can be provided to the constructed SYCL sampled_image via an instance of property_list. |

`sampled_image(std::shared_ptr<const void>& hostPointer, image_format format, image_sampler sampler, const range<dimensions> &rangeRef, const property_list &propList = {})` | Construct a SYCL sampled_image instance with the hostPointer parameter provided. The ownership of this memory is given to the constructed SYCL sampled_image for the duration of its lifetime. The host address is `const`, so the host accesses must be read-only. Since, the hostPointer is `const`, this image is only initialized with this memory and there is no write after its destruction. The element size of the constructed SYCL sampled_image will be derived from the format parameter. The sampling member function of the constructed SYCL sampled_image will be derived from the sampler parameter. The range of the constructed SYCL sampled_image is specified by the rangeRef parameter provided. The pitch of the constructed SYCL sampled_image will be the default size determined by the SYCL runtime. Zero or more properties can be provided to the constructed SYCL sampled_image via an instance of property_list. |

Table 4.38: Constructors of the sampled_image class template.
### Constructor

<table>
<thead>
<tr>
<th>sampled_image(std::shared_ptr&lt;const void&gt;&amp; hostPointer, image_format format, image_sampler sampler, const range&lt;dimensions&gt; &amp;rangeRef, const range&lt;dimensions-1&gt; &amp; pitch, const property_list &amp;propList = {})</th>
</tr>
</thead>
<tbody>
<tr>
<td>Construct a SYCL <code>sampled_image</code> instance with the <code>hostPointer</code> parameter provided. The ownership of this memory is given to the constructed SYCL <code>sampled_image</code> for the duration of its lifetime. The host address is <code>const</code>, so the host accesses can be read-only. Since the <code>hostPointer</code> is <code>const</code>, this image is only initialized with this memory and there is no write after its destruction. The element size of the constructed SYCL <code>sampled_image</code> will be derived from the <code>format</code> parameter. The sampling member function of the constructed SYCL <code>sampled_image</code> will be derived from the <code>sampler</code> parameter. The range of the constructed SYCL <code>sampled_image</code> is specified by the <code>rangeRef</code> parameter provided. The pitch of the constructed SYCL <code>sampled_image</code> will be the <code>pitch</code> parameter provided. Zero or more properties can be provided to the constructed SYCL <code>sampled_image</code> via an instance of <code>property_list</code>.</td>
</tr>
</tbody>
</table>

### Member function

<table>
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<tr>
<th>range&lt;dimensions&gt; get_range()const</th>
</tr>
</thead>
<tbody>
<tr>
<td>Return a range object representing the size of the image in terms of the number of elements in each dimension as passed to the constructor.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>range&lt;dimensions-1&gt; get_pitch()const</th>
</tr>
</thead>
<tbody>
<tr>
<td>Available only when: <code>dimensions &gt; 1</code>. Return a range object representing the pitch of the image in bytes.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>size_t get_count()const</th>
</tr>
</thead>
<tbody>
<tr>
<td>Returns the total number of elements in the image. Equal to <code>get_range()[0] * ... * get_range()[dimensions-1]</code>.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>size_t get_size()const</th>
</tr>
</thead>
<tbody>
<tr>
<td>Returns the size of the image storage in bytes. The number of bytes may be greater than <code>get_count() * element size</code> due to padding of elements, rows and slices of the image for efficient access.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>template&lt;typename dataT, access::mode accessMode&gt; accessor&lt;dataT, dimensions, accessMode, access::target::sampled_image&gt; get_access(handler &amp; commandGroupHandler)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Returns a valid accessor to the image with the specified access mode and target. The only valid types for <code>dataT</code> are <code>int4, uint4, float4</code> and <code>half4</code>.</td>
</tr>
</tbody>
</table>

Table 4.38: Constructors of the `sampled_image` class template.

Table 4.39: Member functions of the `sampled_image` class template.
### 4.7. DATA ACCESS AND STORAGE IN SYCL

#### 4.7.3.3 Image properties

The properties that can be provided when constructing the SYCL `unsampled_image` and `sampled_image` classes are described in Table 4.40.

```cpp
namespace sycl {
    namespace property {
        namespace image {
            class use_host_ptr {
                public:
                use_host_ptr() = default;
            };

            class use_mutex {
                public:
                use_mutex(std::mutex &mutexRef);
                std::mutex *get_mutex_ptr() const;
            };

            class context_bound {
                public:
                context_bound(context boundContext);
                context get_context() const;
            };
        }
    }
}
```

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>property::image::use_host_ptr</td>
<td>The <code>use_host_ptr</code> property adds the requirement that the SYCL runtime must not allocate any memory for the image and instead uses the provided host pointer directly. This prevents the SYCL runtime from allocating additional temporary storage on the host.</td>
</tr>
</tbody>
</table>

Table 4.40: Properties supported by the SYCL image classes.
The property adds the requirement that the memory which is owned by the SYCL image can be shared with the application via a std::mutex provided to the property. The std::mutex is locked by the runtime whenever the data is in use and unlocked otherwise. Data is synchronized with hostData, when the std::mutex is unlocked by the runtime.

The context_bound property adds the requirement that the SYCL image can only be associated with a single SYCL context that is provided to the property.

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>property::image::use_mutex</td>
<td>The property adds the requirement that the memory which is owned by the SYCL image can be shared with the application via a std::mutex provided to the property. The std::mutex is locked by the runtime whenever the data is in use and unlocked otherwise. Data is synchronized with hostData, when the std::mutex is unlocked by the runtime.</td>
</tr>
<tr>
<td>property::image::context_bound</td>
<td>The context_bound property adds the requirement that the SYCL image can only be associated with a single SYCL context that is provided to the property.</td>
</tr>
</tbody>
</table>

Table 4.40: Properties supported by the SYCL image classes.

The constructors and member functions of the image property classes are listed in Tables 4.41 and 4.42.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>property::image::use_host_ptr::use_host_ptr()</td>
<td>Constructs a SYCL use_host_ptr property instance.</td>
</tr>
<tr>
<td>property::image::use_mutex::use_mutex(std::mutex &amp; mutexRef)</td>
<td>Constructs a SYCL use_mutex property instance with a reference to mutexRef parameter provided.</td>
</tr>
<tr>
<td>property::image::context_bound::context_bound(context boundContext)</td>
<td>Constructs a SYCL context_bound property instance with a copy of a SYCL context.</td>
</tr>
</tbody>
</table>

Table 4.41: Constructors of the image property classes.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>std::mutex *property::image::use_mutex::get_mutex_ptr()const</td>
<td>Returns the std::mutex which was specified when constructing this SYCL use_mutex property.</td>
</tr>
<tr>
<td>context property::image::context_bound::get_context()const</td>
<td>Returns the context which was specified when constructing this SYCL context_bound property.</td>
</tr>
</tbody>
</table>

Table 4.42: Member functions of the image property classes.

### 4.7.3.4 Image synchronization rules

The rules are similar to those described in Section 4.7.2.3.

For the lifetime of the image object, the associated host memory must be left available to the SYCL runtime and
4.7. DATA ACCESS AND STORAGE IN SYCL

the contents of the associated host memory is unspecified until the image object is destroyed. If an image object
value is copied, then only a reference to the underlying image object is copied. The underlying image object is
reference-counted. Only after all image value references to the underlying image object have been destroyed is
the actual image object itself destroyed.

If an image object is constructed with associated host memory, then its destructor blocks until all operations in all
SYCL queues on that image object have completed. Any modifications to the image data will be copied back, if
necessary, to the associated host memory. Any errors occurring during destruction are reported to any associated
context’s asynchronous error handler. If an image object is constructed with a storage object, then the storage
object defines what synchronization or copying behavior occurs on image object destruction.

4.7.4 Sharing host memory with the SYCL data management classes

In order to allow the SYCL runtime to do memory management and allow for data dependencies, there are two
classes defined, buffer and image. The default behavior for them is that a “raw” pointer is given during the
construction of the data management class, with full ownership to use it until the destruction of the SYCL object.

In this section we go in greater detail on sharing or explicitly not sharing host memory with the SYCL data classes,
and we will use the buffer class as an example. The same rules will apply to images as well.

4.7.4.1 Default behavior

When using a SYCL buffer, the ownership of the pointer passed to the constructor of the class is, by default,
passed to SYCL runtime, and that pointer cannot be used on the host side until the buffer or image is destroyed. A
SYCL application can use memory managed by a SYCL buffer within the buffer scope by using a host accessor
as defined in 4.7.6. However, there is no guarantee that the host accessor synchronizes with the original host
address used in its constructor.

The pointer passed in is the one used to copy data back to the host, if needed, before buffer destruction. The
memory pointed by host pointer will not be de-allocated by the runtime, and the data is copied back from the
device if there is a need for it.

4.7.4.2 SYCL ownership of the host memory

In the case where there is host memory to be used for initialization of data but there is no intention of using that
host memory after the buffer is destroyed, then the buffer can take full ownership of that host memory.

When a buffer owns the host pointer there is no copy back, by default. In this situation, the SYCL application
may pass a unique pointer to the host data, which will be then used by the runtime internally to initialize the data
in the device.

For example, the following could be used:

```c++
1  {
2     auto ptr = std::make_unique<int>(-1234);
3     buffer<int, 1> b { std::move(ptr), range { 1 } };
4     // ptr is not valid anymore.
5     // There is nowhere to copy data back
6  }
```
However, optionally the `buffer::set_final_data()` can be set to a `std::weak_ptr` to enable copying data back, to another host memory address that is going to be valid after buffer construction.

```cpp
1 { auto ptr = std::make_unique<int>(-42);
2         buffer<int, 1> b { std::move(ptr), range { 1 } };
3 // ptr is not valid anymore.
4 // There is nowhere to copy data back.
5 // To get copy back, a location can be specified:
6 b.set_final_data(std::weak_ptr<int> { .... })
7 }
```

### 4.7.4.3 Shared SYCL ownership of the host memory

When an instance of `std::shared_ptr` is passed to the buffer constructor, then the buffer object and the developer’s application share the memory region. If the shared pointer is still used on the application’s side then the data will be copied back from the buffer or image and will be available to the application after the buffer or image is destroyed.

If the `shared_ptr` is not empty, the contents of the referenced memory are used to initialize the buffer. If the `shared_ptr` is empty, then the buffer is created with uninitialized memory.

When the buffer is destroyed and the data have potentially been updated, if the number of copies of the shared pointer outside the runtime is 0, there is no user-side shared pointer to read the data. Therefore the data is not copied out, and the buffer destructor does not need to wait for the data processes to be finished from OpenCL, as the outcome is not needed on the application’s side.

This behavior can be overridden using the `set_final_data()` member function of the buffer class, which will by any means force the buffer destructor to wait until the data is copied to wherever the `set_final_data()` member function has put the data (or not wait nor copy if set final data is `nullptr`).

```cpp
1 { std::shared_ptr<int> ptr { data };
2 { buffer<int, 1> b { ptr, range<2>{ 10, 10 } };
3 // update the data
4 [...] }
5 // Data is copied back because there is an user side shared_ptr
6 }
```

```cpp
1 { std::shared_ptr<int> ptr { data };
2 { buffer<int, 1> b { ptr, range<2>{ 10, 10 } };
3 // update the data
4 [...] }
5 ptr.reset();
6 // Data is not copied back, there is no user side shared_ptr.
7 }
```
4.7.5 Synchronization primitives

When the user wants to use the buffer simultaneously in the SYCL runtime and their own code (e.g. a multi-threaded mechanism) and wants to use manual synchronization without host accessors, a std::mutex can be passed to the buffer constructor via the right property.

The runtime promises to lock the mutex whenever the data is in use and unlock it when it no longer needs it.

```cpp
{
    std::mutex m;
    auto shD = std::make_shared<int>(42)
    sycl::buffer b { shD, { sycl::property::buffer::use_mutex { m } } };
{
    std::lock_guard lck { m };
    // User accesses the data
    do_something(shD);
    /* m is unlocked when lck goes out of scope, by normal end of this
        block but also if an exception is thrown for example */
}
```

When the runtime releases the mutex the user is guaranteed that the data was copied back on the shared pointer — unless the final data destination has been changed using the member function set_final_data().

4.7.6 Accessors

Accessors provide access to the data managed by a buffer or image, or to local memory allocated by the runtime. Accessors allow users to define requirements to memory objects (see Section 3.7.1). Note that construction of an accessor is what defines a memory object requirement, and these requirements are independent of whether there are any uses of an accessor.

The SYCL accessor class template takes the following template parameters:

- A typename specifying the data type that the accessor is providing access to.
- An integer specifying the dimensionality of the accessor.
- A value of access_mode specifying the mode of access the accessor is providing.
- A value of target specifying the target of access the accessor is providing.

The SYCL host_accessor class template takes the following template parameters:

- A typename specifying the data type that the host_accessor is providing access to.
- An integer specifying the dimensionality of the accessor.
- A value of access_mode specifying the mode of access the host_accessor is providing.

The parameters described above determine the data an accessor provides access to and the way in which that access is provided. This separation allows a SYCL runtime implementation to choose an efficient way to provide access to the data within an execution schedule.
Because of this the interfaces of the accessor and the host_accessor will be different depending on the possible combinations of those parameters.

There are four categories of accessor: buffer device accessors (see Section 4.7.6.9), buffer host accessors (see Section 4.7.6.10), local accessors (see Section 4.7.6.11) and image accessors (see Section 4.7.6.12).

### 4.7.6.1 Access targets

The access target of an accessor specifies what the accessor is providing access to.

The `target` enumeration, shown in Table 4.43, describes the potential targets of an accessor.

```cpp
namespace sycl {
enum class target {
  global_buffer,
  constant_buffer,
  local,
  unsampled_image,
  sampled_image,
  host_buffer,
  host_unsampled_image,
  host_sampled_image
};
}
```

<table>
<thead>
<tr>
<th>target</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>target::global_buffer</code></td>
<td>Access buffer via global memory.</td>
</tr>
<tr>
<td><code>target::constant_buffer</code></td>
<td>Access buffer via constant memory.</td>
</tr>
<tr>
<td><code>target::local</code></td>
<td>Access work-group local memory.</td>
</tr>
<tr>
<td><code>target::unsampled_image</code></td>
<td>Access an unsampled_image.</td>
</tr>
<tr>
<td><code>target::sampled_image</code></td>
<td>Access a sampled_image.</td>
</tr>
<tr>
<td><code>target::host_buffer</code></td>
<td>Access a buffer immediately in host code.</td>
</tr>
<tr>
<td><code>target::host_unsampled_image</code></td>
<td>Access an unsampled_image immediately in host code.</td>
</tr>
<tr>
<td><code>target::host_sampled_image</code></td>
<td>Access a sampled_image immediately in host code.</td>
</tr>
</tbody>
</table>

Table 4.43: Enumeration of access modes available to accessors.

### 4.7.6.2 Access modes

The access mode of an accessor specifies the kind of access that is being provided. This information is used by the runtime to ensure that any data dependencies are resolved by enqueuing any data transfers before or after the execution of a kernel. If a user wants to modify only certain parts of a buffer, preserving other parts of the buffer, then the user should specify the exact sub-range of modification of the buffer.
The `access_mode` enumeration, shown in Table 4.44, describes the potential modes of an accessor.

```cpp
namespace sycl {
    enum class access_mode {
        read,
        write,
        read_write,
        discard_write, // Deprecated in SYCL 2020
        discard_read_write, // Deprecated in SYCL 2020
        atomic // Deprecated in SYCL 2020
    };
}
```

<table>
<thead>
<tr>
<th>access_mode</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>access_mode::read</code></td>
<td>Read-only access.</td>
</tr>
<tr>
<td><code>access_mode::write</code></td>
<td>Write-only access. Previous contents not discarded.</td>
</tr>
<tr>
<td><code>access_mode::read_write</code></td>
<td>Read and write access.</td>
</tr>
<tr>
<td><code>access_mode::discard_write</code></td>
<td>Deprecated in SYCL 2020. Write-only access. Previous contents discarded.</td>
</tr>
<tr>
<td><code>access_mode::discard_read_write</code></td>
<td>Deprecated in SYCL 2020. Read and write access. Previous contents discarded.</td>
</tr>
<tr>
<td><code>access_mode::atomic</code></td>
<td>Read and write atomic access. Deprecated in SYCL 2020.</td>
</tr>
</tbody>
</table>

Table 4.44: Enumeration of access modes available to accessors.

### 4.7.6.3 Access tags

The access mode and access target can be specified via passing tag types to an accessor or host_accessor constructor. This type is used to deduce template arguments of a class.

The Table 4.45, describes the potential tag types and values modes.

SYCL implementations shall provide sufficient list of deduction guides for all template arguments to be deduced for all accessor constructors except for the default constructor and `dimension = 0`. If accessor template arguments are specified by user, but constructor is called with non-matching tag, the SYCL implementation must emit a compilation error. If a `const` data type is specified as an accessor template argument and constructor is called with `write_only` tag, the SYCL implementation must emit a compilation error.

```cpp
namespace sycl {
    template <access_mode>
    struct mode_tag_t {
        explicit mode_tag_t() = default;
    };
}
```
inline constexpr mode_tag_t<access_mode::read> read_only{};
inline constexpr mode_tag_t<access_mode::read_write> read_write{};
inline constexpr mode_tag_t<access_mode::write> write_only{};

template <access_mode, target>
struct mode_target_tag_t {
explicit mode_target_tag_t() = default;
};

inline constexpr mode_target_tag_t<access_mode::read, target::constant_buffer> read_constant{};

Table 4.45: Enumeration of access tags available to accessors.

<table>
<thead>
<tr>
<th>Tag type</th>
<th>Tag value</th>
<th>Access modes</th>
<th>Accessor target</th>
</tr>
</thead>
<tbody>
<tr>
<td>mode_tag_t</td>
<td>read_write</td>
<td>access_mode::read_write</td>
<td>default</td>
</tr>
<tr>
<td>mode_tag_t</td>
<td>read_only</td>
<td>access_mode::read</td>
<td>default</td>
</tr>
<tr>
<td>mode_tag_t</td>
<td>write_only</td>
<td>access_mode::write</td>
<td>default</td>
</tr>
<tr>
<td>mode_target_tag_t</td>
<td>read_constant</td>
<td>access_mode::read</td>
<td>target::constant_buffer</td>
</tr>
</tbody>
</table>

4.7.6.4 Device and host accessors

There are two types that SYCL applications can use to access data: accessor and host_accessor. Some forms of accessor provide access to data from device kernels while other forms provide immediate access to data from the host. All forms of host_accessor provide access to data from the host, though some forms provide immediate access while other forms provide access to host data from host tasks.

If an accessor has the access target target::global_buffer, target::constant_buffer, target::local, target::unsampled_image or target::sampled_image then it is considered a device accessor, and therefore can only be used within a SYCL kernel function and must be associated with a command group. Creating a device accessor is a non-blocking operation which defines a requirement on the device and adds the requirement to the queue.

Some forms of host_accessor provide immediate access to data from the host, as does accessor when used with access target target::host_unsampled_image, target::host_sampled_image or target::host_buffer. These accessors can only be used from the host and their constructors block until their data is available on the host.

4.7.6.5 Placeholder accessor

Certain accessor types are allowed to be placeholder accessors. A placeholder accessor defines an accessor instance that is not bound to a specific command group. The accessor defines only the type of the accessor (target memory, access mode, base type, ...). When associated with a command group using the handler::require() function, it defines a requirement for the command group. The same placeholder accessor can be required by multiple command groups.

Only the following access targets are allowed in placeholder accessors:

- target::global_buffer
- target::constant_buffer
The `enum class` placeholder shown below can be used as an accessor template parameter `isPlaceholder`. This accessor template parameter has been deprecated in SYCL 2020 and placeholder semantics apply regardless of the value `isPlaceholder`.

```cpp
namespace sycl {
    enum class placeholder { // Deprecated in SYCL 2020
        false_t,
        true_t,
    };
}
```

### 4.7.6.6 Accessor declaration

The declaration for the `accessor` and the `host_accessor` classes is provided in Listing 4.1.

```cpp
namespace sycl {
    template <typename dataT,
              int dimensions = 1,
              access_mode accessmode =
                (std::is_const_v<dataT> ? access_mode::read :
                 access_mode::read_write),
              target accessTarget = target::global_buffer,
              access::placeholder isPlaceholder = access::placeholder::false_t // Deprecated in SYCL 2020>
    class accessor;
}
```

Listing 4.1: Accessor declaration.

### 4.7.6.7 Constness of the accessor data type

An `accessor` or `host_accessor` can be constructed with the underlying data type being `const`, resulting in an accessor of `const dataT`. Having an accessor of `const dataT` is semantically equivalent to having an accessor of `access_mode::read`: they are both read-only accessors.

Some access modes contradict the constness of the data. An underlying data type `const dataT` is only valid with the following access modes:

- `access_mode::read`
- `access_mode::read_write`

Even when the access mode for an accessor is `access_mode::read_write`, adding `const` to `dataT` makes it a read-only accessor. This ensures, among other things, that the read-only accessor will not trigger a copy-back that
access_mode::read_write normally would require.

Using const dataT makes the accessor read-only by default, as shown in the following example:

```cpp
accessor<const int> acc;
static_assert(std::is_same_v<
    decltype(acc),
    accessor<const int, 1, access_mode::read, target::global_buffer>>);
```

### 4.7.6.8 Implicit accessor conversions

It is valid to implicitly convert a writable accessor to a read-only one by doing at least one of the following:

- Converting data type from non-const dataT to const dataT
- Converting access mode from access_mode::read_write to access_mode::read

Because of the semantic equivalence defined in 4.7.6.7, the following accessor types can be implicitly converted to one another:

- accessor<dataT, dimensions, access_mode::read, accessTarget>
- accessor<const dataT, dimensions, access_mode::read, accessTarget>
- accessor<const dataT, dimensions, access_mode::read_write, accessTarget>

And the following host_accessor types can be implicitly converted to one another:

- host_accessor<dataT, dimensions, access_mode::read, accessTarget>
- host_accessor<const dataT, dimensions, access_mode::read, accessTarget>
- host_accessor<const dataT, dimensions, access_mode::read_write, accessTarget>

### 4.7.6.9 Device buffer accessor

A device buffer accessor provides access to a SYCL buffer instance on a device. A SYCL accessor is considered a device buffer accessor if it has the access target target::global_buffer, or target::constant_buffer.

A device buffer accessor can provide access to memory managed by a SYCL buffer class via either global memory or constant memory, corresponding to the access targets target::global_buffer and target::constant_buffer respectively. A device buffer accessor accessing a SYCL buffer via constant memory is restricted by the available constant memory available on the SYCL device being executed on.

The data type of an accessor must match that of the SYCL buffer which it is accessing.

The dimensionality of a buffer accessor must match that of the SYCL buffer which it is accessing, with the exception of 0 in which case the dimensionality of the SYCL buffer must be 1.

There are three ways a SYCL accessor can provide access to the elements of a SYCL buffer. Firstly by passing a SYCL id instance of the same dimensionality as the SYCL accessor subscript operator. Secondly by passing a single size_t value to multiple consecutive subscript operators (one for each dimension of the SYCL accessor, for example acc[id0][id1][id2]). Finally, in the case of the SYCL accessor being 0 dimensions, by triggering
the implicit conversion operator. Whenever a multi-dimensional index is passed to a SYCL accessor the linear index is calculated based on the index \( \{id_0, id_1, id_2\} \) provided and the range of the SYCL accessor \( \{r_0, r_1, r_2\} \) according to row-major ordering as follows:

\[
id_2 + (id_1 \cdot r_2) + (id_0 \cdot r_2 \cdot r_1)
\]

An accessor can optionally provide access to a sub range of a SYCL buffer by providing a range and offset on construction. In this case the SYCL runtime will only guarantee the latest copy of the data is available in that given range and any modifications outside that range are considered undefined behavior. This allows the SYCL runtime to perform optimizations such as reducing copies between devices. The indexing performed when a SYCL accessor provides access to the elements of a SYCL buffer is unaffected, i.e, the accessor will continue to index from \( \{0,0,0\} \). This allows the offset to be provided either manually or via the `parallel_for` as shown in Listing 4.2.

```cpp
myQueue.submit([&](handler &cgh) {
  auto singleRange = range<3>(8, 16, 16);
  auto offset = id<3>(8, 0, 0);
  // We define the subset of the accessor we require for the kernel
  accessor ptr(syclBuffer, cgh, singleRange, offset);
  // We offset the kernel by the same value to match indexes
  cgh.parallel_for(singleRange, offset, [=](item<3> itemID) {
    ptr[itemID.get_linear_id()] = 2;
  });
});
```

Listing 4.2: Using an accessor to access a sub-range.

An accessor with access target `target::global_buffer` can optionally provide atomic access to a SYCL buffer, using the access mode `access_mode::atomic`, in which case all operators which return an element of the SYCL buffer return an instance of the deprecated `cl::sycl::atomic` class. This functionality is provided for backwards compatibility and will be removed in a future version of SYCL.

A device buffer accessor meets the C++ requirement of `ContiguousContainer` and `ReversibleContainer`. The exception to this is that the device buffer accessor destructor doesn’t destroy any elements or free the memory, because an accessor doesn’t own the underlying data. The iterator for the container interface is the same pointer type as obtained by calling `get_multi_ptr<access::decorated::no>()`. For multidimensional accessors the iterator linearizes the data according to 4.3.

The full list of capabilities that device buffer accessors can support is described in 4.46.

<table>
<thead>
<tr>
<th>Access target</th>
<th>Access modes</th>
<th>Data types</th>
<th>Dimensionalities</th>
</tr>
</thead>
<tbody>
<tr>
<td>global_buffer</td>
<td>read, write</td>
<td>The data type of the SYCL buffer being accessed.</td>
<td>Between 0 and 3 (inclusive).</td>
</tr>
<tr>
<td>constant_buffer</td>
<td>read</td>
<td>The data type of the SYCL buffer being accessed.</td>
<td>Between 0 and 3 (inclusive).</td>
</tr>
</tbody>
</table>

Table 4.46: Description of all the device buffer accessor capabilities.
4.7.6.9.1 Device buffer accessor interface

A synopsis of the SYCL accessor class template buffer specialization is provided below. The member types for this accessor specialization are listed in Tables 4.47. The constructors for this accessor specialization are listed in Tables 4.48. The member functions for this accessor specialization are listed in Tables 4.49. The additional common special member functions and common member functions are listed in 4.5.3 in Tables 4.1 and 4.2, respectively. For valid implicit conversions between accessor types please refer to 4.7.6.8. Additionally, accessors of the same type must be equality comparable not only on the host application, but also within SYCL kernel functions.

```cpp
namespace sycl {
  template <typename dataT,
    int dimensions,
    access::mode accessmode,
    access::target accessTarget,
    access::placeholder isPlaceholder>
  class accessor {
    public:
      template <access::decorated IsDecorated>
      using accessor_ptr = // Corresponds to the target address space,
       __pointer_class__; // is pointer-to-const
       // when (accessmode == access::mode::read);
      using value_type = // const dataT when (accessmode == access::mode::read),
       __value_type__; // dataT otherwise
       // value_type::value
      using reference = // const dataT& when (accessmode == access::mode::read),
       __reference_type__; // dataT& otherwise
       // reference::value
      using const_reference = const dataT &;
      using iterator = // Corresponds to the target address space,
       __pointer_type__; // is pointer-to-const
       // when (accessmode == access::mode::read)
       // iterator::value
      using const_iterator =
       __pointer_to_const_type__; // Corresponds to the target address space
       // const_iterator::value
      using reverse_iterator = std::reverse_iterator<iterator>;
      using const_reverse_iterator = std::reverse_iterator<const_iterator>;
      using difference_type =
       typename std::iterator_traits<iterator>::difference_type;
      using size_type = size_t;

      accessor();

    /* Available only when: (dimensions == 0) */
    template <typename AllocatorT>
    accessor(buffer<dataT, 1, AllocatorT> &bufferRef,
      const property_list &propList = {});

    /* Available only when: (dimensions == 0) */
    template <typename AllocatorT>
    accessor(buffer<dataT, 1, AllocatorT> &bufferRef,
      handler &commandGroupHandlerRef, const property_list &propList = {});

    /* Available only when: (dimensions > 0) */
    template <typename AllocatorT>
    accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
      const property_list &propList = {});
  };
}
```
4.7. DATA ACCESS AND STORAGE IN SYCL

SYCL 2020 provisional

/* Available only when: (dimensions > 0) */
template <typename AllocatorT, typename TagT>
accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef, TagT tag,
const property_list &propList = {});

/* Available only when: (dimensions > 0) */
template <typename AllocatorT>
accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
handler &commandGroupHandlerRef, const property_list &propList = {});

/* Available only when: (dimensions > 0) */
template <typename AllocatorT, typename TagT>
accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
range<dimensions> accessRange, TagT tag,
const property_list &propList = {});

/* Available only when: (dimensions > 0) */
template <typename AllocatorT>
accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
range<dimensions> accessRange, id<dimensions> accessOffset,
const property_list &propList = {});

/* Available only when: (dimensions > 0) */
template <typename AllocatorT, typename TagT>
accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
range<dimensions> accessRange, id<dimensions> accessOffset,
TagT tag, const property_list &propList = {});

/* Available only when: (dimensions > 0) */
template <typename AllocatorT>
accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
handler &commandGroupHandlerRef, range<dimensions> accessRange,
const property_list &propList = {});

/* Available only when: (dimensions > 0) */
template <typename AllocatorT, typename TagT>
accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
handler &commandGroupHandlerRef, range<dimensions> accessRange,
TagT tag, const property_list &propList = {});
handler &commandGroupHandlerRef, range<dimensions> accessRange,
id<dimensions> accessOffset, const property_list &propList = {});

/* Available only when: (dimensions > 0) */
template <typename AllocatorT, typename TagT>
accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
handler &commandGroupHandlerRef, range<dimensions> accessRange,
id<dimensions> accessOffset, TagT tag,
const property_list &propList = {});

/* -- common interface members -- */

void swap(accessor &other);

bool is_placeholder() const;

size_type byte_size() const noexcept;

size_type size() const noexcept;

size_type max_size() const noexcept;

size_type get_count() const noexcept;

bool empty() const noexcept;

/* Available only when: dimensions > 0 */
range<dimensions> get_range() const;

/* Available only when: dimensions > 0 */
id<dimensions> get_offset() const;

/* Available only when: (dimensions == 0) */
operator reference() const;

/* Available only when: (dimensions > 0) */
reference operator[](id<dimensions> index) const;

/* Deprecated in SYCL 2020
Available only when: accessMode == access::mode::atomic && dimensions == 0 */
operator cl::sycl::atomic<dataT, access::address_space::global_space> () const;

/* Deprecated in SYCL 2020
Available only when: accessMode == access::mode::atomic && dimensions > 0 */
cl::sycl::atomic<dataT, access::address_space::global_space> operator[](id<dimensions> index) const;

/* Available only when: dimensions > 1 */
__unspecified__ &operator[](size_t index) const;

std::add_pointer_t<value_type> get_pointer() const noexcept;

template <access::decorated IsDecorated>
accessor_ptr<IsDecorated> get_multi_ptr() const noexcept;
Listing 4.3: Device accessor class for buffers.

<table>
<thead>
<tr>
<th>Member types</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>value_type</td>
<td>If (accessmode == access_mode::read), equal to const dataT. In other cases equal to dataT.</td>
</tr>
<tr>
<td>template &lt;access::decorated IsDecorated&gt; accessor_ptr</td>
<td>If (accessTarget == access::target::global_buffer): multi_ptr&lt;value_type, access::address_space::global_space, IsDecorated&gt;. If (accessTarget == access::target::constant_buffer): multi_ptr&lt;value_type, access::address_space::constant_space, IsDecorated&gt;.</td>
</tr>
<tr>
<td>reference</td>
<td>If (accessmode == access_mode::read), equal to const dataT&amp;. In other cases equal to dataT&amp;.</td>
</tr>
<tr>
<td>const_reference</td>
<td>const dataT&amp;</td>
</tr>
<tr>
<td>iterator</td>
<td>If (accessTarget == access::target::global_buffer): raw_global_ptr&lt;value_type&gt;. If (accessTarget == access::target::constant_buffer): raw_constant_ptr&lt;value_type&gt;.</td>
</tr>
<tr>
<td>const_iterator</td>
<td>If (accessTarget == access::target::global_buffer): raw_global_ptr&lt;const value_type&gt;. If (accessTarget == access::target::constant_buffer): raw_constant_ptr&lt;const value_type&gt;.</td>
</tr>
<tr>
<td>reverse_iterator</td>
<td>Iterator adaptor that reverses the direction of iterator.</td>
</tr>
<tr>
<td>const_reverse_iterator</td>
<td>Iterator adaptor that reverses the direction of const_iterator.</td>
</tr>
<tr>
<td>difference_type</td>
<td>typename std::iterator_traits&lt;iterator&gt;::difference_type</td>
</tr>
<tr>
<td>size_type</td>
<td>size_t</td>
</tr>
</tbody>
</table>

Table 4.47: Member types of the accessor class template buffer specialization.
<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
</table>
| accessor()                                                                 | Constructs an empty accessor. Fulfills the following post-conditions:  
  • (empty() == true)  
  • All size queries return 0.  
  • The only iterator that can be obtained is nullptr.  
  • Trying to access the underlying memory is undefined behavior.  
A default constructed placeholder accessor can be passed to a SYCL kernel, but it is not valid to register it with the command group handler. |
| accessor(buffer<dataT, 1, AllocatorT> &bufferRef,                           | Available only when: (dimensions == 0). Constructs a placeholder accessor instance for accessing the first element of a SYCL buffer. The optional property_list provides properties for the constructed SYCL accessor object.                     |
|   const property_list &propList = {})                                      |                                                                                                                                                                                                                                                                                                                                            |
| accessor(buffer<dataT, 1, AllocatorT> &bufferRef,                           | Available only when: (dimensions == 0). Constructs a SYCL accessor instance for accessing the first element of a SYCL buffer within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef. The optional property_list provides properties for the constructed SYCL accessor object. |
|   handler &commandGroupHandlerRef,                                        |                                                                                                                                                                                                                                                                                                                                            |
|   const property_list &propList = {})                                      |                                                                                                                                                                                                                                                                                                                                            |
| accessor(buffer<dataT, dimensions, AllocatorT> &                            | Available only when: (dimensions > 0). Constructs a placeholder accessor for accessing a SYCL buffer. The optional property_list provides properties for the constructed SYCL accessor object.                                                                                           |
|   bufferRef,                                                              |                                                                                                                                                                                                                                                                                                                                            |
|   const property_list &propList = {})                                      |                                                                                                                                                                                                                                                                                                                                            |
| accessor(buffer<dataT, dimensions, AllocatorT> &                            | Available only when: (dimensions > 0). Constructs a SYCL accessor instance for accessing a SYCL buffer. The tag is used to deduce template arguments of the accessor as described in Section 4.7.6.3. The optional property_list provides properties for the constructed SYCL accessor object. |
|   bufferRef,                                                              |                                                                                                                                                                                                                                                                                                                                            |
|   TagT tag, const property_list &propList = {})                           |                                                                                                                                                                                                                                                                                                                                            |
| accessor(buffer<dataT, dimensions, AllocatorT> &                            | Available only when: (dimensions > 0). Constructs a SYCL accessor instance for accessing a SYCL buffer within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef. The optional property_list provides properties for the constructed SYCL accessor object. |
|   bufferRef,                                                              |                                                                                                                                                                                                                                                                                                                                            |
|   handler &commandGroupHandlerRef,                                        |                                                                                                                                                                                                                                                                                                                                            |
|   const property_list &propList = {})                                      |                                                                                                                                                                                                                                                                                                                                            |

Table 4.48: Constructors of the accessor class template buffer specialization.
<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>`accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp; bufferRef,</td>
<td>Available only when: <code>(dimensions &gt; 0)</code>. Constructs a SYCL accessor instance for accessing a SYCL buffer within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef. The tag is used to deduce template arguments of the accessor as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>handler &amp;commandGroupHandlerRef,</td>
<td></td>
</tr>
<tr>
<td>TagT tag, const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp; bufferRef,</td>
<td>Available only when: <code>(dimensions &gt; 0)</code>. Constructs a placeholder accessor for accessing a range of a SYCL buffer. The tag is used to deduce template arguments of the accessor as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp; bufferRef,</td>
<td>Available only when: <code>(dimensions &gt; 0)</code>. Constructs a placeholder accessor for accessing a range of a SYCL buffer. The tag is used to deduce template arguments of the accessor as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>TagT tag, const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp; bufferRef,</td>
<td>Available only when: <code>(dimensions &gt; 0)</code>. Constructs a placeholder accessor for accessing a range of a SYCL buffer. The tag is used to deduce template arguments of the accessor as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>id&lt;dimensions&gt; accessOffset,</td>
<td></td>
</tr>
<tr>
<td>const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp; bufferRef,</td>
<td>Available only when: <code>(dimensions &gt; 0)</code>. Constructs a placeholder accessor for accessing a range of a SYCL buffer. The tag is used to deduce template arguments of the accessor as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>id&lt;dimensions&gt; accessOffset,</td>
<td></td>
</tr>
<tr>
<td>TagT tag, const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp; bufferRef,</td>
<td>Available only when: <code>(dimensions &gt; 0)</code>. Constructs a SYCL accessor instance for accessing a range of SYCL buffer within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef, specified by accessRange. The optional <code>property_list</code> provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>handler &amp;commandGroupHandlerRef,</td>
<td></td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>const property_list &amp;propList = {})</td>
<td></td>
</tr>
</tbody>
</table>

Table 4.48: Constructors of the `accessor` class template `buffer` specialization.
### Constructor

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
</table>
| accessor(buffer<
daT, dimensions, AllocatorT> & bufferRef,  
handler &commandGroupHandlerRef,  
rangedimensions> accessRange,  
TagT tag, const property_list &propList = {}) | Available only when: \((\text{dimensions} > 0)\). Constructs a SYCL accessor instance for accessing a range of SYCL buffer within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef, specified by accessRange. The tag is used to deduce template arguments of the accessor as described in Section 4.7.6.3. The optional property_list provides properties for the constructed SYCL accessor object. |

| accessor(buffer<
daT, dimensions, AllocatorT> & bufferRef,  
handler &commandGroupHandlerRef,  
rangedimensions> accessRange,  
id<dimensions> accessOffset,  
const property_list &propList = {}) | Available only when: \((\text{dimensions} > 0)\). Constructs a SYCL accessor instance for accessing a range of SYCL buffer within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef, specified by accessRange and accessOffset. The optional property_list provides properties for the constructed SYCL accessor object. |

| accessor(buffer<
daT, dimensions, AllocatorT> & bufferRef,  
handler &commandGroupHandlerRef,  
rangedimensions> accessRange,  
id<dimensions> accessOffset,  
TagT tag, const property_list &propList = {}) | Available only when: \((\text{dimensions} > 0)\). Constructs a SYCL accessor instance for accessing a range of SYCL buffer within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef, specified by accessRange and accessOffset. The tag is used to deduce template arguments of the accessor as described in Section 4.7.6.3. The optional property_list provides properties for the constructed SYCL accessor object. |

End of table

### Table 4.48: Constructors of the accessor class template buffer specialization.

### Member function

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>void swap(accessor &amp;other);</td>
<td>Swaps the contents of the current accessor with the contents of other.</td>
</tr>
<tr>
<td>bool is_placeholder() const</td>
<td>Returns true if ((\text{accessTarget} \neq \text{target} :: \text{host_buffer})) and the accessor has been constructed without a handler. Otherwise returns false.</td>
</tr>
<tr>
<td>size_type byte_size() const noexcept</td>
<td>Returns the size in bytes of the region of a SYCL buffer that this SYCL accessor is accessing.</td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.49: Member functions of the accessor class template buffer specialization.
### Table 4.49: Member functions of the accessor class template buffer specialization.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>size_type size() const noexcept</code></td>
<td>Returns the number of elements in the region of a SYCL buffer that this SYCL accessor is accessing.</td>
</tr>
<tr>
<td><code>size_type max_size() const noexcept</code></td>
<td>Returns the maximum number of elements any accessor of this type would be able to access.</td>
</tr>
<tr>
<td><code>size_type get_count() const noexcept</code></td>
<td>Returns the same as <code>size()</code>.</td>
</tr>
<tr>
<td><code>bool empty() const noexcept</code></td>
<td>Returns true iff <code>(size()== 0)</code>.</td>
</tr>
<tr>
<td><code>range&lt;dimensions&gt; get_range() const</code></td>
<td>Available only when: dimensions &gt; 0. Returns a range object which represents the number of elements of dataT per dimension that this accessor may access. The range object returned must equal to the range of the buffer this accessor is associated with, unless a range was explicitly specified when this accessor was constructed.</td>
</tr>
<tr>
<td><code>id&lt;dimensions&gt; get_offset() const</code></td>
<td>Available only when: dimensions &gt; 0. Returns an id object which represents the starting point in number of elements of dataT for the range that this accessor may access. The id object returned must equal to <code>id{0, 0, 0}</code>, unless an offset was explicitly specified when this accessor was constructed.</td>
</tr>
<tr>
<td><code>operator reference() const</code></td>
<td>Available only when: (dimensions == 0). Returns a reference to the element stored within the SYCL buffer this SYCL accessor is accessing.</td>
</tr>
<tr>
<td><code>reference operator[](id&lt;dimensions&gt; index) const</code></td>
<td>Available only when: (dimensions &gt; 0). Returns a reference to the element stored within the SYCL buffer this SYCL accessor is accessing at the index specified by index.</td>
</tr>
<tr>
<td><code>operator const dataT &amp;() const</code></td>
<td>Available only when: <code>accessMode == access_mode::read &amp;&amp; dimensions == 0</code> . Returns a const reference to the element stored within the SYCL buffer this SYCL accessor is accessing.</td>
</tr>
<tr>
<td><code>const_reference operator[](id&lt;dimensions&gt; index) const</code></td>
<td>Available only when: <code>accessMode == access_mode::read &amp;&amp; dimensions &gt; 0</code> . Returns a const reference to the element stored within the SYCL buffer this SYCL accessor is accessing at the index specified by index.</td>
</tr>
</tbody>
</table>
### Table 4.49: Member functions of the `accessor` class template buffer specialization.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>operator cl::sycl::atomic&lt;dataT, access::address_space::global_space&gt; () const</code></td>
<td>Deprecated in SYCL 2020. Available only when: <code>accessMode == access_mode::atomic &amp;&amp; dimensions == 0</code>). Returns an instance of <code>cl::sycl::atomic</code> of type <code>dataT</code> providing atomic access to the element stored within the SYCL buffer this SYCL accessor is accessing.</td>
</tr>
<tr>
<td><code>cl::sycl::atomic&lt;dataT, access::address_space::global_space&gt; operator[](id&lt;dimensions&gt; index) const</code></td>
<td>Deprecated in SYCL 2020. Available only when: <code>accessMode == access_mode::atomic &amp;&amp; dimensions &gt; 0</code>). Returns an instance of <code>cl::sycl::atomic</code> of type <code>dataT</code> providing atomic access to the element stored within the SYCL buffer this SYCL accessor is accessing at the index specified by <code>index</code>.</td>
</tr>
<tr>
<td><code>__unspecified__ &amp; operator[](size_t index) const</code></td>
<td>Available only when: <code>dimensions &gt; 1</code>. Returns an instance of an undefined intermediate type representing a SYCL accessor of the same type as this SYCL accessor, with the dimensionality <code>dimensions-1</code> and containing an implicit SYCL <code>id</code> with index dimensions set to <code>index</code>. The intermediate type returned must provide all available subscript operators which take a <code>size_t</code> parameter defined by the SYCL accessor class that are appropriate for the type it represents (including this subscript operator).</td>
</tr>
<tr>
<td><code>std::add_pointer_t&lt;value_type&gt; get_pointer() const noexcept</code></td>
<td>Returns a pointer to the memory this SYCL accessor memory is accessing.</td>
</tr>
<tr>
<td><code>template &lt;access::decorated IsDecorated&gt; accessor_ptr&lt;IsDecorated&gt; get_multi_ptr() const noexcept</code></td>
<td>Returns a <code>multi_ptr</code> to the memory this SYCL accessor memory is accessing.</td>
</tr>
<tr>
<td><code>iterator data() const noexcept</code></td>
<td>Returns a pointer to the memory this SYCL accessor memory is accessing.</td>
</tr>
<tr>
<td><code>iterator begin() const noexcept</code></td>
<td>Returns an iterator to the first element of the memory within the access range.</td>
</tr>
<tr>
<td><code>iterator end() const noexcept</code></td>
<td>Returns an iterator that points past the last element of the memory within the access range.</td>
</tr>
<tr>
<td><code>const_iterator cbegin() const noexcept</code></td>
<td>Returns a <code>const</code> iterator to the first element of the memory within the access range.</td>
</tr>
<tr>
<td><code>const_iterator cend() const noexcept</code></td>
<td>Returns a <code>const</code> iterator that points past the last element of the memory within the access range.</td>
</tr>
</tbody>
</table>

Continued on next page
Table 4.49: Member functions of the accessor class template buffer specialization.

### 4.7.6.9.2 Device buffer accessor properties

The properties that can be provided when constructing the SYCL accessor class are describe in Table 4.50.

```cpp
namespace sycl {
namespace property {
    struct noinit {};
}
}
inline constexpr property::noinit noinit;
```

Table 4.50: Properties supported by the SYCL accessor class.

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>property::noinit</td>
<td>The noinit property notifies the SYCL runtime that previous contents of a buffer can be discarded. Replaces deprecated discard_write and discard_read_write access modes.</td>
</tr>
</tbody>
</table>

Table 4.51: Constructors of the accessor property classes.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>property::noinit::noinit()</td>
<td>Constructs a SYCL noinit property instance.</td>
</tr>
</tbody>
</table>

The constructors of the accessor property classes are listed in Table 4.51.
4.7.6.10 Host buffer accessor

A SYCL host_accessor is a host buffer accessor, which provides access to a SYCL buffer instance on a host.

A host_accessor can be used in either of two ways. If the accessor is used from application scope, it must be constructed without a command group handler object. In this case, the constructor blocks until the requested memory is copied to the host, and the accessor provides immediate access to that memory.

A host_accessor can also be used inside command group scope for a host task (see Section 4.11). In this case, it must be constructed with the command group handler for the host task. The constructor does not block in this case. Instead, the accessor provides a requisite for scheduling the host task.

If the SYCL buffer this SYCL host_accessor is accessing was constructed with the property property::buffer::use_host_ptr the address of the memory accessed on the host must be the address the SYCL buffer was constructed with, otherwise the SYCL runtime is free to allocate temporary memory to provide access on the host.

The data type of a host buffer accessor must match that of the SYCL buffer which it is accessing.

The dimensionality of a buffer accessor must match that of the SYCL buffer which it is accessing, with the exception of 0 in which case the dimensionality of the SYCL buffer must be 1.

There are three ways a SYCL host_accessor can provide access to the elements of a SYCL buffer. Firstly by passing a SYCL id instance of the same dimensionality as the SYCL host_accessor subscript operator. Secondly by passing a single size_t value to multiple consecutive subscript operators (one for each dimension of the SYCL host_accessor, for example acc[id0][id1][id2]). Finally, in the case of the SYCL host_accessor being 0 dimensions, by triggering the implicit conversion operator. Whenever a multi-dimensional index is passed to a SYCL host_accessor the linear index is calculated based on the index {id0, id1, id2} provided and the range of the SYCL host_accessor {r0, r1, r2} according to row-major ordering as follows:

\[ id_2 + (id_1 \cdot r_2) + (id_0 \cdot r_2 \cdot r_1) \]  

4.4

A host buffer accessor can optionally provide access to a sub range of a SYCL buffer by providing a range and offset on construction. In this case the SYCL runtime will only guarantee the latest copy of the data is available in that given range and any modifications outside that range are considered undefined behavior. The indexing performed when a SYCL host_accessor provides access to the elements of a SYCL buffer is unaffected, i.e, the accessor will continue to index from \{0, 0, 0\}.

A host buffer accessor meets the C++ requirement of ContiguousContainer and ReversibleContainer. The exception to this is that the device buffer accessor destructor doesn’t destroy any elements or free the memory, because a host buffer accessor doesn’t own the underlying data. For multidimensional accessors the iterator linearizes the data according to 4.3.

4.7.6.10.1 Host buffer accessor interface

A synopsis of the SYCL host_accessor class template buffer specialization is provided below. The member types for this host_accessor specialization are listed in Tables 4.52. The constructors for this host_accessor specialization are listed in Tables 4.53. The member functions for this host_accessor specialization are listed in Tables 4.54. The additional common special member functions and common member functions are listed in 4.5.3 in Tables 4.1 and 4.2, respectively. For valid implicit conversions between accessor types please refer to 4.7.6.8. Additionally, accessors of the same type must be equality comparable.
namespace sycl {

template <typename dataT,
    int dimensions,
    access::mode accessmode>
    class host_accessor {

    public:
        using value_type = // const dataT when (accessmode == access::mode::read),
            __value_type__; // dataT otherwise
        using reference = // const dataT& when (accessmode == access::mode::read),
            __reference_type__; // dataT& otherwise
        using const_reference = const dataT &;
        using iterator = // const dataT* when (accessmode == access::mode::read),
            __pointer_type__; // dataT* otherwise
        using const_iterator = const dataT *;
        using difference_type =
            typename std::iterator_traits<iterator>::difference_type;
        using size_type = size_t;

        host_accessor();

        /* Available only when: (dimensions == 0) */
        template <typename AllocatorT>
        host_accessor(buffer<dataT, 1, AllocatorT> &bufferRef,
            const property_list &propList = {});

        /* Available only when: (dimensions == 0) */
        template <typename AllocatorT>
        host_accessor(buffer<dataT, 1, AllocatorT> &bufferRef,
            handler &commandGroupHandlerRef, const property_list &propList = {});

        /* Available only when: (dimensions > 0) */
        template <typename AllocatorT>
        host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
            const property_list &propList = {});

        /* Available only when: (dimensions > 0) */
        template <typename AllocatorT, typename TagT>
        host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef, TagT tag,
            const property_list &propList = {});

        /* Available only when: (dimensions > 0) */
        template <typename AllocatorT>
        host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
            handler &commandGroupHandlerRef, const property_list &propList = {});

        /* Available only when: (dimensions > 0) */
        template <typename AllocatorT, typename TagT>
        host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef, TagT tag,
            handler &commandGroupHandlerRef, const property_list &propList = {});

        /* Available only when: (dimensions > 0) */
        template <typename AllocatorT>
        host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
            range<dimensions> accessRange, const property_list &propList = {});
    }  // namespace sycl
}  // SYCL 2020 provisional
56  /* Available only when: (dimensions > 0) */
57  template <typename AllocatorT, typename TagT>
58  host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
59      range<dimensions> accessRange, TagT tag,
60      const property_list &propList = {});
61
62  /* Available only when: (dimensions > 0) */
63  template <typename AllocatorT>
64  host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
65      range<dimensions> accessRange, id<dimensions> accessOffset,
66      const property_list &propList = {});
67
68  /* Available only when: (dimensions > 0) */
69  template <typename AllocatorT, typename TagT>
70  host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
71      range<dimensions> accessRange, id<dimensions> accessOffset,
72      TagT tag, const property_list &propList = {});
73
74  /* Available only when: (dimensions > 0) */
75  template <typename AllocatorT>
76  host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
77      handler &commandGroupHandlerRef, range<dimensions> accessRange,
78      const property_list &propList = {});
79
80  /* Available only when: (dimensions > 0) */
81  template <typename AllocatorT, typename TagT>
82  host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
83      handler &commandGroupHandlerRef, range<dimensions> accessRange,
84      TagT tag, const property_list &propList = {});
85
86  /* Available only when: (dimensions > 0) */
87  template <typename AllocatorT>
88  host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
89      handler &commandGroupHandlerRef, range<dimensions> accessRange,
90      id<dimensions> accessOffset, const property_list &propList = {});
91
92  /* Available only when: (dimensions > 0) */
93  template <typename AllocatorT, typename TagT>
94  host_accessor(buffer<dataT, dimensions, AllocatorT> &bufferRef,
95      handler &commandGroupHandlerRef, range<dimensions> accessRange,
96      id<dimensions> accessOffset, TagT tag,
97      const property_list &propList = {});
98
99  /* -- common interface members -- */
100  void swap(host_accessor &other);
101
102  size_type byte_size() const noexcept;
103
104  size_type size() const noexcept;
105
106  size_type max_size() const noexcept;
107
108  bool empty() const noexcept;
4.7. DATA ACCESS AND STORAGE IN SYCL

Listing 4.4: Host accessor class for buffers.

```cpp
/* Available only when: dimensions > 0 */
range<dimensions> get_range() const;

/* Available only when: dimensions > 0 */
id<dimensions> get_offset() const;

/* Available only when: (dimensions == 0) */
operator reference() const;

/* Available only when: (dimensions > 0) */
reference operator[](id<dimensions> index) const;

/* Available only when: dimensions > 1 */
__unspecified__ &operator[](size_t index) const;

iterator data() const noexcept;

iterator begin() const noexcept;

iterator end() const noexcept;

const_iterator cbegin() const noexcept;

const_iterator cend() const noexcept;
};
} // namespace sycl

<table>
<thead>
<tr>
<th>Member types</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>value_type</td>
<td>If (accessmode == access_mode::read), equal to const dataT. In other cases equal to dataT.</td>
</tr>
<tr>
<td>reference</td>
<td>If (accessmode == access_mode::read), equal to const dataT&amp;. In other cases equal to dataT&amp;.</td>
</tr>
<tr>
<td>const_reference</td>
<td>const dataT&amp;</td>
</tr>
<tr>
<td>iterator</td>
<td>If (accessmode == access_mode::read), equal to const dataT*. In other cases equal to dataT*.</td>
</tr>
<tr>
<td>const_iterator</td>
<td>const dataT*</td>
</tr>
<tr>
<td>difference_type</td>
<td>typename std::iterator_traits&lt;iterator&gt;::difference_type</td>
</tr>
<tr>
<td>size_type</td>
<td>size_t</td>
</tr>
</tbody>
</table>

Table 4.52: Member types of the host_accessor class template.
### Constructors of the `host_accessor` class template

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
</table>
| `host_accessor()`                                                          | Constructs an empty accessor. Fulfills the following post-conditions:  
  - `(empty() == true)`  
  - All size queries return 0.  
  - The only iterator that can be obtained is `nullptr`.  
  - Trying to access the underlying memory is undefined behavior. |
| `host_accessor(buffer<dataT, 1, AllocatorT> & bufferRef,  
                 const property_list &propList = {})` | Available only when: `(dimensions == 0)`. Constructs a `host_accessor` instance for accessing the first element of a SYCL buffer immediately on the host. The optional `property_list` provides properties for the constructed SYCL `host_accessor` object. |
| `host_accessor(buffer<dataT, 1, AllocatorT> & bufferRef,  
                 handler &commandGroupHandlerRef,  
                 const property_list &propList = {})` | Available only when: `(dimensions == 0)`. Constructs a `host_accessor` for accessing the first element of a SYCL buffer inside a host task. The optional `property_list` provides properties for the constructed SYCL `host_accessor` object. |
| `host_accessor(buffer<dataT, dimensions, AllocatorT> & bufferRef,  
                 const property_list &propList = {})` | Available only when: `(dimensions > 0)`. Constructs a `host_accessor` for accessing a SYCL buffer immediately on the host. The optional `property_list` provides properties for the constructed SYCL `host_accessor` object. |
| `host_accessor(buffer<dataT, dimensions, AllocatorT> & bufferRef,  
                 TagT tag, const property_list &propList = {})` | Available only when: `(dimensions > 0)`. Constructs a `host_accessor` for accessing a SYCL buffer immediately on the host. The tag is used to deduce template arguments of the `host_accessor` as described in Section 4.7.6.3. The optional `property_list` provides properties for the constructed SYCL `host_accessor` object. |
| `host_accessor(buffer<dataT, dimensions, AllocatorT> & bufferRef,  
                 handler &commandGroupHandlerRef,  
                 const property_list &propList = {})` | Available only when: `(dimensions > 0)`. Constructs a `host_accessor` for accessing a SYCL buffer inside a host task. The optional `property_list` provides properties for the constructed SYCL `host_accessor` object. |

Table 4.53: Constructors of the `host_accessor` class template.
### Constructor Description

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>`host_accessor(buffer&lt;dataT, dimensions, AllocatorT&gt;&amp; bufferRef,</td>
<td>Available only when: (dimensions &gt; 0). Constructs a <code>host_accessor</code> for accessing a SYCL buffer inside a host task. The tag is used to deduce template arguments of the <code>host_accessor</code> as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL <code>host_accessor</code> object.</td>
</tr>
<tr>
<td>handler &amp;commandGroupHandlerRef, TagT tag, const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`host_accessor(buffer&lt;dataT, dimensions, AllocatorT&gt;&amp; bufferRef,</td>
<td>Available only when: (dimensions &gt; 0). Constructs a <code>host_accessor</code> for accessing a range of a SYCL buffer immediately on the host. The optional <code>property_list</code> provides properties for the constructed SYCL <code>host_accessor</code> object.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`host_accessor(buffer&lt;dataT, dimensions, AllocatorT&gt;&amp; bufferRef,</td>
<td>Available only when: (dimensions &gt; 0). Constructs a <code>host_accessor</code> for accessing a range of a SYCL buffer immediately on the host. The tag is used to deduce template arguments of the <code>host_accessor</code> as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL <code>host_accessor</code> object.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>TagT tag, const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`host_accessor(buffer&lt;dataT, dimensions, AllocatorT&gt;&amp; bufferRef,</td>
<td>Available only when: (dimensions &gt; 0). Constructs a <code>host_accessor</code> for accessing a range of a SYCL buffer immediately on the host. The range is specified by accessRange and accessOffset. The optional <code>property_list</code> provides properties for the constructed SYCL <code>host_accessor</code> object.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>id&lt;dimensions&gt; accessOffset,</td>
<td></td>
</tr>
<tr>
<td>const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>`host_accessor(buffer&lt;dataT, dimensions, AllocatorT&gt;&amp; bufferRef,</td>
<td>Available only when: (dimensions &gt; 0). Constructs a <code>host_accessor</code> for accessing a range of a SYCL buffer immediately on the host. The range is specified by accessRange and accessOffset. The tag is used to deduce template arguments of the <code>host_accessor</code> as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL <code>host_accessor</code> object.</td>
</tr>
<tr>
<td>handler &amp;commandGroupHandlerRef,</td>
<td></td>
</tr>
<tr>
<td>range&lt;dimensions&gt; accessRange,</td>
<td></td>
</tr>
<tr>
<td>const property_list &amp;propList = {})</td>
<td></td>
</tr>
</tbody>
</table>

Table 4.53: Constructors of the `host_accessor` class template.
### Constructors

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>host_accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp;bufferRef,</code></td>
<td>Available only when: ((\text{dimensions} &gt; 0)). Constructs a <code>host_accessor</code> for accessing a range of a SYCL buffer inside a host task. The tag is used to deduce template arguments of the <code>host_accessor</code> as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL <code>host_accessor</code> object.</td>
</tr>
<tr>
<td><code>handler &amp;commandGroupHandlerRef,</code></td>
<td></td>
</tr>
<tr>
<td><code>range&lt;dimensions&gt; accessRange,</code></td>
<td></td>
</tr>
<tr>
<td><code>TagT tag, const property_list &amp;propList = {}</code>).`</td>
<td></td>
</tr>
<tr>
<td><code>host_accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp;bufferRef,</code></td>
<td>Available only when: ((\text{dimensions} &gt; 0)). Constructs a <code>host_accessor</code> for accessing a range of a SYCL buffer inside a host task. The range is specified by <code>accessRange</code> and <code>accessOffset</code>. The optional <code>property_list</code> provides properties for the constructed SYCL <code>host_accessor</code> object.</td>
</tr>
<tr>
<td><code>handler &amp;commandGroupHandlerRef,</code></td>
<td></td>
</tr>
<tr>
<td><code>range&lt;dimensions&gt; accessRange,</code></td>
<td></td>
</tr>
<tr>
<td><code>id&lt;dimensions&gt; accessOffset,</code></td>
<td></td>
</tr>
<tr>
<td><code>const property_list &amp;propList = {}</code>).`</td>
<td></td>
</tr>
<tr>
<td><code>host_accessor(buffer&lt;dataT, dimensions, AllocatorT&gt; &amp;bufferRef,</code></td>
<td>Available only when: ((\text{dimensions} &gt; 0)). Constructs a <code>host_accessor</code> for accessing a range of a SYCL buffer inside a host task. The range is specified by <code>accessRange</code> and <code>accessOffset</code>. The tag is used to deduce template arguments of the <code>host_accessor</code> as described in Section 4.7.6.3. The optional <code>property_list</code> provides properties for the constructed SYCL <code>host_accessor</code> object.</td>
</tr>
<tr>
<td><code>handler &amp;commandGroupHandlerRef,</code></td>
<td></td>
</tr>
<tr>
<td><code>range&lt;dimensions&gt; accessRange,</code></td>
<td></td>
</tr>
<tr>
<td><code>id&lt;dimensions&gt; accessOffset,</code></td>
<td></td>
</tr>
<tr>
<td><code>TagT tag, const property_list &amp;propList = {}</code>).`</td>
<td></td>
</tr>
</tbody>
</table>

End of table

### Member Functions

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>void swap(host_accessor &amp;other);</code></td>
<td>Swaps the contents of the current accessor with the contents of other.</td>
</tr>
<tr>
<td><code>size_type byte_size() const noexcept</code></td>
<td>Returns the size in bytes of the region of a SYCL buffer that this SYCL <code>host_accessor</code> is accessing.</td>
</tr>
<tr>
<td><code>size_type size() const noexcept</code></td>
<td>Returns the number of elements in the region of a SYCL buffer that this SYCL <code>host_accessor</code> is accessing.</td>
</tr>
<tr>
<td><code>size_type max_size() const noexcept</code></td>
<td>Returns the maximum number of elements any accessor of this type would be able to access.</td>
</tr>
<tr>
<td><code>bool empty() const noexcept</code></td>
<td>Returns <code>true</code> iff ((\text{size}() == 0)).</td>
</tr>
</tbody>
</table>

Continued on next page
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>range&lt;dimensions&gt; get_range() const</td>
<td>Available only when: dimensions &gt; 0. Returns a range object which represents the number of elements of dataT per dimension that this host_accessor may access. The range object returned must equal to the range of the buffer this host_accessor is associated with, unless a range was explicitly specified when this host_accessor was constructed.</td>
</tr>
<tr>
<td>id&lt;dimensions&gt; get_offset() const</td>
<td>Available only when: dimensions &gt; 0. Returns an id object which represents the starting point in number of elements of dataT for the range that this host_accessor may access. The id object returned must equal to id {0, 0, 0}, unless an offset was explicitly specified when this host_accessor was constructed.</td>
</tr>
<tr>
<td>operator reference() const</td>
<td>Available only when: (dimensions == 0). Returns a reference to the element stored within the SYCL buffer this SYCL host_accessor is accessing.</td>
</tr>
<tr>
<td>reference operator[](id&lt;dimensions&gt; index) const</td>
<td>Available only when: (dimensions &gt; 0). Returns a reference to the element stored within the SYCL buffer this SYCL host_accessor is accessing at the index specified by index.</td>
</tr>
<tr>
<td><strong>unspecified</strong> &amp;operator[](size_t index) const</td>
<td>Available only when: dimensions &gt; 1. Returns an instance of an undefined intermediate type representing a SYCL host_accessor of the same type as this SYCL host_accessor, with the dimensionality dimensions-1 and containing an implicit SYCL id with index dimensions set to index. The intermediate type returned must provide all available subscript operators which take a size_t parameter defined by the SYCL host_accessor class that are appropriate for the type it represents (including this subscript operator).</td>
</tr>
<tr>
<td>iterator data() const noexcept</td>
<td>Returns a pointer to the memory this SYCL host_accessor memory is accessing.</td>
</tr>
<tr>
<td>iterator begin() const noexcept</td>
<td>Returns an iterator to the first element of the memory within the access range.</td>
</tr>
<tr>
<td>iterator end() const noexcept</td>
<td>Returns an iterator that points past the last element of the memory within the access range.</td>
</tr>
</tbody>
</table>

Table 4.54: Member functions of the host_accessor class template.
Table 4.54: Member functions of the `host_accessor` class template.

### 4.7.6.10.2 Host buffer accessor properties

The `host_accessor` supports the same list of properties as a device buffer accessor listed in 4.7.6.9.2.

### 4.7.6.11 Local accessor

A local accessor provides access to SYCL runtime allocated shared memory via local memory. A SYCL accessor is considered a local accessor if it has the access target `target::local`. The memory allocated by a local accessor is non-initialised so it is the user’s responsibility to construct and destroy objects explicitly if required. The local memory that is allocated is shared between all work-items of a work-group.

A local accessor does not provide access on the host and the memory can not be copied back to the host.

The data type of a local accessor can be any valid SYCL kernel argument (see Section 4.14.4).

Unless the dimensionality of the local accessor is 0, the size of memory allocated by the SYCL runtime is specified by a `range` provided on construction. The dimensionality of this `range` must match the dimensionality of the local accessor.

There are three ways that a SYCL accessor can provide access to the elements of the allocated memory. Firstly by passing a SYCL `id` instance of the same dimensionality as the SYCL accessor subscript operator. Secondly by passing a single `size_t` value to multiple consecutive subscript operators (one for each dimension of the SYCL accessor, for example `acc[z][y][x]`). Finally, in the case of the SYCL accessor having 0 dimensions, by triggering the implicit conversion operator. Whenever a multi-dimensional index is passed to a SYCL accessor, the linear index is calculated based on the index `{id0, id1, id2}` provided and the range of the SYCL accessor `{r0, r1, r2}` according to row-major ordering as follows:

\[
id2 + (id1 \cdot r2) + (id0 \cdot r2 \cdot r1)
\] (4.5)

A local accessor can optionally provide atomic access to allocated memory, using the access mode `access_mode::atomic`, in which case all operators which return an element of the allocated memory return an instance of the SYCL atomic class. This is deprecated in SYCL 2020.

Local accessors are not valid in the `single_task` or basic `parallel_for` SYCL kernel function invocations, due the fact that local work-groups are implicitly created, and the implementation is free to choose any size.

A local accessor meets the C++ requirement of `ContiguousContainer`. The iterator for this container is `multi_ptr<dataT, access::address_space::local_space, access::decorated::no>`. For multidimensional accessors the iterator linearizes the data according to 4.3.

The full list of capabilities that local accessors can support is described in 4.55.
<table>
<thead>
<tr>
<th>Access target</th>
<th>Accessor type</th>
<th>Access modes</th>
<th>Data types</th>
<th>Dimensionalities</th>
<th>Placeholder</th>
</tr>
</thead>
<tbody>
<tr>
<td>local</td>
<td>device</td>
<td>read_write</td>
<td>atomic</td>
<td>All available data types supported in a SYCL kernel function.</td>
<td>Between 0 and 3 (inclusive).</td>
</tr>
</tbody>
</table>

Table 4.55: Description of all the local accessor capabilities.

### 4.7.6.11.1 Local accessor interface

A synopsis of the SYCL accessor class template local specialization is provided below. The member types for this accessor specialization are listed in Tables 4.56. The constructors for this accessor specialization are listed in Tables 4.57. The member functions for this accessor specialization are listed in Tables 4.58. The additional common special member functions and common member functions are listed in 4.5.3 in Tables 4.1 and 4.2, respectively. For valid implicit conversions between accessor types please refer to 4.7.6.8. Additionally, local accessors of the same type must be equality comparable.

```cpp
namespace sycl {
    template <typename dataT,
              int dimensions,
              access::mode accessmode,
              access::target accessTarget,
              access::placeholder isPlaceholder>
    class accessor {
        public:
            template <typename value_type, access::decorated IsDecorated>
            using accessor_ptr = multi_ptr<value_type, access::address_space::local_space, IsDecorated>;
            using value_type = dataT;
            using reference = dataT &;
            using const_reference = const dataT &;
            using iterator = accessor_ptr<dataT, access::decorated::no>;
            using const_iterator = accessor_ptr<const dataT, access::decorated::no>;
            using reverse_iterator = std::reverse_iterator<iterator>;
            using const_reverse_iterator = std::reverse_iterator<const_iterator>;
            using difference_type = typename std::iterator_traits<iterator>::difference_type;
            using size_type = size_t;

            accessor();

            /* Available only when: dimensions == 0 */
            accessor(handler &commandGroupHandlerRef,
                     const property_list &propList = {});

            /* Available only when: dimensions > 0 */
            accessor(range<dimensions> allocationSize, handler &commandGroupHandlerRef,
                     const property_list &propList = {});

            /* -- common interface members -- */
            void swap(accessor &other);

            size_type byte_size() const noexcept;
    }
}
```

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size_type size() const noexcept;

size_type max_size() const noexcept;

size_type get_count() const noexcept;

bool empty() const noexcept;

range<dimensions> get_range() const;

/* Available only when: (dimensions == 0) */
operator reference() const;

/* Available only when: (dimensions > 0) */
reference operator[](id<dimensions> index) const;

/* Deprecated in SYCL 2020
Available only when: accessMode == access::mode::atomic && dimensions == 0 */
operator cl::sycl::atomic<dataT, access::address_space::local_space> () const;

/* Deprecated in SYCL 2020
Available only when: accessMode == access::mode::atomic && dimensions > 0 */
class cl::sycl::atomic<dataT, access::address_space::local_space> operator[](id<dimensions> index) const;

/* Available only when: dimensions > 1 */
__unspecified__ &operator[](size_t index) const;

std::add_pointer_t<value_type> get_pointer() const noexcept;

template <access::decorated IsDecorated>
accessor_ptr<value_type, IsDecorated> get_multi_ptr() const noexcept;

iterator data() const noexcept;

iterator begin() const noexcept;

iterator end() const noexcept;

const_iterator cbegin() const noexcept;

const_iterator cend() const noexcept;

reverse_iterator rbegin() const noexcept;

reverse_iterator rend() const noexcept;

const_reverse_iterator crbegin() const noexcept;

const_reverse_iterator crend() const noexcept;

}; // namespace sycl

Listing 4.5: Accessor class for locals.
4.7. DATA ACCESS AND STORAGE IN SYCL

<table>
<thead>
<tr>
<th>Member types</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>template &lt;access::decorated IsDecorated&gt; accessor_ptr</td>
<td>multi_ptr&lt;value_type, access::address_space::local_space, IsDecorated&gt;</td>
</tr>
<tr>
<td>value_type</td>
<td>dataT</td>
</tr>
<tr>
<td>reference</td>
<td>dataT&amp;</td>
</tr>
<tr>
<td>const_reference</td>
<td>const dataT&amp;</td>
</tr>
<tr>
<td>iterator</td>
<td>raw_local_ptr&lt;value_type&gt;</td>
</tr>
<tr>
<td>const_iterator</td>
<td>raw_local_ptr&lt;const value_type&gt;</td>
</tr>
<tr>
<td>reverse_iterator</td>
<td>Iterator adaptor that reverses the direction of iterator.</td>
</tr>
<tr>
<td>const_reverse_iterator</td>
<td>Iterator adaptor that reverses the direction of const_iterator.</td>
</tr>
<tr>
<td>difference_type</td>
<td>typename std::iterator_traits&lt;iterator&gt;::difference_type</td>
</tr>
<tr>
<td>size_type</td>
<td>size_t</td>
</tr>
</tbody>
</table>

End of table

Table 4.56: Member types of the accessor class template local specialization.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
</table>
| accessor() | Constructs an empty accessor. Fulfills the following post-conditions:  
  - (empty()) == true  
  - All size queries return 0.  
  - The only iterator that can be obtained is nullptr.  
  - Trying to access the underlying memory is undefined behavior. |
| accessor(handler &commandGroupHandlerRef, const property_list &propList = {}) | Available only when: dimensions == 0. Constructs a SYCL accessor instance for accessing runtime allocated local memory of a single element (of type dataT) within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef. The allocation is per work-group. The optional property_list provides properties for the constructed SYCL accessor object. |

Table 4.57:Constructors of the accessor class template local specialization.
### Constructor Description

| accessor(range<dimensions> allocationSize, handler &commandGroupHandlerRef, const property_list &propList = {}) | Available only when: dimensions > 0. Constructs a SYCL accessor instance for accessing runtime allocated local memory of size specified by allocationSize within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef. allocationSize defines the number of elements of type dataT to be allocated. The allocation is per work-group, and if multiple work-groups execute simultaneously in an implementation, each work-group will receive its own functionally independent allocation of size allocationSize elements of type dataT. The optional property_list provides properties for the constructed SYCL accessor object. |

| void swap(accessor &other); | Swaps the contents of the current accessor with the contents of other. |
| size_type byte_size()const noexcept | Returns the size in bytes of the local memory allocation, per work-group, that this SYCL accessor is accessing. |
| size_type size()const noexcept | Returns the number of dataT elements in the local memory allocation, per work-group, that this SYCL accessor is accessing. |
| size_type max_size()const noexcept | Returns the maximum number of elements any accessor of this type would be able to access. |
| size_type get_count()const noexcept | Returns the same as size(). |
| bool empty()const noexcept | Returns true iff (size()== 0). |
| range<dimensions> get_range()const | Available only when: dimensions > 0. Returns a range object which represents the number of elements of dataT per dimension that this accessor may access, per work-group. |
| operator reference()const | Available only when: (dimensions == 0). Returns a reference to the single element stored within the work-group’s local memory allocation that this accessor is accessing. |

Table 4.57: Constructors of the accessor class template local specialization.

Table 4.58: Member functions of the accessor class template local specialization.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>reference <strong>operator[](id&lt;dimensions&gt; index)const</strong></td>
<td>Available only when: (dimensions &gt; 0). Returns a reference to the element stored within the work-group’s local memory allocation that this SYCL accessor is accessing, at the index specified by index.</td>
</tr>
<tr>
<td><strong>operator cl::sycl::atomic&lt;dataT, access::address_space::local_space&gt; &amp;()const</strong></td>
<td>Deprecated in SYCL 2020. Available only when: accessMode == access_mode::atomic &amp; dimensions == 0). Returns a reference to an instance of cl::sycl::atomic of type dataT providing atomic access to the element stored within the work-group’s local memory allocation that this SYCL accessor is accessing.</td>
</tr>
<tr>
<td><strong>cl::sycl::atomic&lt;dataT, access::address_space::local_space&gt; &amp; operator[](id&lt;dimensions&gt; index)const</strong></td>
<td>Deprecated in SYCL 2020. Available only when: accessMode == access_mode::atomic &amp; dimensions &gt; 0). Returns a reference to an instance of cl::sycl::atomic of type dataT providing atomic access to the element stored within the work-group’s local memory allocation that this SYCL accessor is accessing, at the index specified by index.</td>
</tr>
<tr>
<td><strong><strong>unspecified</strong> &amp; operator[](size_t index)const</strong></td>
<td>Available only when: dimensions &gt; 1. Returns an instance of an undefined intermediate type representing a SYCL accessor of the same type as this SYCL accessor, with the dimensionality dimensions-1 and containing an implicit SYCL id with index dimensions set to index. The intermediate type returned must provide all available subscript operators which take a size_t parameter defined by the SYCL accessor class that are appropriate for the type it represents (including this subscript operator).</td>
</tr>
<tr>
<td><strong>std::add_pointer_t&lt;value_type&gt; get_pointer()const noexcept</strong></td>
<td>Returns a pointer to the work-group’s local memory allocation that this SYCL accessor is accessing.</td>
</tr>
<tr>
<td><strong>template &lt;access::decorated IsDecorated&gt; accessor_ptr&lt;IsDecorated&gt; get_multi_ptr()const noexcept</strong></td>
<td>Returns a multi_ptr to the work-group’s local memory allocation that this SYCL accessor is accessing.</td>
</tr>
<tr>
<td><strong>iterator data()const noexcept</strong></td>
<td>Returns a pointer to the work-group’s local memory allocation that this SYCL accessor is accessing.</td>
</tr>
<tr>
<td><strong>iterator begin()const noexcept</strong></td>
<td>Returns an iterator to the first element of allocated local memory.</td>
</tr>
</tbody>
</table>

Table 4.58: Member functions of the accessor class template local specialization.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>iterator end() const noexcept</td>
<td>Returns an iterator that points past the last element of allocated local memory.</td>
</tr>
<tr>
<td>const_iterator cbegin() const noexcept</td>
<td>Returns a const iterator to the first element of allocated local memory.</td>
</tr>
<tr>
<td>const_iterator cend() const noexcept</td>
<td>Returns a const iterator that points past the last element of allocated local memory.</td>
</tr>
<tr>
<td>reverse_iterator rbegin() const noexcept</td>
<td>Returns an iterator adaptor to the last element of the memory of allocated local memory.</td>
</tr>
<tr>
<td>reverse_iterator rend() const noexcept</td>
<td>Returns an iterator adaptor that points before the first element of the memory of allocated local memory.</td>
</tr>
<tr>
<td>const_reverse_iterator crbegin() const noexcept</td>
<td>Returns a const iterator adaptor to the last element of the memory of allocated local memory.</td>
</tr>
<tr>
<td>const_reverse_iterator crend() const noexcept</td>
<td>Returns a const iterator adaptor that points before the first element of the memory of allocated local memory.</td>
</tr>
</tbody>
</table>

Table 4.58: Member functions of the accessor class template local specialization.

4.7.6.11.2 Local accessor properties

The property_list constructor parameters are present for extensibility.

4.7.6.12 Image accessor

An image accessor provides access to either an instance of a SYCL unsampled_image or sampled_image. A SYCL accessor is considered an image accessor if it has the access target target::unsampled_image, target::sampled_image, target::host_unsampled_image or target::host_sampled_image.

An image accessor can provide access to memory managed by a SYCL unsampled_image or sampled_image class, using the access target target::unsampled_image or target::sampled_image.

Alternatively an image accessor can provide access to memory managed by a SYCL unsampled_image or sampled_image immediately on the host, using the access target target::host_unsampled_image or target::host_sampled_image, respectively. If the SYCL image this SYCL accessor is accessing was constructed with the property property::image::use_host_ptr the address of the memory accessed on the host must be the address the SYCL image was constructed with, otherwise the SYCL runtime is free to allocate temporary memory to provide access on the host.

The data type of an image accessor must be either int4, uint4, float4 or half4.

The dimensionality of an image accessor must match that of the SYCL image which it is providing access to, with the exception of when the access target is target::image_array, in which case the dimensionality of the SYCL accessor must be 1 less.

An image accessor with the access target target::image or target::host_image can provide access to the elements of a SYCL image by passing a SYCL int4 or float4 instance to the read or write member functions.
An image accessor with the access target `target::image_array` can provide access to a slice of an image array by passing a `size_t` value to the subscript operator. This returns an instance of `__image_array_slice__`, an unspecified type providing the interface of `accessor<dataT, dimensions, mode, target::image>`, which will provide access to a slice of the image array specified by index. The `__image_array_slice__` returned can then provide access via the `read` or `write` member functions as described above. For example `acc[arrayIndex].read(coords)`.

The full list of capabilities that image accessors can support is described in 4.59.

<table>
<thead>
<tr>
<th>Access target</th>
<th>Accessor type</th>
<th>Access modes</th>
<th>Data types</th>
<th>Dimensionalities</th>
<th>Placeholder</th>
</tr>
</thead>
<tbody>
<tr>
<td>unsampled_image</td>
<td>device</td>
<td>read, write, discard_write</td>
<td>int4, uint4, float4, half4</td>
<td>Between 1 and 3 (inclusive).</td>
<td>No</td>
</tr>
<tr>
<td>sampled_image</td>
<td>device</td>
<td>read</td>
<td>int4, uint4, float4, half4</td>
<td>Between 1 and 3 (inclusive).</td>
<td>No</td>
</tr>
<tr>
<td>host_unsampled_image</td>
<td>host</td>
<td>read, write, discard_write</td>
<td>int4, uint4, float4, half4</td>
<td>Between 1 and 3 (inclusive).</td>
<td>No</td>
</tr>
<tr>
<td>host_sampled_image</td>
<td>host</td>
<td>read</td>
<td>int4, uint4, float4, half4</td>
<td>Between 1 and 3 (inclusive).</td>
<td>No</td>
</tr>
</tbody>
</table>

Table 4.59: Description of all the image accessor capabilities.

### 4.7.6.12.1 Image accessor interface

A synopsis of the SYCL accessor class template image specialization is provided below. The constructors and member functions of the SYCL accessor class template image specialization are listed in Tables 4.60 and 4.61 respectively. The additional common special member functions and common member functions are listed in 4.5.3 in Tables 4.1 and 4.2, respectively. For valid implicit conversions between accessor types please refer to 4.7.6.8.

```cpp
namespace sycl {

  template <typename dataT,
             int dimensions,
             access::mode accessMode,
             access::target accessTarget>
  class accessor {
    public:
      using value_type = dataT;
      using reference = dataT &;
      using const_reference = const dataT &;

      /* Available only when: accessTarget == access::target::host_unsampled_image */
      template <typename AllocatorT>
      accessor(unsampled_image<dimensions, AllocatorT>& imageRef);
  }

```

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Listing 4.6: Accessor interface for images.
### 4.7. DATA ACCESS AND STORAGE IN SYCL

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>template &lt;typename AllocatorT&gt;</td>
<td>Constructs a SYCL accessor instance for accessing a SYCL unsampled_image immediately on the host. The optional property_list provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>accessor(unsampled_image&lt;dimensions, AllocatorT&gt;, &amp;imageRef, const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>template &lt;typename AllocatorT&gt;</td>
<td>Constructs a SYCL accessor instance for accessing a SYCL sampled_image immediately on the host. The optional property_list provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>accessor(sampled_image&lt;dimensions, AllocatorT&gt;, &amp;imageRef, const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>template &lt;typename AllocatorT&gt;</td>
<td>Constructs a SYCL accessor instance for accessing a SYCL unsampled_image within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef. The optional property_list provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>accessor(unsampled_image&lt;dimensions, AllocatorT&gt;, &amp;imageRef, handler &amp;commandGroupHandlerRef, const property_list &amp;propList = {})</td>
<td></td>
</tr>
<tr>
<td>template &lt;typename AllocatorT&gt;</td>
<td>Constructs a SYCL accessor instance for accessing a SYCL sampled_image within a SYCL kernel function on the SYCL queue associated with commandGroupHandlerRef. The optional property_list provides properties for the constructed SYCL accessor object.</td>
</tr>
<tr>
<td>accessor(sampled_image&lt;dimensions, AllocatorT&gt;, &amp;imageRef, handler &amp;commandGroupHandlerRef, const property_list &amp;propList = {})</td>
<td></td>
</tr>
</tbody>
</table>

End of table

Table 4.60: Constructors of the accessor class template image specialization.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>size_t get_size() const</td>
<td>Returns the size in bytes of the SYCL unsampled_image or sampled_image this SYCL accessor is accessing.</td>
</tr>
<tr>
<td>size_t get_count() const</td>
<td>Returns the number of elements of the SYCL unsampled_image or sampled_image this SYCL accessor is accessing.</td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.61: Member functions of the accessor class template image specialization.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `template <typename coordT>
dataT read(const coordT &coords)const` | Available only when: (accessTarget == target::unsampled_image && accessMode == access_mode::read) || (accessTarget == target::host_unsampled_image && accessMode == access_mode::read). Reads and returns an element of the unsampled_image at the coordinates specified by coords. Permitted types for coordT are `int` when dimensions == 1, `int2` when dimensions == 2 and `int4` when dimensions == 3. |
| `template <typename coordT>
dataT read(const coordT &coords)const` | Available only when: (accessTarget == target::sampled_image && accessMode == access_mode::read) || (accessTarget == target::host_sampled_image && accessMode == access_mode::read). Reads and returns a sampled element of the sampled_image at the coordinates specified by coords. Permitted types for coordT are `float` when dimensions == 1, `float2` when dimensions == 2 and `float4` when dimensions == 3. |
| `template <typename coordT>
void write(const coordT &coords, const dataT &color) const` | Available only when: (accessTarget == target::unsampled_image && (accessMode == access_mode::write || accessMode == access_mode::discard_write)) || (accessTarget == target::host_unsampled_image && (accessMode == access_mode::write || accessMode == access_mode::discard_write)). Writes the value specified by color to the element of the image at the coordinates specified by coords. Permitted type for coordT are `int` when dimensions == 1, `int2` when dimensions == 2 and `int4` when dimensions == 3. |
| `__image_array_slice__ operator[](size_t index)const` | Available only when: accessTarget == target::image_array && dimensions < 3 . Returns an instance of __image_array_slice__, an unspecified type which provides the interface of accessor< dataT, dimensions, mode, target::image > which will provide access to a slice of the image array specified by index. |

Table 4.61: Member functions of the accessor class template image specialization.
4.7. DATA ACCESS AND STORAGE IN SYCL

4.7.6.12.2 Image accessor properties

The property_list constructor parameters are present for extensibility.

4.7.7 Address space classes

In SYCL, there are five different address spaces: global, local, constant, private and generic. In a SYCL generic implementation, types are not affected by the address spaces. However, there are situations where users need to explicitly carry address spaces in the type. For example:

- For performance tuning and genericity. Even if the platform supports the representation of the generic address space, this may come at some performance sacrifice. In order to help the target compiler, it can be useful to track specifically which address space a pointer is addressing.

- When linking SYCL kernels with SYCL backend-specific functions. In this case, it might be necessary to specify the address space for any pointer parameters.

Direct declaration of pointers with address spaces is discouraged as the definition is implementation defined. Users must rely on the multi_ptr class to handle address space boundaries and interoperability.

4.7.7.1 Multi-pointer class

The multi-pointer class is the common interface for the explicit pointer classes, defined in 4.7.7.2.

There are situations where a user may want to make their type address space dependent. This allows performing generic programming that depends on the address space associated with their data. An example might be wrapping a pointer inside a class, where a user may need to template the class according to the address space of the pointer the class is initialized with. In this case, the multi_ptr class enables users to do this in a portable and stable way.

The multi_ptr class exposes 3 flavors of the same interface. If the value of access::decorated is access::decorated::no, the interface exposes pointers and references type that are not decorated by an address space. If the value of access::decorated is access::decorated::yes, the interface exposes pointers and references type that are decorated by an address space. The decoration is implementation dependent and relies on device compiler extensions. The decorated type may be distinct from the non-decorated one. For interoperability with the SYCL backend, users should rely on types exposed by the decorated version. If the value of access::decorated is access::decorated::legacy, the 1.2.1 interface is exposed. This interface is deprecated.

The template traits remove_decoration and type alias remove_decoration_t retrieve the non-decorated pointer or reference from a decorated one. Using this template trait with a non-decorated type is safe and returns the same type.

It is possible to use the void type for the multi_ptr class, but in that case some functionality is disabled. multi_ptr<void> does not provide the reference or const_reference types, the access operators (operator *, operator->()), the arithmetic operators or prefetch member function. Conversions from multi_ptr to multi_ptr<void> of the same address space are allowed, and will occur implicitly. Conversions from multi_ptr<void> to any other multi_ptr type of the same address space are allowed, but must be explicit. The same rules apply to multi_ptr<const void>.

An overview of the interface provided for the multi_ptr class follows.

```cpp
namespace sycl {
namespace access {
```
enum class address_space : int {
    global_space,
    local_space,
    constant_space,
    private_space,
    generic_space,
};

enum class decorated : int {
    no,
    yes,
    legacy,
};

} // namespace access

template<typename T> struct remove_decoration {
    using type = /* ... */;
};

template<typename T>
using remove_decoration_t = remove_decoration::type;

template <typename ElementType, access::address_space Space, access::decorated DecorateAddress>
class multi_ptr {
public:
    static constexpr bool is_decorated = DecorateAddress == access::decorated::yes;
    static constexpr access::address_space address_space = Space;

    using value_type = ElementType;
    using pointer = std::conditional<is_decorated, __unspecified__ *,
        std::add_pointer_t<value_type>>;
    using reference = std::conditional<is_decorated, __unspecified__ &,
        std::add_lvalue_reference_t<value_type>>;
    using iterator_category = std::random_access_iterator_tag;
    using difference_type = std::ptrdiff_t;

    static_assert(std::is_same_v<remove_decoration_t<pointer>, std::add_pointer_t<value_type>>);
    static_assert(std::is_same_v<remove_decoration_t<reference>, std::add_lvalue_reference_t<
        value_type>>);

    // Legacy has a different interface.
    static_assert(DecorateAddress != access::decorated::legacy);

    // Constructors
    multi_ptr();
    multi_ptr(const multi_ptr&);
    multi_ptr(multi_ptr&&);
    explicit multi_ptr(multi_ptr<ElementType, Space, yes>::pointer);
    multi_ptr(std::nullptr_t);

    // Only if Space == global_space or generic_space
    template <int dimensions, access::mode Mode, access::placeholder isPlaceholder>
    multi_ptr(accessor<value_type, dimensions, Mode, access::target::global_buffer, isPlaceholder>);
    multi_ptr<ElementType, Space, yes>::pointer);

    // Only if Space == local_space or generic_space

template <int dimensions, access::mode Mode, access::placeholder isPlaceholder>
multi_ptr(accessor<value_type, dimensions, Mode, access::target::local, isPlaceholder>);

// Only if Space == constant_space
template <int dimensions, access::mode Mode, access::placeholder isPlaceholder>
multi_ptr(accessor<value_type, dimensions, Mode, access::target::constant_buffer, isPlaceholder>);

// Assignment and access operators
multi_ptr &operator=(const multi_ptr&);
multi_ptr &operator=(multi_ptr&&);
multi_ptr &operator=(std::nullptr_t);

// Only if Space == address_space::generic_space
// and ASP != access::address_space::constant_space
template<access::address_space ASP, access::decorated IsDecorated>
multi_ptr &operator=(const multi_ptr<value_type, ASP, IsDecorated>&);

// Only if Space == address_space::generic_space
// and ASP != access::address_space::constant_space
template<access::address_space ASP, access::decorated IsDecorated>
multi_ptr &operator=(multi_ptr<value_type, ASP, IsDecorated>&&);

reference operator*() const;
pointer operator->() const;

pointer get() const;
std::add_pointer_t<value_type> get_raw() const;
__unspecified__ * get_decorated() const;

// Conversion to the underlying pointer type
// Deprecated, get() should be used instead.
operator pointer() const;

// Only if Space == address_space::generic_space
// Cast to private_ptr
explicit operator multi_ptr<value_type, access::address_space::private_space, DecorateAddress>() const;

// Only if Space == address_space::generic_space
// Cast to global_ptr
explicit operator multi_ptr<value_type, access::address_space::global_space, DecorateAddress>() const;

// Only if Space == address_space::generic_space
// Cast to local_ptr
explicit operator multi_ptr<value_type, access::address_space::local_space, DecorateAddress>() const;
// Only if Space == address_space::generic_space
// Cast to local_ptr
explicit
operator multi_ptr<const value_type, access::address_space::local_space, DecorateAddress>() const;

// Implicit conversion to a multi_ptr<void>.
// Only available when value_type is not const-qualified.
template<access::decorated DecorateAddress>
operator multi_ptr<void, Space, DecorateAddress>() const;

// Implicit conversion to a multi_ptr<const void>.
// Only available when value_type is const-qualified.
template<access::decorated DecorateAddress>
operator multi_ptr<const void, Space, DecorateAddress>() const;

// Implicit conversion to multi_ptr<const value_type, Space>.
// Only available when is_decorated is true.
operator multi_ptr<const value_type, Space, access::decorated::no>() const;

// Implicit conversion to the non-decorated version of multi_ptr.
// Only available when is_decorated is false.
operator multi_ptr<value_type, Space, access::decorated::yes>() const;

void prefetch(size_t numElements) const;

// Arithmetic operators
friend multi_ptr& operator++(multi_ptr& mp) { /* ... */ }
friend multi_ptr operator++(multi_ptr& mp, int) { /* ... */ }
friend multi_ptr& operator--(multi_ptr& mp) { /* ... */ }
friend multi_ptr operator--(multi_ptr& mp, int) { /* ... */ }
friend multi_ptr& operator+=(multi_ptr& lhs, difference_type r) { /* ... */ }
friend multi_ptr& operator-=(multi_ptr& lhs, difference_type r) { /* ... */ }
friend multi_ptr operator+(const multi_ptr& lhs, difference_type r) { /* ... */ }
friend multi_ptr operator-(const multi_ptr& lhs, difference_type r) { /* ... */ }
friend reference operator*(const multi_ptr& lhs) { /* ... */ }
friend bool operator==(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator==(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator!=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator<(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator>(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator<=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator>=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }

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friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }

friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }

friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }

friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }

friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }

friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
pointer get() const;

// Conversion to the underlying pointer type
explicit operator pointer() const;

// Explicit conversion to a multi_ptr<ElementType>
// If VoidType is const, ElementType must be as well
template <typename ElementType>
explicit operator multi_ptr<ElementType, Space, DecorateAddress>() const;

// Implicit conversion to the non-decorated version of multi_ptr.
// Only available when is_decorated is true.
operator multi_ptr<value_type, Space, access::decorated::no>() const;

// Implicit conversion to the decorated version of multi_ptr.
// Only available when is_decorated is false.
operator multi_ptr<value_type, Space, access::decorated::yes>() const;

// Implicit conversion to multi_ptr<const void, Space>
operator multi_ptr<const void, Space, DecorateAddress>() const;

friend bool operator==(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }

friend bool operator==(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator!=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator<(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator>(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator<=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator>=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }

friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }

};

// Deprecated, address_space_cast should be used instead.
template <typename ElementType, access::address_space Space, access::decorated DecorateAddress>
multi_ptr<ElementType, Space, DecorateAddress> make_ptr(ElementType *);

template <access::address_space Space, access::decorated DecorateAddress, typename ElementType>
multi_ptr<ElementType, Space, DecorateAddress> address_space_cast(ElementType *);

// Deduction guides
template <int dimensions, access::mode Mode, access::placeholder isPlaceholder,
### Table 4.62: Constructors of the SYCL `multi_ptr` class template.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename ElementType, access::address_space Space, access::decorated DecorateAddress&gt; multi_ptr()</code></td>
<td>Default constructor.</td>
</tr>
<tr>
<td><code>template &lt;typename ElementType, access::address_space Space, access::decorated DecorateAddress&gt; multi_ptr(const multi_ptr &amp;)</code></td>
<td>Copy constructor.</td>
</tr>
<tr>
<td><code>template &lt;typename ElementType, access::address_space Space, access::decorated DecorateAddress&gt; multi_ptr(multi_ptr&amp;&amp;)</code></td>
<td>Move constructor.</td>
</tr>
<tr>
<td><code>template &lt;typename ElementType, access::address_space Space, access::decorated DecorateAddress&gt; multi_ptr(multi_ptr&lt;ElementType, Space, yes&gt;::pointer)</code></td>
<td>Constructor that takes as an argument a decorated pointer.</td>
</tr>
<tr>
<td><code>template &lt;typename ElementType, access::address_space Space, access::decorated DecorateAddress&gt; multi_ptr(std::nullptr_t)</code></td>
<td>Constructor from a nullptr.</td>
</tr>
<tr>
<td><code>template &lt;typename ElementType, access::address_space Space = access::address_space::global_space&gt; multi_ptr(accessor&lt;ElementType, dimensions, Mode, access::target::global_buffer&gt;)</code></td>
<td>Available only when: <code>Space == access::address_space::global_space</code>. Constructs a <code>multi_ptr&lt;ElementType, access::address_space::global_space&gt;</code> from an accessor of <code>access::target::global_buffer</code>.</td>
</tr>
</tbody>
</table>

Continued on next page
**Constructor** | **Description**
---|---
\texttt{template<typename ElementType, access::address_space Space = access::address_space::local_space>}
\texttt{multi_ptr<dimensions, access::mode Mode>}
\texttt{accessor<ElementType, dimensions, Mode, access::target::local>)} | Available only when: \texttt{Space == access::address_space::local_space}.
Constructs a \texttt{multi_ptr<ElementType, access::address_space::local_space>} from an accessor of \texttt{access::target::local}.

\texttt{template<typename ElementType, access::address_space Space = access::address_space::constant_space>}
\texttt{multi_ptr<dimensions, access::mode Mode>}
\texttt{accessor<ElementType, dimensions, Mode, access::target::constant_buffer>)} | Available only when: \texttt{Space == access::address_space::constant_space}.
Constructs a \texttt{multi_ptr<ElementType, access::address_space::constant_space>} from an accessor of \texttt{access::target::constant_buffer}.

\texttt{template<typename ElementType, access::address_space Space, access::decorated DecorateAddress>}
\texttt{multi_ptr<ElementType, Space, DecorateAddress>}
\texttt{make_ptr(ElementType*)} | Global function to create a \texttt{multi_ptr} instance depending on the address space of the pointer type. An implementation must reject an argument if the deduced address space is not compatible with \texttt{Space}.

| **Operators** | **Description**
---|---
\texttt{template<typename value_type, access::address_space Space, access::decorated DecorateAddress>}
\texttt{multi_ptr &operator=(const multi_ptr&)} | Copy assignment operator.

\texttt{template<typename value_type, access::address_space Space, access::decorated DecorateAddress>}
\texttt{multi_ptr &operator=(multi_ptr&&)} | Move assignment operator.

\texttt{template<typename value_type, access::address_space Space, access::decorated DecorateAddress>}
\texttt{multi_ptr &operator=(std::nullptr_t)} | Assigns \texttt{nullptr} to the \texttt{multi_ptr}.

\texttt{template<access::address_space ASP, access::decorated IsDecorated>}
\texttt{multi_ptr &operator=(const multi_ptr<value_type, ASP, IsDecorated>&)} | Available only when: \texttt{Space == access::address_space::generic_space \&\& ASP != access::address_space::constant_space}.
Assigns the value of the left hand side \texttt{multi_ptr} into the \texttt{generic_ptr}.

\texttt{template<access::address_space ASP, access::decorated IsDecorated>}
\texttt{multi_ptr &operator=(multi_ptr<value_type, ASP, IsDecorated>&&)} | Available only when: \texttt{Space == access::address_space::generic_space \&\& ASP != access::address_space::constant_space}.
Move the value of the left hand side \texttt{multi_ptr} into the \texttt{generic_ptr}.

| **Table 4.62** | Constructors of the SYCL \texttt{multi_ptr} class template.

| **Table 4.63** | Operators of \texttt{multi_ptr} class.

Continued on next page
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#### SYCL 2020 provisional

<table>
<thead>
<tr>
<th>Operators</th>
<th>Description</th>
</tr>
</thead>
</table>
| template <typename value_type, access::address_space Space, access::decorated DecorateAddress>
  
  pointer operator->()const | Available only when: !std::is_void<
 value_type>::value. Returns the underlying pointer. |
| template <typename value_type, access::address_space Space, access::decorated DecorateAddress>
  
  reference operator*()const | Available only when: !std::is_void<
 value_type>::value. Returns a reference to the pointed value. |
| template <typename value_type, access::address_space Space, access::decorated DecorateAddress>
  
  operator pointer()const | Implicit conversion to the underlying pointer type. **Deprecated**: The member function `get` should be used instead |
| template <access::decorated IsDecorated>
  
  operator multi_ptr<value_type, access::address_space::private_space, IsDecorated>()const | Available only when: Space == access::address_space::generic_space. Conversion from `generic_ptr` to `private_ptr`. The result is undefined if the pointer does not address the private address space. |
| template <access::decorated IsDecorated>
  
  operator multi_ptr<const value_type, access::address_space::private_space, IsDecorated>()const | Available only when: Space == access::address_space::generic_space. Conversion from `generic_ptr` to `private_ptr`. The result is undefined if the pointer does not address the private address space. |
| template <access::decorated IsDecorated>
  
  operator multi_ptr<value_type, access::address_space::global_space, IsDecorated>()const | Available only when: Space == access::address_space::generic_space. Conversion from `generic_ptr` to `global_ptr`. The result is undefined if the pointer does not address the global address space. |
| template <access::decorated IsDecorated>
  
  operator multi_ptr<const value_type, access::address_space::global_space, IsDecorated>()const | Available only when: Space == access::address_space::generic_space. Conversion from `generic_ptr` to `global_ptr`. The result is undefined if the pointer does not address the global address space. |
| template <access::decorated IsDecorated>
  
  operator multi_ptr<value_type, access::address_space::local_space, IsDecorated>()const | Available only when: Space == access::address_space::generic_space. Conversion from `generic_ptr` to `local_ptr`. The result is undefined if the pointer does not address the local address space. |
| template <access::decorated IsDecorated>
  
  operator multi_ptr<const value_type, access::address_space::local_space, IsDecorated>()const | Available only when: Space == access::address_space::generic_space. Conversion from `generic_ptr` to `local_ptr`. The result is undefined if the pointer does not address the local address space. |

Table 4.63: Operators of `multi_ptr` class.

Continued on next page
### Operators

<table>
<thead>
<tr>
<th>Description</th>
<th>Template</th>
</tr>
</thead>
<tbody>
<tr>
<td>Available only when: !std::is_void&lt;value_type&gt;::value &amp;&amp; !std::is_const&lt;value_type&gt;::value. Implicit conversion to a <code>multi_ptr</code> of type <code>void</code>.</td>
<td>\template&lt;access::decorated IsDecorated&gt; operator multi_ptr&lt;void, Space, IsDecorated&gt;() const</td>
</tr>
<tr>
<td>Available only when: !std::is_void&lt;value_type&gt;::value &amp;&amp; std::is_const&lt;value_type&gt;::value. Implicit conversion to a <code>multi_ptr</code> of type <code>const void</code>.</td>
<td>\template&lt;access::decorated IsDecorated&gt; operator multi_ptr&lt;const void, Space, IsDecorated&gt;() const</td>
</tr>
<tr>
<td>Implicit conversion to a <code>multi_ptr</code> of type <code>const value_type</code>.</td>
<td>\template&lt;access::decorated IsDecorated&gt; operator multi_ptr&lt;const value_type, Space, IsDecorated&gt;() const</td>
</tr>
<tr>
<td>Available only when: is_decorated == true. Implicit conversion to the equivalent <code>multi_ptr</code> object that does not expose decorated pointers or references.</td>
<td>\template<a href="">access::decorated::no</a> operator multi_ptr&lt;const value_type, Space, access::decorated::no&gt;() const</td>
</tr>
<tr>
<td>Available only when: is_decorated == false. Implicit conversion to the equivalent <code>multi_ptr</code> object that exposes decorated pointers and references.</td>
<td>\template<a href="">access::decorated::yes</a> operator multi_ptr&lt;const value_type, Space, access::decorated::yes&gt;() const</td>
</tr>
</tbody>
</table>

End of table

### Member functions

<table>
<thead>
<tr>
<th>Description</th>
<th>Member function</th>
</tr>
</thead>
<tbody>
<tr>
<td>Returns the underlying pointer. Whether the pointer is decorated depends on the value of DecorateAddress.</td>
<td>pointer <code>get()</code> const</td>
</tr>
<tr>
<td>Returns the underlying pointer decorated by the address space that it addressed. Note that the support involves implementation defined device compiler extensions.</td>
<td><code>__unspecified__ * get_decorated()</code> const</td>
</tr>
<tr>
<td>Returns the underlying pointer, always undecorated.</td>
<td><code>std::add_pointer_t&lt;value_type&gt; get_raw()</code> const</td>
</tr>
<tr>
<td>Available only when: Space == access::address_space::global_space. Prefetches a number of elements specified by numElements into the global memory cache. This operation is an implementation defined optimization and does not effect the functional behavior of the SYCL kernel function.</td>
<td>void <code>prefetch(size_t numElements)</code> const</td>
</tr>
</tbody>
</table>

End of table
### Table 4.65: Hidden friend functions of the `multi_ptr` class.

<table>
<thead>
<tr>
<th>Hidden friend function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>reference operator*(const multi_ptr&amp; mp)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Operator that returns a reference to the value_type of mp.</td>
</tr>
<tr>
<td><code>multi_ptr&amp; operator++(multi_ptr&amp; mp)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Increments mp by 1 and returns mp.</td>
</tr>
<tr>
<td><code>multi_ptr operator++(multi_ptr mp, int)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Increments mp by 1 and returns a new multi_ptr with the value of the original mp.</td>
</tr>
<tr>
<td><code>multi_ptr&amp; operator--(multi_ptr&amp; mp)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Decrements mp by 1 and returns mp.</td>
</tr>
<tr>
<td><code>multi_ptr operator--(multi_ptr mp, int)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Decrements mp by 1 and returns a new multi_ptr with the value of the original mp.</td>
</tr>
<tr>
<td><code>multi_ptr&amp; operator+=(multi_ptr&amp; lhs, difference_type r)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Moves mp forward by r and returns lhs.</td>
</tr>
<tr>
<td><code>multi_ptr&amp; operator-=(multi_ptr&amp; lhs, difference_type r)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Moves mp backward by r and returns lhs.</td>
</tr>
<tr>
<td><code>multi_ptr operator+(const multi_ptr&amp; lhs, difference_type r)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Creates a new multi_ptr that points r forward compared to lhs.</td>
</tr>
<tr>
<td><code>multi_ptr operator-(const multi_ptr&amp; lhs, difference_type r)</code></td>
<td>Available only when: <code>!std::is_void&lt;ElementType&gt;::value</code>. Creates a new multi_ptr that points r backward compared to lhs.</td>
</tr>
<tr>
<td><code>bool operator==(const multi_ptr&amp; lhs, const multi_ptr&amp; rhs)</code></td>
<td>Comparison operator <code>==</code> for multi_ptr class.</td>
</tr>
<tr>
<td><code>bool operator!=(const multi_ptr&amp; lhs, const multi_ptr&amp; rhs)</code></td>
<td>Comparison operator <code>!=</code> for multi_ptr class.</td>
</tr>
<tr>
<td><code>bool operator&lt;(const multi_ptr&amp; lhs, const multi_ptr&amp; rhs)</code></td>
<td>Comparison operator <code>&lt;</code> for multi_ptr class.</td>
</tr>
<tr>
<td><code>bool operator&gt;(const multi_ptr&amp; lhs, const multi_ptr&amp; rhs)</code></td>
<td>Comparison operator <code>&gt;</code> for multi_ptr class.</td>
</tr>
<tr>
<td><code>bool operator&lt;=(const multi_ptr&amp; lhs, const multi_ptr&amp; rhs)</code></td>
<td>Comparison operator <code>&lt;=</code> for multi_ptr class.</td>
</tr>
<tr>
<td><code>bool operator&gt;=(const multi_ptr&amp; lhs, const multi_ptr&amp; rhs)</code></td>
<td>Comparison operator <code>&gt;=</code> for multi_ptr class.</td>
</tr>
<tr>
<td><code>bool operator==(const multi_ptr&amp; lhs, std::nullptr_t)</code></td>
<td>Comparison operator <code>==</code> for multi_ptr class with a std::nullptr_t.</td>
</tr>
</tbody>
</table>

Continued on next page
Hidden friend function | Description
--- | ---
bool operator!=(const multi_ptr& lhs, std::nullptr_t) | Comparison operator != for multi_ptr class with a std::nullptr_t.
bool operator<(const multi_ptr& lhs, std::nullptr_t) | Comparison operator < for multi_ptr class with a std::nullptr_t.
bool operator>(const multi_ptr& lhs, std::nullptr_t) | Comparison operator > for multi_ptr class with a std::nullptr_t.
bool operator<=(const multi_ptr& lhs, std::nullptr_t) | Comparison operator <= for multi_ptr class with a std::nullptr_t.
bool operator>=(const multi_ptr& lhs, std::nullptr_t) | Comparison operator >= for multi_ptr class with a std::nullptr_t.
bool operator==(std::nullptr_t, const multi_ptr& rhs) | Comparison operator == for multi_ptr class with a std::nullptr_t.
bool operator!=(std::nullptr_t, const multi_ptr& rhs) | Comparison operator != for multi_ptr class with a std::nullptr_t.
bool operator<(std::nullptr_t, const multi_ptr& rhs) | Comparison operator < for multi_ptr class with a std::nullptr_t.
bool operator>(std::nullptr_t, const multi_ptr& rhs) | Comparison operator > for multi_ptr class with a std::nullptr_t.
bool operator<=(std::nullptr_t, const multi_ptr& rhs) | Comparison operator <= for multi_ptr class with a std::nullptr_t.
bool operator>=(std::nullptr_t, const multi_ptr& rhs) | Comparison operator >= for multi_ptr class with a std::nullptr_t.

Table 4.65: Hidden friend functions of the multi_ptr class.

The following is the overview of the legacy interface from 1.2.1 provided for the multi_ptr class.

```cpp
namespace sycl {

    // Legacy interface, inherited from 1.2.1.
    // Deprecated.
    template <typename ElementType, access::address_space Space>
    class [[deprecated]] multi_ptr<ElementType, Space, access::decorated::legacy> {
        public:
            using element_type = ElementType;
            using difference_type = std::ptrdiff_t;

            // Implementation defined pointer and reference types that correspond to
            // SYCL/OpenCL interoperability types for OpenCL C functions.
            using pointer_t = multi_ptr<ElementType, Space, access::decorated::yes>::pointer;
            using const_pointer_t = multi_ptr<const ElementType, Space, access::decorated::yes>::pointer;
            using reference_t = multi_ptr<ElementType, Space, access::decorated::yes>::reference;
            using const_reference_t = multi_ptr<const ElementType, Space, access::decorated::yes>::reference;

            static constexpr access::address_space address_space = Space;

    // Constructors
    multi_ptr();

```

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multi_ptr(const multi_ptr&);
multi_ptr(multi_ptr&&);
multi_ptr(pointer_t);
multi_ptr(ElementType*);
multi_ptr(std::nullptr_t);
~multi_ptr();

// Assignment and access operators
multi_ptr &operator=(const multi_ptr&);
multi_ptr &operator=(multi_ptr&&);
multi_ptr &operator=(pointer_t);
multi_ptr &operator=(ElementType*);
multi_ptr &operator=(std::nullptr_t);
frend ElementType& operator* (const multi_ptr& mp) { /* ... */ } 
ElementType* operator->() const;

// Assignment and access operators (cont.)
friend multi_ptr &operator++ (multi_ptr & mp) { /* ... */ } 
friend multi_ptr operator++ (multi_ptr && mp, int) { /* ... */ } 
friend multi_ptr &operator-- (multi_ptr & mp) { /* ... */ } 
friend multi_ptr operator-- (multi_ptr && mp, int) { /* ... */ } 
friend multi_ptr &operator+=(multi_ptr & lhs, difference_type r) { /* ... */ } 
friend multi_ptr &operator-=(multi_ptr & lhs, difference_type r) { /* ... */ } 
friend multi_ptr &operator*=(const multi_ptr & lhs, difference_type r) { /* ... */ }
friend multi_ptr operator-(const multi_ptr& lhs, difference_type r) { /* ... */ }

void prefetch(size_t numElements) const;

friend bool operator==(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }

friend bool operator==(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator!=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator<(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator>(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator<=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator>=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }

friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }

};

// Legacy interface, inherited from 1.2.1.
// Deprecated.
// Specialization of multi_ptr for void and const void
// VoidType can be either void or const void

template <access::address_space Space>
class [[deprecated]] multi_ptr<VoidType, Space, access::decorated::legacy> {
public:
  using element_type = VoidType;
  using difference_type = std::ptrdiff_t;

  // Implementation defined pointer types that correspond to
  // SYCL/OpenCL interoperability types for OpenCL C functions
  using pointer_t = multi_ptr<VoidType, Space, access::decorated::yes>::pointer;
  using const_pointer_t = multi_ptr<const VoidType, Space, access::decorated::yes>::pointer;

  static constexpr access::address_space address_space = Space;

  // Constructors
  multi_ptr();
  multi_ptr(const multi_ptr&);
  multi_ptr(multi_ptr&&);
  multi_ptr(pointer_t);
  multi_ptr(VoidType);
  multi_ptr(VoidType*);
  ~multi_ptr();

  // Assignment operators
  multi_ptr &operator=(const multi_ptr&);
4.7. DATA ACCESS AND STORAGE IN SYCL

```cpp
multi_ptr &operator=(multi_ptr&&);
multi_ptr &operator=(pointer_t);
multi_ptr &operator=(VoidType*);
multi_ptr &operator=(std::nullptr_t);

// Only if Space == global_space
template <typename ElementType, int dimensions, access::mode Mode>
multi_ptr(accessor<ElementType, dimensions, Mode, access::target::global_buffer>);

// Only if Space == local_space
template <typename ElementType, int dimensions, access::mode Mode>
multi_ptr(accessor<ElementType, dimensions, Mode, access::target::local>);

// Only if Space == constant_space
template <typename ElementType, int dimensions, access::mode Mode>
multi_ptr(accessor<ElementType, dimensions, Mode, access::target::constant_buffer>);

// Returns the underlying OpenCL C pointer
pointer_t get() const;

// Implicit conversion to the underlying pointer type
operator VoidType*() const;

// Explicit conversion to a multi_ptr<ElementType>
// If VoidType is const, ElementType must be as well
template <typename ElementType>
explicit operator multi_ptr<ElementType, Space>() const;

// Implicit conversion to multi_ptr<const void, Space>
operator multi_ptr<const void, Space>() const;

friend bool operator==(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(const multi_ptr& lhs, const multi_ptr& rhs) { /* ... */ }

friend bool operator==(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator!=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator<(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator>(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator<=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }
friend bool operator>=(const multi_ptr& lhs, std::nullptr_t) { /* ... */ }

friend bool operator==(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator!=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator<=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }
friend bool operator>=(std::nullptr_t, const multi_ptr& rhs) { /* ... */ }

} // namespace sycl
```
4.7.7.2 Explicit pointer aliases

SYCL provides aliases to the `multi_ptr` class template (see Section 4.7.7.1) for each specialization of `access::address_space`.

A synopsis of the SYCL `multi_ptr` class template aliases is provided below.

```cpp
namespace sycl {

// Template specialization aliases for different pointer address spaces

// The interface exposes non-decorated pointer while keeping the
// address space information internally.

template <typename ElementType, access::decorated IsDecorated = access::decorated::no>
using raw_global_ptr = multi_ptr<ElementType, access::address_space::global_space,
                                 IsDecorated>;

// The interface exposes decorated pointer.

template <typename ElementType, access::decorated IsDecorated = access::decorated::legacy>
using decorated_global_ptr = multi_ptr<ElementType, access::address_space::global_space,
                                     IsDecorated>;

// The interface exposes non-decorated pointer while keeping the
// address space information internally.

template <typename ElementType, access::decorated IsDecorated = access::decorated::no>
using raw_local_ptr = multi_ptr<ElementType, access::address_space::local_space,
                               IsDecorated>;

// The interface exposes decorated pointer.

template <typename ElementType, access::decorated IsDecorated = access::decorated::legacy>
using decorated_local_ptr = multi_ptr<ElementType, access::address_space::local_space,
                                     IsDecorated>;

// The interface exposes non-decorated pointer while keeping the
// address space information internally.

template <typename ElementType, access::decorated IsDecorated = access::decorated::no>
using raw_constant_ptr = multi_ptr<ElementType, access::address_space::constant_space,
                                  IsDecorated>;

// The interface exposes decorated pointer.

template <typename ElementType, access::decorated IsDecorated = access::decorated::legacy>
using decorated_constant_ptr = multi_ptr<ElementType, access::address_space::constant_space,
                                         IsDecorated>;

// The interface exposes non-decorated pointer while keeping the
// address space information internally.

template <typename ElementType, access::decorated IsDecorated = access::decorated::no>
using raw_private_ptr = multi_ptr<ElementType, access::address_space::private_space,
                                 IsDecorated>;

// The interface exposes decorated pointer.

template <typename ElementType, access::decorated IsDecorated = access::decorated::legacy>
using decorated_private_ptr = multi_ptr<ElementType, access::address_space::private_space,
                                        IsDecorated>;

// The interface exposes non-decorated pointer while keeping the
// address space information internally.

template <typename ElementType, access::decorated IsDecorated = access::decorated::no>
using raw_global_ptr = multi_ptr<ElementType, access::address_space::global_space,
                                 access::decorated::no>;

// The interface exposes decorated pointer.

template <typename ElementType, access::decorated IsDecorated = access::decorated::legacy>
using decorated_global_ptr = multi_ptr<ElementType, access::address_space::global_space,
                                     access::decorated::no>;

// The interface exposes non-decorated pointer while keeping the
// address space information internally.

template <typename ElementType, access::decorated IsDecorated = access::decorated::no>
using raw_local_ptr = multi_ptr<ElementType, access::address_space::local_space,
                               access::decorated::no>;

// The interface exposes decorated pointer.

template <typename ElementType, access::decorated IsDecorated = access::decorated::legacy>
using decorated_local_ptr = multi_ptr<ElementType, access::address_space::local_space,
                                     access::decorated::no>;

// The interface exposes non-decorated pointer while keeping the
// address space information internally.

template <typename ElementType, access::decorated IsDecorated = access::decorated::no>
using raw_constant_ptr = multi_ptr<ElementType, access::address_space::constant_space,
                                  access::decorated::no>;

// The interface exposes decorated pointer.

template <typename ElementType, access::decorated IsDecorated = access::decorated::legacy>
using decorated_constant_ptr = multi_ptr<ElementType, access::address_space::constant_space,
                                         access::decorated::no>;

// The interface exposes non-decorated pointer while keeping the
// address space information internally.

template <typename ElementType, access::decorated IsDecorated = access::decorated::no>
using raw_private_ptr = multi_ptr<ElementType, access::address_space::private_space,
                                 access::decorated::no>;

// The interface exposes decorated pointer.

template <typename ElementType, access::decorated IsDecorated = access::decorated::legacy>
using decorated_private_ptr = multi_ptr<ElementType, access::address_space::private_space,
                                        access::decorated::no>;

} // namespace sycl
```
4.7.1 Data Access and Storage in SYCL

```cpp
49 access::decorated::yes;)
50
51 template <typename ElementType>
52 using decorated_local_ptr = multi_ptr<ElementType, access::address_space::local_space,
53 access::decorated::yes;)
54
55 template <typename ElementType>
56 using decorated_constant_ptr = multi_ptr<ElementType, access::address_space::constant_space,
57 access::decorated::yes;)
58
59 template <typename ElementType>
60 using decorated_private_ptr = multi_ptr<ElementType, access::address_space::private_space,
61 access::decorated::yes;)
62
63 } // namespace sycl
```

Note that using `global_ptr`, `local_ptr`, `constant_ptr` or `private_ptr` without specifying the decoration is deprecated. The default argument is provided for compatibility with 1.2.1.

### 4.7.8 Samplers

The SYCL sampler struct encapsulates a configuration for sampling a `sampled_image`. A SYCL sampler can map to one native backend object.

```cpp
1 namespace sycl {
2 enum class addressing_mode: unsigned int {
3 mirrored_repeat,
4 repeat,
5 clamp_to_edge,
6 clamp,
7 none
8 };
9
10 enum class filtering_mode: unsigned int {
11 nearest,
12 linear
13 };
14
15 enum class coordinate_normalization_mode : unsigned int {
16 normalized,
17 unnormalized
18 };
19
20 struct image_sampler {
21 addressing_mode addressing;
22 coordinate_mode coordinate;
23 filtering_mode filtering;
24 }
25 } // namespace sycl
```

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### Addressing modes description

<table>
<thead>
<tr>
<th>addressing_mode</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>mirrored_repeat</td>
<td>Out of range coordinates will be flipped at every integer junction. This addressing mode can only be used with normalized coordinates. If normalized coordinates are not used, this addressing mode may generate image coordinates that are undefined.</td>
</tr>
<tr>
<td>repeat</td>
<td>Out of range image coordinates are wrapped to the valid range. This addressing mode can only be used with normalized coordinates. If normalized coordinates are not used, this addressing mode may generate image coordinates that are undefined.</td>
</tr>
<tr>
<td>clamp_to_edge</td>
<td>Out of range image coordinates are clamped to the extent.</td>
</tr>
<tr>
<td>clamp</td>
<td>Out of range image coordinates will return a border color.</td>
</tr>
<tr>
<td>none</td>
<td>For this addressing mode the programmer guarantees that the image coordinates used to sample elements of the image refer to a location inside the image; otherwise the results are undefined.</td>
</tr>
</tbody>
</table>

End of table

### Filtering modes description

<table>
<thead>
<tr>
<th>filtering_mode</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>nearest</td>
<td>Chooses a color of nearest pixel.</td>
</tr>
<tr>
<td>linear</td>
<td>Performs a linear sampling of adjacent pixels.</td>
</tr>
</tbody>
</table>

End of table

### Coordinate normalization modes description

<table>
<thead>
<tr>
<th>coordinate_normalization_mode</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>normalized</td>
<td>Normalizes image coordinates.</td>
</tr>
<tr>
<td>unnormalized</td>
<td>Does not normalize image coordinates.</td>
</tr>
</tbody>
</table>

End of table
4.8 Unified shared memory (USM)

This section describes new properties and routines for pointer-based memory management interfaces in SYCL. These routines augment, rather than replace, the existing buffer-based interfaces in SYCL.

Unified Shared Memory (USM) provides a pointer-based alternative to the buffer programming model. USM enables:

- Easier integration into existing code bases by representing allocations as pointers rather than buffers, with full support for pointer arithmetic into allocations.
- Fine-grain control over ownership and accessibility of allocations, to optimally choose between performance and programmer convenience.
- A simpler programming model, by automatically migrating some allocations between SYCL devices and the host.

4.8.1 Unified addressing

Unified Addressing guarantees that all devices will use a unified address space. Pointer values in the unified address space will always refer to the same location in memory. The unified address space encompasses the host and one or more devices. Note that this does not require addresses in the unified address space to be accessible on all devices, just that pointer values will be consistent.
4.8.2 Kinds of unified shared memory

USM is a capability that, when available, provides the ability to create allocations that are visible to both host and device(s). USM builds upon Unified Addressing to define a shared address space where pointer values in this space always refer to the same location in memory. USM defines multiple tiers of increasing capability described in the following sections:

- Explicit USM
- Restricted USM
- Concurrent USM
- System USM

USM is an optional feature which may not be supported by all devices, and devices that support USM may only support some of these tiers. A SYCL application can use the `device::has()` function to determine the level of USM support for a device. See Table 4.20 in Section 4.6.4.3 for more details.

4.8.2.1 Explicit USM

Explicit USM defines capabilities for explicitly managing device memory. Programmers directly allocate device memory, and data must be explicitly copied between the host and a device. Device allocations are obtained through SYCL USM device allocation routines instead of system allocation routines like `std::malloc` or C++ `new`. Device allocations are not accessible on the host, but the pointer values remain consistent on account of Unified Addressing. Greater detail about how allocations are used is described by the following tables.

4.8.2.2 Restricted USM

Restricted USM defines capabilities for implicitly sharing data between host and devices. However, Restricted USM, as the name implies, is limited in that host and device may not concurrently compute on memory in the shared address space. Restricted USM builds upon Explicit USM by adding two new types of allocations, host and shared. Allocations are obtained through SYCL allocator instead of the system allocator. shared allocations may be limited by device memory. Greater detail about the allocation types defined in Restricted USM and their usage is described by the following tables.

4.8.2.3 Concurrent USM

Concurrent USM builds upon Restricted USM by enabling concurrent access to shared allocations between host and devices. Additionally, some implementations may support a working set of shared allocations larger than device memory.

4.8.2.4 System USM

System USM extends upon the previous tiers by performing all shared allocations with the normal system memory allocation routines. In particular, programmers may now use `std::malloc` or C++ `new` instead of USM allocation routines to create shared allocations. Likewise, `std::free` and `delete` are used instead of `sycl::free`. Note that host and device allocations are unaffected by this change and must still be allocated using their respective USM functions in order to guarantee their behavior.
### 4.8. UNIFIED SHARED MEMORY (USM)

#### USM allocation type

<table>
<thead>
<tr>
<th>USM allocation type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>host</td>
<td>Allocations in host memory that are accessible by a device</td>
</tr>
<tr>
<td>device</td>
<td>Allocations in device memory that are not accessible by the host</td>
</tr>
<tr>
<td>shared</td>
<td>Allocations in shared memory that are accessible by both host and device</td>
</tr>
</tbody>
</table>

Table 4.71: Type of USM allocations.

#### Allocation Type | Initial Location | Accessible By | Migratable To |
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>device</td>
<td>device</td>
<td>host device</td>
<td>host device</td>
</tr>
<tr>
<td></td>
<td></td>
<td>host device</td>
<td>device</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Another device</td>
<td>Another device</td>
</tr>
<tr>
<td>host</td>
<td>host</td>
<td>host device</td>
<td>host device</td>
</tr>
<tr>
<td></td>
<td></td>
<td>host device</td>
<td>device</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Any device</td>
<td>N/A</td>
</tr>
<tr>
<td>shared</td>
<td>Unspecified</td>
<td>host device</td>
<td>host device</td>
</tr>
<tr>
<td></td>
<td></td>
<td>host device</td>
<td>device</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Another device</td>
<td>Another device</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Optional (P2P)</td>
<td>Optional</td>
</tr>
</tbody>
</table>

Table 4.72: Characteristics of the different kinds of USM allocation.

#### 4.8.3 USM allocations

There are different types of allocation described on Table 4.71 with different characteristics exposed on Table 4.72 which can be referred using the following `enum`:

```cpp
namespace sycl {
    namespace usm {
        enum class alloc {
            host,
            device,
            shared,
            unknown
        };
    }
}
```

#### 4.8.4 C++ allocator interface

```cpp
template<typename T, usm::alloc AllocKind, size_t Alignment = 0>
class usm_allocator {
public:
    using value_type = T;

public:
    template<typename U> struct rebind {
        typedef usm_allocator<U, AllocKind, Alignment> other;
    };

    usm_allocator() noexcept = delete;
    usm_allocator(const context &ctx, const device &dev) noexcept;
    usm_allocator(const queue &q) noexcept;
};
```
usm_allocator(const usm_allocator &other) noexcept;

template <class U> usm_allocator(usm_allocator<U, AllocKind, Alignment> const &) noexcept;

/// Allocate memory
T *allocate(size_t Size);

/// Deallocate memory
void deallocate(T *Ptr, size_t size);

/// Constructs an object on memory pointed by Ptr.
///
/// Note: AllocKind == alloc::device is not allowed.
template <
    usm::alloc AllocT = AllocKind,
    typename std::enable_if<AllocT != usm::alloc::device, int>::type = 0,
    class U, class... ArgsTs>
void construct(U *Ptr, ArgsTs &... Args);

/// Throws an error when trying to construct a device allocation
/// on the host
template <
    usm::alloc AllocT = AllocKind,
    typename std::enable_if<AllocT == usm::alloc::device, int>::type = 0,
    class U, class... ArgsTs>
void construct(U *Ptr, ArgsTs &... Args);

/// Destroys an object.
///
/// Note:: AllocKind == alloc::device is not allowed
template <
    usm::alloc AllocT = AllocKind,
    typename std::enable_if<AllocT != usm::alloc::device, int>::type = 0>
void destroy(T *Ptr);

/// Throws an error when trying to destroy a device allocation
/// on the host
template <
    usm::alloc AllocT = AllocKind,
    typename std::enable_if<AllocT == usm::alloc::device, int>::type = 0>
void destroy(T *Ptr);

/// Equality Comparison
/// Allocators only compare equal if they are of the same USM kind, alignment,
/// context, and device (when kind is not host)
template <class T, usm::alloc AllocKindT, size_t AlignmentT, class U,
usm::alloc AllocKindU, size_t AlignmentU>
bool operator==(const usm_allocator<T, AllocKindT, AlignmentT> &,
    const usm_allocator<U, AllocKindU, AlignmentU> &) noexcept;

/// Inequality Comparison
/// Allocators only compare unequal if they are not of the same USM kind, alignment,
4.8.5 Utility functions

While the modern C++ usm_allocator interface is sufficient for specifying USM allocations and deallocations, many programmers may prefer C-style malloc-influenced APIs. As a convenience to programmers, malloc-style APIs are also defined. Additionally, other utility functions are specified in the following sections to perform various operations such as memory copies and initializations as well as to provide performance hints.

4.8.5.1 Explicit USM

4.8.5.1.1 malloc_device

1 (1)
2 void* sycl::malloc_device(size_t num_bytes,
3       const sycl::device& dev,
4       const sycl::context& ctxt);
5
6 (2)
7 template <typename T>
8 T* sycl::malloc_device(size_t count,
9       const sycl::device& dev,
10      const sycl::context& ctxt);

Parameters

• (1) size_t num_bytes - number of bytes to allocate
• (2) size_t count - number of elements of type T to allocate
• const sycl::device& dev - the SYCL device to allocate on
• const sycl::context& ctxt - the SYCL context to which device belongs

Return value Returns a pointer to the newly allocated memory on the specified device on success. This memory is not accessible on the host. Memory allocated by sycl::malloc_device must be deallocated with sycl::free to avoid memory leaks. If ctxt is a host context, it should behave as if calling malloc_host. On failure, returns nullptr.

1 (1)
2 void* sycl::malloc_device(size_t num_bytes,
3       const sycl::queue& q);
4
5 (2)
6 template <typename T>
7 T* sycl::malloc_device(size_t count,
8       const sycl::queue& q);

Parameters
• (1) size_t num_bytes - number of bytes to allocate
• (2) size_t count - number of elements of type T to allocate
• const sycl::queue& q - the SYCL q that provides the device and context to allocate against

Return value  Returns a pointer to the newly allocated memory on the device associated with q on success. This memory is not accessible on the host. Memory allocated by sycl::malloc_device must be deallocated with sycl::free to avoid memory leaks. If q is a host queue, it should behave as if calling malloc_host. On failure, returns nullptr.

4.8.5.1.2 aligned_alloc_device

```c
1 (1)
2 void* sycl::aligned_alloc_device(size_t alignment,
3     size_t num_bytes,
4     const sycl::device& dev,
5     const sycl::context& ctxt);

2 (2)
3 template <typename T>
4 T* sycl::aligned_alloc_device(size_t alignment,
5     size_t count,
6     const sycl::device& dev,
7     const sycl::context& ctxt);
```

Parameters

• size_t alignment - specifies the byte alignment. Must be a valid alignment supported by the implementation.
• (1) size_t num_bytes - number of bytes to allocate
• (2) size_t count - number of elements of type T to allocate
• const sycl::device& dev - the device to allocate on
• const sycl::context& ctxt - the SYCL context to which device belongs

Return value  Returns a pointer to the newly allocated memory on the specified device on success. This memory is not accessible on the host. Memory allocated by sycl::aligned_alloc_device must be deallocated with sycl::free to avoid memory leaks. If ctxt is a host context, it should behave as if calling aligned_alloc_host. On failure, returns nullptr.
const sycl::queue& q);

Parameters

- **size_t alignment** - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- **(1) size_t size** - number of bytes to allocate
- **(2) size_t count** - number of elements of type `T` to allocate
- **const sycl::queue& q** - the SYCL q that provides the device and context to allocate against

Return value Returns a pointer to the newly allocated memory on the device associated with q on success. This memory is not accessible on the host. Memory allocated by `sycl::aligned_alloc_device` must be deallocated with `sycl::free` to avoid memory leaks. If q is a host `queue`, it should behave as if calling `aligned_alloc_host`. On failure, returns `nullptr`.

4.8.5.1.3 memcpy

class handler {
  ...
  public:
  ...
  void memcpy(void* dest, const void* src, size_t num_bytes);
};

class queue {
  ...
  public:
  ...
  event memcpy(void* dest, const void* src, size_t num_bytes);
};

Parameters

- **void* dest** - pointer to the destination memory
- **const void* src** - pointer to the source memory
- **size_t num_bytes** - number of bytes to copy

Return value Returns an event representing the copy operation.

4.8.5.1.4 memset

class handler {
  ...
  public:
  ...
  void memset(void* ptr, int value, size_t num_bytes);
};
class queue {
  ...
  public:
  ...
  event memset(void* ptr, int value, size_t num_bytes);
};

Parameters

- void* ptr - pointer to the memory to fill
- int value - value to be set. Value is interpreted as an unsigned char
- size_t num_bytes - number of bytes to fill

Return value Returns an event representing the fill operation.

4.8.5.1.5 fill

class handler {
  ...
  public:
  ...
  template <typename T>
  void fill(void* ptr, const T& pattern, size_t count);
};

class queue {
  ...
  public:
  ...
  template <typename T>
  event fill(void* ptr, const T& pattern, size_t count);
};

Parameters

- void* ptr - pointer to the memory to fill
- const T& pattern - pattern to be filled. T should be trivially copyable.
- size_t count - number of times to fill pattern into ptr

Return value Returns an event representing the fill operation or void if on the handler.

4.8.5.2 Restricted USM

Restricted USM includes all of the Utility Functions of Explicit USM. It additionally introduces new functions to support host and shared allocations.
4.8. UNIFIED SHARED MEMORY (USM) SYCL 2020 provisional

1 (1)  
2 void* sycl::malloc_host(size_t num_bytes, const sycl::context& ctxt);  
3 (2)  
4 template <typename T>  
5 T* sycl::malloc_host(size_t count, const sycl::context& ctxt);

Parameters

- (1) size_t num_bytes - number of bytes to allocate
- (2) size_t count - number of elements of type T to allocate
- const sycl::context& ctxt - the SYCL context that contains the devices that will access the host allocation

Return value Returns a pointer to the newly allocated host memory on success. Memory allocated by sycl::malloc_host must be deallocated with sycl::free to avoid memory leaks. On failure, returns nullptr.

1 (1)  
2 void* sycl::malloc_host(size_t num_bytes, const sycl::queue& q);  
3 (2)  
4 template <typename T>  
5 T* sycl::malloc_host(size_t count, const sycl::queue& q);

Parameters

- (1) size_t num_bytes - number of bytes to allocate
- (2) size_t count - number of elements of type T to allocate
- const sycl::queue& q - the SYCL queue whose context contains the devices that will access the host allocation

Return value Returns a pointer to the newly allocated host memory on success. Memory allocated by sycl::malloc_host must be deallocated with sycl::free to avoid memory leaks. On failure, returns nullptr.

1 (1)  
2 void* sycl::malloc_shared(size_t num_bytes, const sycl::device& dev, const sycl::context& ctxt);  
3 (2)  
4 template <typename T>  
5 T* sycl::malloc_shared(size_t count, const sycl::device& dev, const sycl::context& ctxt);

Parameters

- (1) size_t num_bytes - number of bytes to allocate
- (2) size_t count - number of elements of type T to allocate
- const sycl::device& dev - the SYCL device which shares access to this memory with the host.
requested memory is allocated on this device.

- `const sycl::context& ctxt` - defines the set of devices that may also share access to this memory, if supported. The dev device must be contained within this context.

**Return value** Returns a pointer to the newly allocated shared memory on the specified device on success. Memory allocated by `sycl::malloc_shared` must be deallocated with `sycl::free` to avoid memory leaks. If `ctxt` is a host context, should behave as if calling `malloc_host`. On failure, returns `nullptr`.

```c
1  (1)
2  void* sycl::malloc_shared(size_t num_bytes,
3   const sycl::queue& q);
4  (2)
5  template <typename T>
6  T* sycl::malloc_shared(size_t count,
7   const sycl::queue& q);
```

**Parameters**

- (1) `size_t num_bytes` - number of bytes to allocate
- (2) `size_t count` - number of elements of type `T` to allocate
- `const sycl::queue& q` - the SYCL queue that provides the device and context to allocate against

**Return value** Returns a pointer to the newly allocated shared memory on the device associated with `q` on success. Memory allocated by `sycl::malloc_shared` must be deallocated with `sycl::free` to avoid memory leaks. On failure, returns `nullptr`.

### 4.8.5.2.2 aligned_alloc_host

```c
1  (1)
2  void* sycl::aligned_alloc_host(size_t alignment, size_t num_bytes, const sycl::context& ctxt);
3  (2)
4  template <typename T>
5  T* sycl::aligned_alloc_host(size_t alignment, size_t count, const sycl::context& ctxt);
```

**Parameters**

- `size_t alignment` - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- (1) `size_t num_bytes` - number of bytes to allocate
- (2) `size_t count` - number of elements of type `T` to allocate
- `const sycl::context& ctxt` - the SYCL context that contains the devices that will access the host allocation

**Return value** Returns a pointer to the newly allocated host memory on success. Memory allocated by `sycl::aligned_alloc_host` must be deallocated with `sycl::free` to avoid memory leaks. On failure, returns `nullptr`.
4.8. UNIFIED SHARED MEMORY (USM)

```cpp
void* sycl::aligned_alloc_host(size_t alignment, size_t num_bytes, const sycl::queue& q);
```

### Parameters

- **size_t alignment** - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- **size_t num_bytes** - number of bytes to allocate
- **const sycl::queue& q** - the SYCL q whose context contains the devices that will access the host allocation

### Return value

Returns a pointer to the newly allocated host memory on success. Memory allocated by `sycl::aligned_alloc_host` must be deallocated with `sycl::free` to avoid memory leaks. On failure, returns `nullptr`.

```cpp
template <typename T>
void* sycl::aligned_alloc_host(size_t alignment, size_t count, const sycl::queue& q);
```

### Parameters

- **size_t alignment** - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- **size_t count** - number of elements of type `T` to allocate
- **const sycl::queue& q** - the SYCL q whose context contains the devices that will access the host allocation

### Return value

Returns a pointer to the newly allocated host memory on success. Memory allocated by `sycl::aligned_alloc_host` must be deallocated with `sycl::free` to avoid memory leaks. On failure, returns `nullptr`.

```cpp
void* sycl::aligned_alloc_shared(size_t alignment, size_t num_bytes, const sycl::device& dev, const sycl::context& ctxt);
```

### Parameters

- **size_t alignment** - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- **size_t num_bytes** - number of bytes to allocate
- **const sycl::device& dev** - the SYCL device which shares access to this memory with the host. The requested memory is allocated on this device.
- **const sycl::context& ctxt** - defines the set of devices that may also share access to this memory, if supported. The dev device must be contained within this context.

### Return value

Returns a pointer to the newly allocated shared memory on the specified device on success. Memory allocated by `sycl::aligned_alloc_shared` must be deallocated with `sycl::free` to avoid memory leaks. If `ctxt` is a host `context`, should behave as if calling `aligned_alloc_host`. On failure, returns `nullptr`.

```cpp
template <typename T>
T* sycl::aligned_alloc_shared(size_t alignment, size_t count, const sycl::device& dev, const sycl::context& ctxt);
```

### Parameters

- **size_t alignment** - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- **size_t count** - number of elements of type `T` to allocate
- **const sycl::device& dev** - the SYCL device which shares access to this memory with the host. The requested memory is allocated on this device.
- **const sycl::context& ctxt** - defines the set of devices that may also share access to this memory, if supported. The dev device must be contained within this context.

### Return value

Returns a pointer to the newly allocated shared memory on the specified device on success. Memory allocated by `sycl::aligned_alloc_shared` must be deallocated with `sycl::free` to avoid memory leaks. If `ctxt` is a host `context`, should behave as if calling `aligned_alloc_host`. On failure, returns `nullptr`. 
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4.8. UNIFIED SHARED MEMORY (USM)

```c
1 (1)
2 void* sycl::aligned_alloc_shared(size_t alignment,
3     size_t num_bytes,
4     const sycl::queue& q);
5 (2)
6 template <typename T>
7 T* sycl::aligned_alloc_shared(size_t alignment,
8     size_t count,
9     const sycl::queue& q);
```

**Parameters**

- `size_t alignment` - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- `(1) size_t num_bytes` - number of bytes to allocate
- `(2) size_t count` - number of elements of type `T` to allocate
- `const sycl::queue& q` - the SYCL `q` that provides the **device** and **context** to allocate against

**Return value**  Returns a pointer to the newly allocated **shared** memory on the **device** associated with `q` on success. Memory allocated by `sycl::aligned_alloc_shared` must be deallocated with `sycl::free` to avoid memory leaks. If `ctxt` is a host `context`, should behave as if calling `aligned_alloc_host`. On failure, returns `nullptr`.

### 4.8.5.2.3 Performance hints

Programmers may provide hints to the runtime that data should be made available on a device earlier than Unified Shared Memory would normally require it to be available. This can be accomplished through enqueueing prefetch commands. Prefetch commands may not be overlapped with kernel execution in Restricted USM.

#### 4.8.5.2.3.1 prefetch

```c
1 class handler {
2     ...
3     public:
4         ...
5         void prefetch(const void* ptr, size_t num_bytes);
6     }
7
class queue {
8     ...
9     public:
10        ...
11        event prefetch(const void* ptr, size_t num_bytes);
12 }
```

**Parameters**

- `const void* ptr` - pointer to the memory to be prefetched to the device
- `size_t num_bytes` - number of bytes requested to be prefetched
Return value  Returns an event representing the prefetch operation.

4.8.5.3 Concurrent USM

Concurrent USM contains all the utility functions of Explicit USM and Restricted USM. It introduces a new function, sycl::queue::mem_advise, that allows programmers to provide additional information to the underlying runtime about how different allocations are used.

4.8.5.3.1 Performance hints

4.8.5.3.1.1 prefetch

In Concurrent USM, prefetch commands may be overlapped with kernel execution.

4.8.5.3.1.2 mem_advise

```cpp
1 class handler {
2     ...
3     public:
4     ...
5         void mem_advise(const void *addr, size_t num_bytes, int advice);
6     }
7
class queue {
8     ...
9     public:
10     ...
11         event mem_advise(const void *addr, size_t num_bytes, int advice);
12 }
```

Parameters

- void* addr - address of allocation
- size_t num_bytes - number of bytes in the allocation
- int advice - device-defined advice for the specified allocation. A value of 0 reverts the advice for addr to the default behavior.

Return Value  Returns an event representing the operation.

4.8.5.4 General

4.8.5.4.1 malloc

```cpp
1 (1)
2 void *sycl::malloc(size_t num_bytes,
3 const sycl::device& dev,
4 const sycl::context& ctxt,
5 usm::alloc kind);
6 (2)
7 template <typename T>
8     T *sycl::malloc(size_t count,
```
9    const sycl::device& dev,
10    const sycl::context& ctxt,
11    usm::alloc kind);

Parameters

• (1) size_t num_bytes - number of bytes to allocate

• (2) size_t count - number of elements of type T to allocate

• const sycl::device& dev - the SYCL device to allocate on (if applicable)

• const sycl::context& ctxt - the SYCL context to which device belongs

• usm::alloc kind - the type of allocation to perform

Return value Returns a pointer to the newly allocated kind memory on the specified device on success. If kind is alloc::host, dev is ignored. Memory allocated by sycl::malloc must be deallocated with sycl::free to avoid memory leaks. On failure, returns nullptr.

void *sycl::malloc(size_t num_bytes,
const sycl::queue& q,
usm::alloc kind);

Parameters

• (1) size_t num_bytes - number of bytes to allocate

• (2) size_t count - number of elements of type T to allocate

• const sycl::queue& q - the SYCL q that provides the device (if applicable) and context to allocate against

• usm::alloc kind - the type of allocation to perform

Return value Returns a pointer to the newly allocated kind memory on success. Memory allocated by sycl::malloc must be deallocated with sycl::free to avoid memory leaks. On failure, returns nullptr.

4.8.5.4.2 aligned_alloc

void *sycl::aligned_alloc(size_t alignment,
size_t num_bytes,
const sycl::device& dev,
const sycl::context& ctxt,
usm::alloc kind);
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```cpp
8  template <typename T>
9  T* sycl::aligned_alloc(size_t alignment,
10             size_t count,
11             const sycl::device& dev,
12             const sycl::context& ctxt,
13             usm::alloc kind);
```

**Parameters**

- `size_t alignment` - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- `(1)` `size_t num_bytes` - number of bytes to allocate
- `(2)` `size_t count` - number of elements of type `T` to allocate
- `const sycl::device& dev` - the SYCL device to allocate on (if applicable)
- `const sycl::context& ctxt` - the SYCL context to which `device` belongs
- `usm::alloc kind` - the type of allocation to perform

**Return value** Returns a pointer to the newly allocated kind memory on the specified `device` on success. If `kind` is `alloc::host`, `dev` is ignored. Memory allocated by `sycl::aligned_alloc` must be deallocated with `sycl::free` to avoid memory leaks. On failure, returns `nullptr`.

```cpp
1 (1)
2  void *sycl::aligned_alloc(size_t alignment,
3             size_t num_bytes,
4             const sycl::queue& q,
5             usm::alloc kind);
6 (2)
7  template <typename T>
8  T* sycl::aligned_alloc(size_t alignment,
9             size_t count,
10             const sycl::queue& q,
11             usm::alloc kind);
```

**Parameters**

- `size_t alignment` - specifies the byte alignment. Must be a valid alignment supported by the implementation.
- `(1)` `size_t num_bytes` - number of bytes to allocate
- `(2)` `size_t count` - number of elements of type `T` to allocate
- `const sycl::queue& q` - the SYCL `q` that provides the `device` (if applicable) and `context` to allocate against.
- `usm::alloc kind` - the type of allocation to perform

**Return value** Returns a pointer to the newly allocated kind memory on success. Memory allocated by `sycl::aligned_alloc` must be deallocated with `sycl::free` to avoid memory leaks. On failure, returns `nullptr`.

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4.8.5.4.3 free

```c
void sycl::free(void* ptr, sycl::context& context);
```

**Parameters**
- `void* ptr` - pointer to the memory to deallocate. Must have been allocated by a SYCL `malloc` or `aligned_alloc` function.
- `const sycl::context& ctxt` - the SYCL context in which `ptr` was allocated

**Return value** none

```c
void sycl::free(void* ptr, sycl::queue& q);
```

**Parameters**
- `void* ptr` - pointer to the memory to deallocate. Must have been allocated by a SYCL `malloc` or `aligned_alloc` function.
- `const sycl::queue& q` - the SYCL queue that provides the context in which `ptr` was allocated

**Return value** none

### 4.8.6 Unified shared memory information

#### 4.8.6.1 Pointer queries

##### 4.8.6.1.1 get_pointer_type

```c
usm::alloc get_pointer_type(const void *ptr, const context &ctxt);
```

**Parameters**
- `const void* ptr` - the pointer to query.
- `const sycl::context& ctxt` - the SYCL context to which the USM allocation belongs

**Return value** Returns the USM allocation type for `ptr` if `ptr` falls inside a valid USM allocation. If `ctxt` is a host `context`, returns `usm::alloc::host`. Returns `usm::alloc::unknown` if `ptr` is not a valid USM allocation.

##### 4.8.6.1.2 get_pointer_device

```c
sycl::device get_pointer_device(const void *ptr, const context &ctxt);
```

**Parameters**
- `const void* ptr` - the pointer to query
- `const sycl::context& ctxt` - the SYCL context to which the USM allocation belongs
Return value  Returns the device associated with the USM allocation. If ctxt is a host context, returns the host device in ctx. If ptr is an allocation of type usm::alloc::host, returns the first device in ctx. Throws an error if ptr is not a valid USM allocation.

4.9 SYCL scheduling

SYCL 1.2.1 defines an execution model based on tasks submitted to Out-of-Order queues. Dependences between these tasks are constructed from the data they read and write. The data usage of a task is conveyed to the runtime by constructing accessors on buffer objects that specify their intent. Pointers obtained from using explicit memory management interfaces in SYCL cannot create accessors, so dependence graphs cannot be constructed in the same fashion. New member functions are required to specify dependences between tasks.

4.9.1 DAGs without accessors

Unified Shared Memory changes how the SYCL runtime manages data movement. Since the runtime might no longer be responsible for orchestrating data movement, it makes sense to enable a way to build dependence graphs based on ordering computations rather than accesses to data inside them. Conveniently, a SYCL queue already returns an event upon calls to submit. These events can be used by the programmer to wait for the submitted task to complete.

```cpp
1 queue q;
2 auto dev = q.get_device();
3 auto ctxt = q.get_context();
4 float* a = static_cast<float*>(malloc_shared(10*sizeof(float), dev, ctxt));
5 float* b = static_cast<float*>(malloc_shared(10*sizeof(float), dev, ctxt));
6 float* c = static_cast<float*>(malloc_shared(10*sizeof(float), dev, ctxt));
7 auto e = q.submit([&](handler& cgh) {
8   cgh.parallel_for(range<1> {10}, [=](id<1> ID) {
9     c[ID[0]] = a[ID[0]] + b[ID[0]];  
10   });
11 });
12 e.wait();
```

4.9.2 Coarse grain DAGs with depends_on

While SYCL already defines the capability to wait on specific tasks, programmers should still be able to easily define relationships between tasks.

```cpp
1 class handler {
2   ...
3   public:
4   ...
5   void depends_on(event e);
6   void depends_on(const std::vector<event> &e);
7 ;
```

Parameters  e - event or vector of events representing task(s) required to complete before this task may begin
4.10 Expressing parallelism through kernels

4.10.1 Ranges and index space identifiers

The data parallelism of the SYCL kernel execution model requires instantiation of a parallel execution over a range of iteration space coordinates. To achieve this, SYCL exposes types to define the range of execution and to identify a given execution instance's point in the iteration space.

The following types are defined: `range`, `nd_range`, `id`, `item`, `h_item`, `nd_item` and `group`.

When constructing ids or ranges from integers, the elements are written in row-major format.

<table>
<thead>
<tr>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>id</td>
<td>A point within a range</td>
</tr>
<tr>
<td>range</td>
<td>Bounds over which an id may vary</td>
</tr>
<tr>
<td>item</td>
<td>Pairing of an id (specific point) and the range that it is bounded by</td>
</tr>
<tr>
<td>nd_range</td>
<td>Encapsulates both global and local (work-group size) ranges over which work-item ids will vary</td>
</tr>
<tr>
<td>nd_item</td>
<td>Encapsulates two items, one for global id and range, and one for local id and range</td>
</tr>
<tr>
<td>h_item</td>
<td>Index point queries within hierarchical parallelism (parallel_for_work_item). Encapsulates physical global and local ids and ranges, as well as a logical local id and range defined by hierarchical parallelism</td>
</tr>
<tr>
<td>group</td>
<td>Work-group queries within hierarchical parallelism (parallel_for_work_group), and exposes the parallel_for_work_item construct that identifies code to be executed by each work-item. Encapsulates work-group ids and ranges</td>
</tr>
</tbody>
</table>

Table 4.73: Summary of types used to identify points in an index space, and ranges over which those points can vary.

4.10.1.1 range class

`range<int dimensions>` is a 1D, 2D or 3D vector that defines the iteration domain of either a single work-group in a parallel dispatch, or the overall dimensions of the dispatch. It can be constructed from integers.

The SYCL `range` class template provides the common by-value semantics (see Section 4.5.4).

A synopsis of the SYCL `range` class is provided below. The constructors, member functions and non-member functions of the SYCL `range` class are listed in Tables 4.74, 4.75 and 4.76 respectively. The additional common special member functions and common member functions are listed in 4.5.4 in Tables 4.3 and 4.4 respectively.
namespace sycl {

template <int dimensions = 1>
class range {
public:

/* The following constructor is only available in the range class specialization where:
   dimensions==1 */
range(size_t dim0);

/* The following constructor is only available in the range class specialization where:
   dimensions==2 */
range(size_t dim0, size_t dim1);

/* The following constructor is only available in the range class specialization where:
   dimensions==3 */
range(size_t dim0, size_t dim1, size_t dim2);

/* -- common interface members -- */

size_t get(int dimension) const;
size_t &operator[](int dimension);
size_t operator[](int dimension) const;

size_t size() const;

// OP is: +, -, *, /, %, <<, >>, &, |, ^, &&, ||, <, >, <=, >=
friend range operatorOP(const range &lhs, const range &rhs) { /* ... */ }
friend range operatorOP(const range &lhs, const size_t &rhs) { /* ... */ }

// OP is: +=, -=, *=, /=, %=, <<=, >>=, &=, |=, ^=
friend range & operatorOP(range &lhs, const range &rhs) { /* ... */ }
friend range & operatorOP(range &lhs, const size_t &rhs) { /* ... */ }

// OP is: +, -, *, /, %, <<, >>, &, |, ^, &&, ||, <, >, <=, >=
friend range operatorOP(const size_t &lhs, const range &rhs) { /* ... */ }

}; // sycl

// Deduction guides
range(size_t) -> range<1>;
range(size_t, size_t) -> range<2>;
range(size_t, size_t, size_t) -> range<3>;

} // sycl

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>range(size_t dim0)</td>
<td>Construct a 1D range with value dim0. Only valid when the template parameter dimensions is equal to 1.</td>
</tr>
<tr>
<td>range(size_t dim0, size_t dim1)</td>
<td>Construct a 2D range with values dim0 and dim1. Only valid when the template parameter dimensions is equal to 2.</td>
</tr>
<tr>
<td>range(size_t dim0, size_t dim1, size_t dim2)</td>
<td>Construct a 3D range with values dim0, dim1 and dim2. Only valid when the template parameter dimensions is equal to 3.</td>
</tr>
</tbody>
</table>

Table 4.74: Constructors of the range class template.
### Table 4.75: Member functions of the `range` class template.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>size_t get(int dimension) const</td>
<td>Return the value of the specified dimension of the range.</td>
</tr>
<tr>
<td>size_t &amp;operator[](int dimension)</td>
<td>Return the l-value of the specified dimension of the range.</td>
</tr>
<tr>
<td>size_t operator[](int dimension) const</td>
<td>Return the value of the specified dimension of the range.</td>
</tr>
<tr>
<td>size_t size() const</td>
<td>Return the size of the range computed as dimension0*...*dimensionN.</td>
</tr>
</tbody>
</table>

End of table

### Table 4.76: Hidden friend functions of the SYCL `range` class template.

<table>
<thead>
<tr>
<th>Hidden friend function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>range operatorOP(const range &amp;lhs, const range &amp;rhs)</td>
<td>Where OP is: +, -, *, /, %, &lt;&lt;, &gt;&gt;,</td>
</tr>
<tr>
<td>range operatorOP(const range &amp;lhs, const size_t &amp;rhs)</td>
<td>Where OP is: +, -, *, /, %, &lt;&lt;, &gt;&gt;,</td>
</tr>
<tr>
<td>range &amp;operatorOP(range &amp;lhs, const range &amp;rhs)</td>
<td>Where OP is: +=, -=, *=, /=, %=, &lt;&lt;=, &gt;&gt;=, &amp;=,</td>
</tr>
</tbody>
</table>

Continued on next page
4.10. EXPRESSING PARALLELISM THROUGH KERNELS

<table>
<thead>
<tr>
<th>Hidden friend function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>range &amp;operatorOP(range &amp;lhs, const size_t &amp;rhs)</td>
<td>Where OP is: +=, -=, *=, /=, %=, &lt;&lt;=, &gt;&gt;=, &amp;=,</td>
</tr>
<tr>
<td>range operatorOP(const size_t &amp;lhs, const range &amp;rhs )</td>
<td>Where OP is: +, -, *, /, %, &lt;&lt;, &gt;&gt;, &amp;</td>
</tr>
</tbody>
</table>

Table 4.76: Hidden friend functions of the SYCL range class template.

4.10.1.2 nd_range class

```cpp
namespace sycl {
    template <int dimensions = 1>
    class nd_range {
    public:
        /* -- common interface members -- */
        nd_range(range<dimensions> globalSize, range<dimensions> localSize,
                  id<dimensions> offset = id<dimensions>());
        range<dimensions> get_global_range() const;
        range<dimensions> get_local_range() const;
        range<dimensions> get_group_range() const;
        id<dimensions> get_offset() const;
    }
} // namespace sycl
```

nd_range<int dimensions> defines the iteration domain of both the work-groups and the overall dispatch. To define this the nd_range comprises two ranges: the whole range over which the kernel is to be executed, and the range of each work group.

The SYCL nd_range class template provides the common by-value semantics (see Section 4.5.4).

A synopsis of the SYCL nd_range class is provided below. The constructors and member functions of the SYCL nd_range class are listed in Tables 4.77 and 4.78 respectively. The additional common special member functions...
and common member functions are listed in 4.5.4 in Tables 4.3 and 4.4 respectively.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>nd_range&lt;dimensions&gt;(</td>
<td>Construct an nd_range from the local and global constituent ranges as well as an optional offset. If the offset is not provided it will default to no offset.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; globalSize,</td>
<td></td>
</tr>
<tr>
<td>range&lt;dimensions&gt; localSize)</td>
<td></td>
</tr>
<tr>
<td>id&lt;dimensions&gt; offset = id&lt;dimensions&gt;()</td>
<td></td>
</tr>
</tbody>
</table>

Table 4.77: Constructors of the nd_range class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>range&lt;dimensions&gt; get_global_range()const</td>
<td>Return the constituent global range.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; get_local_range()const</td>
<td>Return the constituent local range.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; get_group_range()const</td>
<td>Return a range representing the number of groups in each dimension. This range would result from globalSize/localSize as provided on construction.</td>
</tr>
<tr>
<td>id&lt;dimensions&gt; get_offset()const</td>
<td>Return the constituent offset.</td>
</tr>
</tbody>
</table>

Table 4.78: Member functions for the nd_range class.

### 4.10.1.3 id class

id<int dimensions> is a vector of dimensions that is used to represent an id into a global or local range. It can be used as an index in an accessor of the same rank. The [n] operator returns the component n as a size_t.

The SYCL id class template provides the common by-value semantics (see Section 4.5.4).

A synopsis of the SYCL id class is provided below. The constructors, member functions and non-member functions of the SYCL id class are listed in Tables 4.79, 4.80 and 4.81 respectively. The additional common special member functions and common member functions are listed in 4.5.4 in Tables 4.3 and 4.4 respectively.

```cpp
namespace sycl {
  template <int dimensions = 1>
  class id {
  public:
    id();

    /* The following constructor is only available in the id class */
    id(size_t dim0);

    /* The following constructor is only available in the id class */
    id(size_t dim0, size_t dim1);

    /* The following constructor is only available in the id class */
    id(size_t dim0, size_t dim1, size_t dim2);

    /* -- common interface members -- */
  }
}
```
id(const range<dimensions> &range);
id(const item<dimensions> &item);

size_t get(int dimension) const;
size_t &operator[](int dimension);
size_t operator[](int dimension) const;

// OP is: +, -, *, /, %, <<, >>, &, |, ^, &&, ||, <, >, <=, >=
friend id operatorOP(const id &lhs, const id &rhs) { /* ... */ }
friend id operatorOP(const size_t &lhs, const size_t &rhs) { /* ... */ }

// OP is: +=, -=, *=, /=, %=, <<=, >>=, &=, |=, ^=, &&, ||, <, >, <=, >=
friend id &operatorOP(id &lhs, const id &rhs) { /* ... */ }
friend id &operatorOP(id &lhs, const size_t &rhs) { /* ... */ }

// OP is: +, -, *, /, %, <<, >>, &, |, ^, &&, ||, <, >, <=, >=
friend id operatorOP(const size_t &lhs, const id &rhs) { /* ... */ }

};

// Deduction guides
id(size_t) -> id<1>;
id(size_t, size_t) -> id<2>;
id(size_t, size_t, size_t) -> id<3>;

} // namespace sycl

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>id()</td>
<td>Construct a SYCL id with the value 0 for each dimension.</td>
</tr>
<tr>
<td>id(size_t dim0)</td>
<td>Construct a 1D id with value dim0. Only valid when the template parameter dimensions is equal to 1.</td>
</tr>
<tr>
<td>id(size_t dim0, size_t dim1)</td>
<td>Construct a 2D id with values dim0, dim1. Only valid when the template parameter dimensions is equal to 2.</td>
</tr>
<tr>
<td>id(size_t dim0, size_t dim1, size_t dim2)</td>
<td>Construct a 3D id with values dim0, dim1, dim2. Only valid when the template parameter dimensions is equal to 3.</td>
</tr>
<tr>
<td>id(const range&lt;dimensions&gt; &amp;range)</td>
<td>Construct an id from the dimensions of range.</td>
</tr>
<tr>
<td>id(const item&lt;dimensions&gt; &amp;item)</td>
<td>Construct an id from item.get_id().</td>
</tr>
</tbody>
</table>

Table 4.79: Constructors of the id class template.
### Member function

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>size_t get(int dimension) const</td>
<td>Return the value of the id for dimension</td>
</tr>
<tr>
<td>size_t &amp;operator[](int dimension)</td>
<td>Return a reference to the requested dimension of the id object.</td>
</tr>
<tr>
<td>size_t operator[](int dimension) const</td>
<td>Return the value of the requested dimension of the id object.</td>
</tr>
</tbody>
</table>

End of table

Table 4.80: Member functions of the id class template.

### Hidden friend function

<table>
<thead>
<tr>
<th>Hidden friend function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>id operatorOP(const id &amp;lhs, const id &amp;rhs)</td>
<td>Where OP is: +, -, *, /, %, &lt;&lt;&gt;, &amp;&amp;, ]], &lt;&gt;, &lt;=&gt;, &gt;=. Constructs and returns a new instance of the SYCL id class template with the same dimensionality as lhs id, where each element of the new SYCL id instance is the result of an element-wise OP operator between each element of lhs id and each element of the rhs id. If the operator returns a bool, the result is the cast to size_t.</td>
</tr>
<tr>
<td>id operatorOP(const id &amp;lhs, const size_t &amp;rhs)</td>
<td>Where OP is: +, -, *, /, %, &lt;&lt;&gt;, &amp;&amp;, ]], &lt;&gt;, &lt;=&gt;, &gt;=. Constructs and returns a new instance of the SYCL id class template with the same dimensionality as lhs id, where each element of the new SYCL id instance is the result of an element-wise OP operator between each element of lhs id and the rhs size_t. If the operator returns a bool, the result is the cast to size_t.</td>
</tr>
<tr>
<td>id &amp;operatorOP(id &amp;lhs, const id &amp;rhs)</td>
<td>Where OP is: +=, -=, *=, /=, %=, &lt;&lt;=, &gt;&gt;=, &amp;=. Assigns each element of lhs id instance with the result of an element-wise OP operator between each element of lhs id and each element of the rhs id and returns lhs id. If the operator returns a bool, the result is the cast to size_t.</td>
</tr>
<tr>
<td>id &amp;operatorOP(id &amp;lhs, const size_t &amp;rhs)</td>
<td>Where OP is: +=, -=, *=, /=, %=, &lt;&lt;=, &gt;&gt;=, &amp;=. Assigns each element of lhs id instance with the result of an element-wise OP operator between each element of lhs id and the rhs size_t and returns lhs id. If the operator returns a bool, the result is the cast to size_t.</td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.81: Hidden friend functions of the id class template.
### 4.10. EXPRESSING PARALLELISM THROUGH KERNELS

#### 4.10.1.4 **item class**

**item** identifies an instance of the function object executing at each point in a **range**. It is passed to a **parallel_for** call or returned by member functions of **h_item**. It encapsulates enough information to identify the work-item’s range of possible values and its ID in that range. It can optionally carry the offset of the range if provided to the **parallel_for**. Instances of the **item** class are not user-constructible and are passed by the runtime to each instance of the function object.

The SYCL **item** class template provides the common by-value semantics (see Section 4.5.4).

A synopsis of the SYCL **item** class is provided below. The member functions of the SYCL **item** class are listed in Table 4.80. The additional common special member functions and common member functions are listed in 4.5.4 in Tables 4.3 and 4.4 respectively.

```cpp
namespace sycl {
    template <int dimensions = 1, bool with_offset = true>
    class item {
        public:
            item() = delete;

            id<dimensions> get_id() const;
            size_t get_id(int dimension) const;
            size_t operator[](int dimension) const;
            range<dimensions> get_range() const;
            size_t get_range(int dimension) const;
            // only available if with_offset is true
            id<dimensions> get_offset() const;
            // only available if with_offset is false
            operator item<dimensions, true>() const;
    }
}
```

---

<table>
<thead>
<tr>
<th>Hidden friend function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>id</code> operatorOP(const size_t &amp;lhs, const id &amp;rhs)</td>
<td>Where OP is: +, -, *, /, %, &lt;&lt;, &gt;&gt;,</td>
</tr>
</tbody>
</table>

Table 4.81: Hidden friend functions of the `id` class template.
// only available if dimensions == 1
operator size_t() const;

size_t get_linear_id() const;
}; // namespace sycl

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>id&lt;dimensions&gt; get_id() const</td>
<td>Return the constituent id representing the work-item’s position in the iteration space.</td>
</tr>
<tr>
<td>size_t get_id(int dimension) const</td>
<td>Return the same value as get_id()[dimension].</td>
</tr>
<tr>
<td>size_t operator[](int dimension) const</td>
<td>Return the same value as get_id(dimension).</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; get_range() const</td>
<td>Returns a range representing the dimensions of the range of possible values of the item.</td>
</tr>
<tr>
<td>size_t get_range(int dimension) const</td>
<td>Return the same value as get_range().get(dimension).</td>
</tr>
<tr>
<td>id&lt;dimensions&gt; get_offset() const</td>
<td>Returns an id representing the n-dimensional offset provided to the parallel_for and that is added by the runtime to the global-ID of each work-item, if this item represents a global range. For an item converted from an item with no offset this will always return an id of all 0 values. This member function is only available if with_offset is true.</td>
</tr>
<tr>
<td>operator item&lt;dimensions, true&gt;() const</td>
<td>Available only when: with_offset == false Returns an item representing the same information as the object holds but also includes the offset set to 0. This conversion allow users to seamlessly write code that assumes an offset and still provides an offset-less item.</td>
</tr>
<tr>
<td>operator size_t() const</td>
<td>Available only when: dimensions == 1 Returns the index representing the work-item position in the iteration space.</td>
</tr>
<tr>
<td>size_t get_linear_id() const</td>
<td>Return the id as a linear index value. Calculating a linear address from the multi-dimensional index follow the equation 4.3.</td>
</tr>
</tbody>
</table>

Table 4.82: Member functions for the item class.
**4.10. EXPRESSING PARALLELISM THROUGH KERNELS**

SYCL 2020 provisional

### 4.10.1.5 nd_item class

`nd_item<int dimensions>` identifies an instance of the function object executing at each point in an `nd_range<int dimensions>` passed to a `parallel_for` call. It encapsulates enough information to identify the work-item’s local and global ids, the work-group id and also provides access to the `group` and `sub_group` classes. Instances of the `nd_item<int dimensions>` class are not user-constructible and are passed by the runtime to each instance of the function object.

The SYCL `nd_item` class template provides the common by-value semantics (see Section 4.5.4).

A synopsis of the SYCL `nd_item` class is provided below. The member functions of the SYCL `nd_item` class are listed in Table 4.83. The additional common special member functions and common member functions are listed in 4.5.4 in Tables 4.3 and 4.4 respectively.

```cpp
namespace sycl {

    template <int dimensions = 1>
    class nd_item {

    public:
        nd_item() = delete;

        /* -- common interface members -- */

        id<dimensions> get_global_id() const;
        size_t get_global_id(int dimension) const;
        size_t get_global_linear_id() const;
        id<dimensions> get_local_id() const;
        size_t get_local_id(int dimension) const;
        size_t get_local_linear_id() const;
        group<dimensions> get_group() const;
        size_t get_group(int dimension) const;
        size_t get_group_linear_id() const;
        range<dimensions> get_group_range() const;
        size_t get_group_range(int dimension) const;
        range<dimensions> get_global_range() const;
        size_t get_global_range(int dimension) const;
        range<dimensions> get_local_range() const;
        size_t get_local_range(int dimension) const;
        id<dimensions> get_offset() const;
        nd_range<dimensions> get_nd_range() const;

    };

});
```

CHAPTER 4. SYCL PROGRAMMING INTERFACE
template<typename dataT>
device_event async_work_group_copy(decorated_local_ptr<dataT> dest,
decorated_global_ptr<dataT> src, size_t numElements) const;

template<typename dataT>
device_event async_work_group_copy(decorated_global_ptr<dataT> dest,
decorated_local_ptr<dataT> src, size_t numElements) const;

template<typename dataT>
device_event async_work_group_copy(decorated_local_ptr<dataT> dest,
decorated_global_ptr<dataT> src, size_t numElements, size_t srcStride) const;

template<typename dataT>
device_event async_work_group_copy(decorated_global_ptr<dataT> dest,
decorated_local_ptr<dataT> src, size_t numElements, size_t destStride) const;

template<typename... eventTN>
void wait_for(eventTN... events) const;
}
} // namespace sycl

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>id&lt;dimensions&gt; get_global_id() const</code></td>
<td>Return the constituent global id representing the work-item’s position in the global iteration space.</td>
</tr>
<tr>
<td><code>size_t get_global_id(int dimension) const</code></td>
<td>Return the constituent element of the global id representing the work-item’s position in the nd-range in the given dimension.</td>
</tr>
<tr>
<td><code>size_t get_global_linear_id() const</code></td>
<td>Return the flattened id of the current work-item after subtracting the offset. Calculating a linear id from a multi-dimensional index follows the equation 4.3.</td>
</tr>
<tr>
<td><code>id&lt;dimensions&gt; get_local_id() const</code></td>
<td>Return the constituent local id representing the work-item’s position within the current work-group.</td>
</tr>
<tr>
<td><code>size_t get_local_id(int dimension) const</code></td>
<td>Return the constituent element of the local id representing the work-item’s position within the current work-group in the given dimension.</td>
</tr>
<tr>
<td><code>size_t get_local_linear_id() const</code></td>
<td>Return the flattened id of the current work-item within the current work-group. Calculating a linear address from a multi-dimensional index follows the equation 4.3.</td>
</tr>
<tr>
<td><code>group&lt;dimensions&gt; get_group() const</code></td>
<td>Return the constituent work-group, group representing the work-group’s position within the overall nd-range.</td>
</tr>
<tr>
<td><code>sub_group get_sub_group() const</code></td>
<td>Return a sub_group representing the subgroup to which the work-item belongs.</td>
</tr>
</tbody>
</table>

Table 4.83: Member functions for the `nd_item` class.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>size_t get_group(int dimension) const</td>
<td>Return the constituent element of the group id representing the work-group’s position within the overall nd_range in the given dimension.</td>
</tr>
<tr>
<td>size_t get_group_linear_id() const</td>
<td>Return the group id as a linear index value. Calculating a linear address from a multi-dimensional index follows the equation 4.3.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; get_group_range() const</td>
<td>Returns the number of work-groups in the iteration space.</td>
</tr>
<tr>
<td>size_t get_group_range(int dimension) const</td>
<td>Return the number of work-groups for dimension in the iteration space.</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; get_global_range() const</td>
<td>Returns a range representing the dimensions of the global iteration space.</td>
</tr>
<tr>
<td>size_t get_global_range(int dimension) const</td>
<td>Return the same value as get_global_range().get(dimension)</td>
</tr>
<tr>
<td>range&lt;dimensions&gt; get_local_range() const</td>
<td>Returns a range representing the dimensions of the current work-group.</td>
</tr>
<tr>
<td>size_t get_local_range(int dimension) const</td>
<td>Return the same value as get_local_range().get(dimension)</td>
</tr>
<tr>
<td>id&lt;dimensions&gt; get_offset() const</td>
<td>Returns an id representing the n-dimensional offset provided to the constructor of the nd_range and that is added by the runtime to the global id of each work-item.</td>
</tr>
<tr>
<td>nd_range&lt;dimensions&gt; get_nd_range() const</td>
<td>Returns the nd_range of the current execution.</td>
</tr>
<tr>
<td>template&lt;typename dataT&gt; device_event async_work_group_copy( decorated_local_ptr&lt;dataT&gt; dest, decorated_global_ptr&lt;dataT&gt; src, size_t numElements) const</td>
<td>Permitted types for dataT are all scalar and vector types. Asynchronously copies a number of elements specified by numElements from the source pointer src to destination pointer dest and returns a SYCL device_event which can be used to wait on the completion of the copy.</td>
</tr>
<tr>
<td>template&lt;typename dataT&gt; device_event async_work_group_copy( decorated_global_ptr&lt;dataT&gt; dest, decorated_local_ptr&lt;dataT&gt; src, size_t numElements) const</td>
<td>Permitted types for dataT are all scalar and vector types. Asynchronously copies a number of elements specified by numElements from the source pointer src to destination pointer dest and returns a SYCL device_event which can be used to wait on the completion of the copy.</td>
</tr>
</tbody>
</table>

Table 4.83: Member functions for the nd_item class.
### 4.10. EXPRESSING PARALLELISM THROUGH KERNELS

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `template <typename dataT>`
  `device_event async_work_group_copy(
    decorated_local_ptr<dataT> dest,
    decorated_global_ptr<dataT> src,
    size_t numElements, size_t srcStride)const` | Permitted types for dataT are all scalar and vector types. Asynchronously copies a number of elements specified by numElements from the source pointer src to destination pointer dest with a source stride specified by srcStride and returns a SYCL device_event which can be used to wait on the completion of the copy. |
| `template <typename dataT>`
  `device_event async_work_group_copy(
    decorated_global_ptr<dataT> dest,
    decorated_local_ptr<dataT> src,
    size_t numElements, size_t destStride)const` | Permitted types for dataT are all scalar and vector types. Asynchronously copies a number of elements specified by numElements from the source pointer src to destination pointer dest with a destination stride specified by destStride and returns a SYCL device_event which can be used to wait on the completion of the copy. |
| `template <typename... eventTN>`
  `void wait_for(eventTN... events)const` | Permitted type for eventTN is device_event. Waits for the asynchronous operations associated with each device_event to complete. |

Table 4.83: Member functions for the nd_item class.

#### 4.10.1.6 h_item class

`h_item<int dimensions>` identifies an instance of a `group::parallel_for_work_item` function object executing at each point in a local `range<int dimensions>` passed to a `parallel_for_work_item` call or to the corresponding `parallel_for_work_group` call if no `range` is passed to the `parallel_for_work_item` call. It encapsulates enough information to identify the work-item’s local and global items according to the information given to `parallel_for_work_group` (physical ids) as well as the work-item’s logical local items in the logical local range. All returned items objects are offset-less. Instances of the `h_item<int dimensions>` class are not user-constructible and are passed by the runtime to each instance of the function object.

The SYCL `h_item` class template provides the common by-value semantics (see Section 4.5.4).

A synopsis of the SYCL `h_item` class is provided below. The member functions of the SYCL `h_item` class are listed in Table 4.84. The additional common special member functions and common member functions are listed in 4.5.4 in Tables 4.3 and 4.4 respectively.

```cpp
namespace sycl {
  template <int dimensions>
  class h_item {
    public:
      h_item() = delete;
      /* -- common interface members -- */
      item<dimensions, false> get_global() const;
      item<dimensions, false> get_local() const;
  }
}
```

CHAPTER 4. SYCL PROGRAMMING INTERFACE
### Member function

<table>
<thead>
<tr>
<th><strong>Member function</strong></th>
<th><strong>Description</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td><code>item&lt;dimensions, false&gt; get_global() const</code></td>
<td>Return the constituent global <code>item</code> representing the work-item’s position in the global iteration space as provided upon kernel invocation.</td>
</tr>
<tr>
<td><code>item&lt;dimensions, false&gt; get_local() const</code></td>
<td>Return the same value as <code>get_logical_local()</code>.</td>
</tr>
</tbody>
</table>

Table 4.84: Member functions for the `h_item` class.
**Table 4.84: Member functions for the **\textit{h\_item}** class.**

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>\textit{item&lt;dimensions, false&gt; get_logical_local() const}</td>
<td>Return the constituent element of the logical local \textit{item} work-item’s position in the local iteration space as provided upon the invocation of the \textit{group::parallel_for_work_item}. If the \textit{group::parallel_for_work_item} was called without any logical local range then the member function returns the physical local \textit{item}. A physical id can be computed from a logical id by getting the remainder of the integer division of the logical id and the physical range: \textit{get_logical_local().get()} % \textit{get_physical_local().get()} == \textit{get_physical_local().get()}.</td>
</tr>
<tr>
<td>\textit{item&lt;dimensions, false&gt; get_physical_local() const}</td>
<td>Return the constituent element of the physical local \textit{item} work-item’s position in the local iteration space as provided (by the user or the runtime) upon the kernel invocation.</td>
</tr>
<tr>
<td>\textit{range&lt;dimensions&gt; get_global_range() const}</td>
<td>Return the same value as \textit{get_global()}.get_range()</td>
</tr>
<tr>
<td>\textit{size_t get_global_range(int dimension) const}</td>
<td>Return the same value as \textit{get_global()}.get_range(dimension)</td>
</tr>
<tr>
<td>\textit{id&lt;dimensions&gt; get_global_id() const}</td>
<td>Return the same value as \textit{get_global()}.get_id()</td>
</tr>
<tr>
<td>\textit{size_t get_global_id(int dimension) const}</td>
<td>Return the same value as \textit{get_global()}.get_id(dimension)</td>
</tr>
<tr>
<td>\textit{range&lt;dimensions&gt; get_local_range() const}</td>
<td>Return the same value as \textit{get_local()}.get_range()</td>
</tr>
<tr>
<td>\textit{size_t get_local_range(int dimension) const}</td>
<td>Return the same value as \textit{get_local()}.get_range(dimension)</td>
</tr>
<tr>
<td>\textit{id&lt;dimensions&gt; get_local_id() const}</td>
<td>Return the same value as \textit{get_local()}.get_id()</td>
</tr>
<tr>
<td>\textit{size_t get_local_id(int dimension) const}</td>
<td>Return the same value as \textit{get_local()}.get_id(dimension)</td>
</tr>
<tr>
<td>\textit{range&lt;dimensions&gt; get_logical_local_range() const}</td>
<td>Return the same value as \textit{get_logical_local()}.get_range()</td>
</tr>
<tr>
<td>\textit{size_t get_logical_local_range(int dimension) const}</td>
<td>Return the same value as \textit{get_logical_local()}.get_range(dimension)</td>
</tr>
<tr>
<td>\textit{id&lt;dimensions&gt; get_logical_local_id() const}</td>
<td>Return the same value as \textit{get_logical_local()}.get_id()</td>
</tr>
<tr>
<td>\textit{size_t get_logical_local_id(int dimension) const}</td>
<td>Return the same value as \textit{get_logical_local()}.get_id(dimension)</td>
</tr>
<tr>
<td>\textit{range&lt;dimensions&gt; get_physical_local_range() const}</td>
<td>Return the same value as \textit{get_physical_local()}.get_range()</td>
</tr>
</tbody>
</table>

Continued on next page
### 4.10. EXPRESSING PARALLELISM THROUGH KERNELS

#### 4.10.1.7 **group class**

The `group<int dimensions>` encapsulates all functionality required to represent a particular work-group within a parallel execution. It is not user-constructable.

The local range stored in the group class is provided either by the programmer, when it is passed as an optional parameter to `parallel_for_work_group`, or by the runtime system when it selects the optimal work-group size. This allows the developer to always know how many concurrent work-items are active in each executing work-group, even through the abstracted iteration range of the `parallel_for_work_item` loops.

The SYCL `group` class template provides the common by-value semantics (see Section 4.5.4).

A synopsis of the SYCL `group` class is provided below. The member functions of the SYCL `group` class are listed in Table 4.85. The additional common special member functions and common member functions are listed in 4.5.4 in Tables 4.3 and 4.4 respectively.

```cpp
namespace sycl {
    template <int Dimensions = 1>
    class group {
        public:

            using id_type = id<Dimensions>;
            using range_type = range<Dimensions>;
            using linear_id_type = size_t;

            static constexpr int dimensions = Dimensions;
            static constexpr memory_scope fence_scope = memory_scope::work_group;

            /* -- common interface members -- */

            id<Dimensions> get_group_id() const;
            size_t get_group_id(int dimension) const;

            id<Dimensions> get_local_id() const;
            size_t get_local_id(int dimension) const;

            range<Dimensions> get_local_range() const;
            size_t get_local_range(int dimension) const;
    }
}
```

Table 4.84: Member functions for the `h_item` class.
range<Dimensions> get_group_range() const;

size_t get_group_range(int dimension) const;

range<Dimensions> get_max_local_range() const;

range<Dimensions> get_uniform_group_range() const;

size_t operator[](int dimension) const;

size_t get_group_linear_id() const;

size_t get_local_linear_id() const;

size_t get_group_linear_range() const;

size_t get_local_linear_range() const;

bool leader() const;

template<typename workItemFunctionT>
void parallel_for_work_item(const workItemFunctionT &func) const;

template<typename workItemFunctionT>
void parallel_for_work_item(range<dimensions> logicalRange,
const workItemFunctionT &func) const;

template<typename dataT>
device_event async_work_group_copy(decorated_local_ptr<dataT> dest,
decorated_global_ptr<dataT> src, size_t numElements) const;

template<typename dataT>
device_event async_work_group_copy(decorated_global_ptr<dataT> dest,
decorated_local_ptr<dataT> src, size_t numElements) const;

template<typename dataT>
device_event async_work_group_copy(decorated_local_ptr<dataT> dest,
decorated_global_ptr<dataT> src, size_t numElements, size_t srcStride) const;

template<typename dataT>
device_event async_work_group_copy(decorated_global_ptr<dataT> dest,
decorated_local_ptr<dataT> src, size_t numElements, size_t destStride) const;

template<typename... eventTN>
void wait_for(eventTN... events) const;

} // sycl
### 4.10. EXPRESSING PARALLELISM THROUGH KERNELS

SYCL 2020 provisional

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>id&lt;dimensions&gt;</code> get_group_id() const</td>
<td>Return an id representing the index of the work-group within the nd-range for every dimension.</td>
</tr>
<tr>
<td><code>size_t get_group_id(int dimension) const</code></td>
<td>Return the same value as get_id()[dimension].</td>
</tr>
<tr>
<td><code>id&lt;dimensions&gt;</code> get_local_id() const</td>
<td>Return a SYCL id representing the calling work-item’s position within the work-group. It is undefined behavior for this member function to be invoked from within a parallel_for_work_item context.</td>
</tr>
<tr>
<td><code>size_t get_local_id(int dimension) const</code></td>
<td>Return the calling work-item’s position within the work-group in the specified dimension. It is undefined behavior for this member function to be invoked from within a parallel_for_work_item context.</td>
</tr>
<tr>
<td><code>range&lt;dimensions&gt;</code> get_local_range() const</td>
<td>Return a SYCL range representing all dimensions of the local range. This local range may have been provided by the programmer, or chosen by the SYCL runtime.</td>
</tr>
<tr>
<td><code>size_t get_local_range(int dimension) const</code></td>
<td>Return the dimension of the local range specified by the dimension parameter.</td>
</tr>
<tr>
<td><code>range&lt;dimensions&gt;</code> get_group_range() const</td>
<td>Return a range representing the number of work-groups in the nd_range.</td>
</tr>
<tr>
<td><code>size_t get_group_range(int dimension) const</code></td>
<td>Return element dimension from the constituent group range.</td>
</tr>
<tr>
<td><code>size_t operator[](int dimension) const</code></td>
<td>Return the same value as get_id()[dimension].</td>
</tr>
<tr>
<td><code>range&lt;dimensions&gt;</code> get_max_local_range() const</td>
<td>Return a range representing the maximum number of work-items in any work-group in the nd_range.</td>
</tr>
<tr>
<td><code>range&lt;dimensions&gt;</code> get_uniform_group_range() const</td>
<td>Return a range representing the number of work-groups in the uniform region of the nd_range.</td>
</tr>
<tr>
<td><code>size_t get_group_linear_id()</code> const</td>
<td>Get a linearized version of the work-group id. Calculating a linear work-group id from a multi-dimensional index follows the equation 4.3.</td>
</tr>
<tr>
<td><code>size_t get_group_linear_range()</code> const</td>
<td>Return the total number of work-groups in the nd_range.</td>
</tr>
<tr>
<td><code>size_t get_local_linear_id()</code> const</td>
<td>Get a linearized version of the calling work-item’s local id. Calculating a linear local id from a multi-dimensional index follows the equation 4.3. It is undefined behavior for this member function to be invoked from within a parallel_for_work_item context.</td>
</tr>
</tbody>
</table>

Table 4.85: Member functions for the group class.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>size_t get_local_linear_range() const</code></td>
<td>Return the total number of work-items in the work-group.</td>
</tr>
<tr>
<td><code>bool leader() const</code></td>
<td>Return true for exactly one work-item in the work-group, if the calling work-item is the leader of the work-group, and false for all other work-items in the work-group. The leader of the work-group is determined during construction of the work-group, and is invariant for the lifetime of the work-group. The leader of the work-group is guaranteed to be the work-item with a local id of 0.</td>
</tr>
<tr>
<td><code>template &lt;typename workItemFunctionT&gt; void parallel_for_work_item(const workItemFunctionT &amp;func) const</code></td>
<td>Launch the work-items for this work-group. <code>func</code> is a function object type with a public member function <code>void F::operator() (h_item&lt;dimensions&gt;)</code> representing the work-item computation. This member function can only be invoked within a <code>parallel_for_work_group</code> context. It is undefined behavior for this member function to be invoked from within the <code>parallel_for_work_group</code> form that does not define work-group size, because then the number of work-items that should execute the code is not defined. It is expected that this form of <code>parallel_for_work_item</code> is invoked within the <code>parallel_for_work_group</code> form that specifies the size of a work-group.</td>
</tr>
</tbody>
</table>

Table 4.85: Member functions for the `group` class.
### 4.10. Expressing Parallelism Through Kernels

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<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename workItemFunctionT&gt; void parallel_for_work_item(range&lt;dimensions&gt; logicalRange, const workItemFunctionT &amp;func) const</code></td>
<td>Launch the work-items for this work-group using a logical local range. The function object <code>func</code> is executed as if the kernel were invoked with <code>logicalRange</code> as the local range. This new local range is emulated and may not map one-to-one with the physical range. <code>logicalRange</code> is the new local range to be used. This range can be smaller or larger than the one used to invoke the kernel. <code>func</code> is a function object type with a public member function <code>void F::operator()(h_item&lt;dimensions&gt;)</code> representing the work-item computation. <strong>Note</strong> that the logical range does not need to be uniform across all work-groups in a kernel. For example the logical range may depend on a work-group varying query (e.g. <code>group::get_linear_id</code>), such that different work-groups in the same kernel invocation execute different logical range sizes. This member function can only be invoked within a <code>parallel_for_work_group</code> context.</td>
</tr>
</tbody>
</table>

| `template <typename dataT> device_event async_work_group_copy(decorated_local_ptr<dataT> dest, decorated_global_ptr<dataT> src, size_t numElements)const` | Permitted types for `dataT` are all scalar and vector types. Asynchronously copies a number of elements specified by `numElements` from the source pointer `src` to destination pointer `dest` and returns a SYCL `device_event` which can be used to wait on the completion of the copy. |

| `template <typename dataT> device_event async_work_group_copy(decorated_global_ptr<dataT> dest, decorated_local_ptr<dataT> src, size_t numElements)const` | Permitted types for `dataT` are all scalar and vector types. Asynchronously copies a number of elements specified by `numElements` from the source pointer `src` to destination pointer `dest` and returns a SYCL `device_event` which can be used to wait on the completion of the copy. |

| `template <typename dataT> device_event async_work_group_copy(decorated_local_ptr<dataT> dest, decorated_global_ptr<dataT> src, size_t numElements, size_t srcStride)const` | Permitted types for `dataT` are all scalar and vector types. Asynchronously copies a number of elements specified by `numElements` from the source pointer `src` to destination pointer `dest` with a source stride specified by `srcStride` and returns a SYCL `device_event` which can be used to wait on the completion of the copy. |

Table 4.85: Member functions for the `group` class.
### 4.10.1.8 sub_group class

The `sub_group` class encapsulates all functionality required to represent a particular `sub-group` within a parallel execution. It is not user-constructible.

The SYCL `sub_group` class provides the common by-value semantics (see Section 4.5.4).

A synopsis of the SYCL `sub_group` class is provided below. The member functions of the SYCL `sub_group` class are listed in Table 4.86. The additional common special member functions and common member functions are listed in 4.5.4 in Tables 4.3 and 4.4 respectively.

```cpp
namespace sycl {
    class sub_group {
        public:

        using id_type = id<1>;
        using range_type = range<1>;
        using linear_id_type = uint32_t;

        static constexpr int dimensions = 1;
        static constexpr memory_scope fence_scope = memory_scope::sub_group;

        /* -- common interface members -- */

        id<1> get_group_id() const;
        id<1> get_local_id() const;
        range<1> get_local_range() const;
        range<1> get_group_range() const;
        range<1> get_max_local_range() const;
        uint32_t get_group_linear_id() const;
        uint32_t get_local_linear_id() const;
    }
}
```

---

**Table 4.85: Member functions for the group class.**

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename dataT&gt; device_event async_work_group_copy(decorated_global_ptr&lt;dataT&gt; dest, decorated_local_ptr&lt;dataT&gt; src, size_t numElements, size_t destStride)</code></td>
<td>Permitted types for <code>dataT</code> are all scalar and vector types. Asynchronously copies a number of elements specified by <code>numElements</code> from the source pointer <code>src</code> to destination pointer <code>dest</code> with a destination stride specified by <code>destStride</code> and returns a SYCL <code>device_event</code> which can be used to wait on the completion of the copy.</td>
</tr>
<tr>
<td><code>template &lt;typename... eventTN&gt; void wait_for(eventTN... events)</code></td>
<td>Permitted type for <code>eventTN</code> is <code>device_event</code>. Waits for the asynchronous operations associated with each <code>device_event</code> to complete.</td>
</tr>
</tbody>
</table>

---

1. namespace sycl {
2. class sub_group {
3. public:
4.   using id_type = id<1>;
5.   using range_type = range<1>;
6.   using linear_id_type = uint32_t;
7.   static constexpr int dimensions = 1;
8.   static constexpr memory_scope fence_scope = memory_scope::sub_group;
9.   /* -- common interface members -- */
10.  id<1> get_group_id() const;
11.  id<1> get_local_id() const;
12.  range<1> get_local_range() const;
13.  range<1> get_group_range() const;
14.  range<1> get_max_local_range() const;
15.  uint32_t get_group_linear_id() const;
16.  uint32_t get_local_linear_id() const;
```
uint32_t get_group_linear_range() const;

uint32_t get_local_linear_range() const;

bool leader() const;

}; // sycl

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>id&lt;1&gt; get_group_id() const</td>
<td>Return an id representing the index of the sub-group within the work-group.</td>
</tr>
<tr>
<td>id&lt;1&gt; get_local_id() const</td>
<td>Return a SYCL id representing the calling work-item's position within the sub-group.</td>
</tr>
<tr>
<td>range&lt;1&gt; get_local_range() const</td>
<td>Return a SYCL range representing the size of the sub-group. This size may have been chosen by the programmer via an attribute, or chosen by the device compiler.</td>
</tr>
<tr>
<td>range&lt;1&gt; get_group_range() const</td>
<td>Return a range representing the number of sub-groups in the work-group.</td>
</tr>
<tr>
<td>range&lt;1&gt; get_max_local_range() const</td>
<td>Return a range representing the maximum number of work-items in any sub-group in the work-group.</td>
</tr>
<tr>
<td>uint32_t get_group_linear_id() const</td>
<td>Equivalent to get_group_id().</td>
</tr>
<tr>
<td>uint32_t get_group_linear_range() const</td>
<td>Equivalent to get_group_range().</td>
</tr>
<tr>
<td>uint32_t get_local_linear_id() const</td>
<td>Equivalent to get_local_id().</td>
</tr>
<tr>
<td>uint32_t get_local_linear_range() const</td>
<td>Equivalent to get_local_range().</td>
</tr>
<tr>
<td>bool leader() const</td>
<td>Return true for exactly one work-item in the sub-group, if the calling work-item is the leader of the sub-group, and false for all other work-items in the sub-group. The leader of the sub-group is determined during construction of the sub-group, and is invariant for the lifetime of the sub-group. The leader of the sub-group is guaranteed to be the work-item with a local id of 0.</td>
</tr>
</tbody>
</table>

Table 4.86: Member functions for the sub_group class.

4.10.2 Reduction variables

[Note for this provisional version: The reduction features described in this section support two alternative approaches for creating reducer objects and launching reduction kernels. Both alternatives are shown here to encourage feedback from implementers and developers, and it is expected that the final version of the SYCL specification will include only one approach. Please provide feedback on your preference or issues with either approach, by creating a new issue at https://github.com/KhronosGroup/SYCL-Docs/issues or extending an existing one. — end note]

All functionality related to reductions is captured by the reducer class, the reduction function, and the
The examples below demonstrate how to write a reduction kernel that performs two reductions simultaneously on the same input values, computing both the sum of all values in a buffer and the maximum value in the buffer.

In the first example, a reducer is created explicitly for each reduction variable and captured by the lambda passed to parallel_for.

```cpp
buffer<int> valuesBuf { 1024 };  
{
  // Initialize buffer on the host with 0, 1, 2, 3, ..., 1023
  host_accessor a { valuesBuf };  
  std::iota(a.begin(), a.end(), 0);
 }
// Buffers with just 1 element to get the reduction results
int sumResult = 0;  
buffer<int> sumBuf { &sumResult, 1 };  
int maxResult = 0;  
buffer<int> maxBuf { &maxResult, 1 };  
myQueue.submit([&](handler& cgh) {
  // Input values to reductions are standard accessors
  auto inputValues = valuesBuf.get_access<access::mode::read>(cgh);  
  // Create a reducer explicitly for each variable with reduction semantics
  auto sum = reducer(sumBuf.get_access<access::mode::read_write>(cgh), plus<>());  
  auto max = reducer(maxBuf.get_access<access::mode::read_write>(cgh), maximum<>());  
  // parallel_for operates on two reducers captured directly by the lambda
  cggh.parallel_for(range<1>{1024},
    [=](id<1> idx) {
    // plus<>() corresponds to += operator, so sum can be updated via += or combine()
    sum += inputValues[idx];  
    // maximum<>() has no shorthand operator, so max can only be updated via combine()
    max.combine(inputValues[idx]);
    });
  });
// sumBuf and maxBuf contain the reduction results once the kernel completes
assert(maxBuf.get_host_access()[0] == 1023 && sumBuf.get_host_access()[0] == 523776);
```

In the second example, one reducer is created explicitly and captured by the kernel lambda, and the other is created using the reduction function and passed to parallel_reduce as an argument.

```cpp
buffer<int> valuesBuf { 1024 };  
{
  // Initialize buffer on the host with 0, 1, 2, 3, ..., 1023
  host_accessor a { valuesBuf };  
  std::iota(a.begin(), a.end(), 0);
 }
```
// Buffers with just 1 element to get the reduction results
int sumResult = 0;
buffer<int> sumBuf { &sumResult, 1 };
int maxResult = 0;
buffer<int> maxBuf { &maxResult, 1 };

myQueue.submit([&](handler& cgh) {
    // Input values to reductions are standard accessors
    auto inputValues = valuesBuf.get_access<access::mode::read>(cgh);
    // Create a reducer explicitly for a variable with reduction semantics
    auto sum = reducer(sumBuf.get_access<access::mode::read_write>(cgh), plus<>());
    // Create a temporary object describing a variable with reduction semantics
    auto maxReduction = reduction(maxBuf.get_access<access::mode::read_write>(cgh), maximum<>());
    // parallel_reduce operates on two reducers:
    // - sum is captured directly by the lambda
    // - max is created from maxReduction and passed to the lambda call operator
    cgh.parallel_reduce(range<1>{1024},
            maxReduction,
            [=](id<1> id, auto& max) {
                // plus<>() corresponds to += operator, so sum can be updated via += or combine()
                sum += inputValues[id];
                // maximum<>() has no shorthand operator, so max can only be updated via combine()
                max.combine(inputValues[id]);
            });
    // sumBuf and maxBuf contain the reduction results once the kernel completes
    assert(maxBuf.get_host_access()[0] == 1023 && sumBuf.get_host_access()[0] == 523776);
});
4.10.2.1 reduction interface

The reduction interface is used to attach reduction semantics to a variable, by specifying: the reduction variable, the reduction operator and an optional identity value associated with the operator. The overloads of the interface are described in Table 4.87. The return value of the reduction interface is an implementation-defined object of unspecified type, which is interpreted by parallel_reduce to construct an appropriate reducer type as detailed in Section 4.10.2.2.

```cpp
1  template <typename AccessorT, typename BinaryOperation>
2      __unspecified__ reduction(AccessorT vars, BinaryOperation combiner);
3
4  template <typename AccessorT, typename BinaryOperation>
5      __unspecified__ reduction(AccessorT vars, const T& identity, BinaryOperation combiner);
6
7  template <typename T, typename BinaryOperation>
8      __unspecified__ reduction(T* var, BinaryOperation combiner);
9
10 template <typename T, typename BinaryOperation>
11      __unspecified__ reduction(T* var, T& identity, BinaryOperation combiner);
12
13 template <typename T, typename Extent, typename BinaryOperation>
14      __unspecified__ reduction(span<T, Extent> vars, BinaryOperation combiner);
15
16 template <typename T, typename Extent, typename BinaryOperation>
17      __unspecified__ reduction(span<T, Extent> vars, const T& identity, BinaryOperation combiner);
```

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>reduction&lt;AccessorType, BinaryOperation&gt;(AccessorType vars, BinaryOperation combiner)</td>
<td>Construct an unspecified object representing a reduction of the variable(s) described by vars using the combination operation specified by combiner. If the access mode of vars is access::mode::read_write then the reduction operation includes the original value(s) of the variable(s) described by vars. vars must not be a placeholder accessor.</td>
</tr>
</tbody>
</table>

Available only when: (accessMode == access::mode::read_write || accessMode == access::mode::discard_write) & accessTarget == access::target::global_buffer

Table 4.87: Overloads of the reduction interface.
### Table 4.87: Overloads of the `reduction` interface.

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>reduction&lt;AccessorT, BinaryOperation&gt;(AccessorT vars, AccessorT::value_type identity, BinaryOperation combiner)</code></td>
<td>Construct an unspecified object representing a reduction of the variable(s) described by <code>vars</code> using the combination operation specified by <code>combiner</code>. The value of <code>identity</code> may be used by the implementation to initialize temporary accumulation variables; using an <code>identity</code> value that is not the identity value of the combination operation specified by <code>combiner</code> results in undefined behavior. If the access mode of <code>vars</code> is <code>access::mode::read_write</code> then the reduction operation includes the original value(s) of the variable(s) described by <code>vars</code>. <code>vars</code> must not be a placeholder accessor. Available only when: `(accessMode == access::mode::read_write</td>
</tr>
<tr>
<td><code>reduction&lt;T, BinaryOperation&gt;(T* var, BinaryOperation combiner)</code></td>
<td>Construct an unspecified object representing a reduction of the variable described by <code>var</code> using the combination operation specified by <code>combiner</code>. The reduction operation includes the original value of the variable described by <code>var</code>.</td>
</tr>
<tr>
<td><code>reduction&lt;T, BinaryOperation&gt;(T* var, T identity, BinaryOperation combiner)</code></td>
<td>Construct an unspecified object representing a reduction of the variable described by <code>var</code> using the combination operation specified by <code>combiner</code>. The value of <code>identity</code> may be used by the implementation to initialize temporary accumulation variables; using an <code>identity</code> value that is not the identity value of the combination operation specified by <code>combiner</code> results in undefined behavior. The reduction operation includes the original value of the variable described by <code>var</code>.</td>
</tr>
<tr>
<td><code>reduction&lt;T, BinaryOperation&gt;(span&lt;T, Extent&gt; vars, BinaryOperation combiner)</code></td>
<td>Construct an unspecified object representing a reduction of the variable(s) described by <code>vars</code> using the combination operation specified by <code>combiner</code>. The reduction operation includes the original value(s) of the variable described by <code>vars</code>.</td>
</tr>
</tbody>
</table>
**Function** | **Description**  
---|---  
`reduction<T, BinaryOperation>(span<T, Extent> vars, T identity, BinaryOperation combiner)` | Construct an unspecified object representing a reduction of the variable(s) described by `vars` using the combination operation specified by `combiner`. The value of `identity` may be used by the implementation to initialize temporary accumulation variables; using an identity value that is not the identity value of the combination operation specified by `combiner` results in undefined behavior. The reduction operation includes the original value(s) of the variable(s) described by `vars`.

**Table 4.87: Overloads of the `reduction` interface.**

### 4.10.2.2 reducer class

The `reducer` class defines the interface between a work-item and a reduction variable during the execution of a SYCL kernel, restricting access to the underlying reduction variable. The intermediate values of a reduction variable cannot be inspected during kernel execution, and the variable cannot be updated using anything other than the reduction’s specified combination operation. The combination order of different reducers is unspecified, as are when and how the value of each reducer is combined with the original reduction variable.

A `reducer` can be constructed explicitly by a user and bound to a `command group handler`, or constructed by an implementation of `parallel_reduce` given the return value of a call to the `reduction` function. To enable compile-time specialization of reduction algorithms, the implementation of the `reducer` class is unspecified, except for the functions and operators defined in Tables 4.88, 4.89 and 4.90. It is recommended that developers use `auto` in place of specifying the template arguments of a `reducer` directly.

An implementation must guarantee that it is safe for each concurrently executing work-item in a kernel to call the combine function of a `reducer` in parallel. An implementation is free to re-use reducer variables (e.g. across work-groups scheduled to the same compute unit) if it can guarantee that it is safe to do so.

The member functions of the `reducer` class are listed in Table 4.89. Additional shorthand operators may be made available for certain combinations of reduction variable type and combination operation, as described in Table 4.90.

```cpp
// Exposition only
template <typename T, typename BinaryOperation, int Dimensions>
class reducer {

    template <access_mode Mode>
    reducer(accessor<T, Dimensions, Mode> vars, BinaryOperation combiner);

    template <access_mode Mode>
    reducer(accessor<T, Dimensions, Mode> vars, const T& identity, BinaryOperation combiner);

    reducer(T* var, BinaryOperation combiner, handler& cgh);
    reducer(T* var, const T& identity, BinaryOperation combiner, handler& cgh);
}
```

**CHAPTER 4. SYCL PROGRAMMING INTERFACE**
```cpp
template <typename Extent>
reducer(span<T, Extent> vars, BinaryOperation combiner, handler& cgh);

template <typename Extent>
reducer(span<T, Extent> vars, const T& identity, BinaryOperation combiner, handler& cgh);

reducer(const reducer<T,BinaryOperation,Dimensions>&) = delete;
reducer<T,BinaryOperation,Dimensions>& operator(const reducer<T,BinaryOperation,Dimensions>&) =
delete;

// Only available if Dimensions == 0
void combine(const T& partial);

// Only available if Dimensions > 1
__unspecified__ &operator[](size_t index) const;

// Only available if identity value is known
T identity() const;

};

template <typename T>
void operator+=(reducer<T,plus<T>,0>&, const T&);

template <typename T>
void operator*=(reducer<T,multiplies<T>,0>&, const T&);

// Only available for integral types
template <typename T>
void operator&=(reducer<T,bit_and<T>,0>&, const T&);

// Only available for integral types
template <typename T>
void operator|=(reducer<T,bit_or<T>,0>&, const T&);

// Only available for integral types
template <typename T>
void operatorˆ=(reducer<T,bit_xor<T>,0>&, const T&);

// Only available for integral types
void operator++(reducer<T,plus<T>,0>&);
```
### Constructors of the `reducer` class

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>reducer&lt;access_mode Mode&gt;(accessor&lt;T, Dimensions, Mode&gt; vars, BinaryOperation combiner)</td>
<td>Construct a <code>reducer</code> representing a reduction of the variable(s) described by <code>vars</code> using the combination operation specified by <code>combiner</code>. If the access mode of <code>vars</code> is <code>access::mode::read_write</code> then the reduction operation includes the original value(s) of the variable(s) described by <code>vars</code>. <code>vars</code> must not be a placeholder accessor. Available only when: (accessMode == access::mode::read_write</td>
</tr>
<tr>
<td>reducer&lt;access_mode Mode&gt;(accessor&lt;T, Dimensions, Mode&gt; vars, const T&amp; identity, BinaryOperation combiner)</td>
<td>Construct a <code>reducer</code> representing a reduction of the variable(s) described by <code>vars</code> using the combination operation specified by <code>combiner</code>. The value of <code>identity</code> may be used by the implementation to initialize temporary accumulation variables; using an identity value that is not the identity value of the combination operation specified by <code>combiner</code> results in undefined behavior. If the access mode of <code>vars</code> is <code>access::mode::read_write</code> then the reduction operation includes the original value(s) of the variable(s) described by <code>vars</code>. <code>vars</code> must not be a placeholder accessor. Available only when: (accessMode == access::mode::read_write</td>
</tr>
<tr>
<td>reducer(T* var, BinaryOperation combiner, handler&amp; cgh)</td>
<td>Construct a <code>reducer</code> representing a reduction of the variable described by <code>var</code> using the combination operation specified by <code>combiner</code>. The reduction operation includes the original value of the variable described by <code>var</code>.</td>
</tr>
</tbody>
</table>

Table 4.88: Constructors of the `reducer` class.
### Table 4.88: Constructors of the `reducer` class.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>reducer(T* var, const T&amp; identity, BinaryOperation combiner, handler&amp; cgh)</code></td>
<td>Construct a <code>reducer</code> representing a reduction of the variable described by <code>var</code> using the combination operation specified by <code>combiner</code>. The value of identity may be used by the implementation to initialize temporary accumulation variables; using an identity value that is not the identity value of the combination operation specified by <code>combiner</code> results in undefined behavior. The reduction operation includes the original value of the variable described by <code>var</code>.</td>
</tr>
<tr>
<td><code>reducer&lt;Extent&gt;(span&lt;T, Extent&gt; vars, BinaryOperation combine, handler&amp; cgh)</code></td>
<td>Construct a <code>reducer</code> representing a reduction of the variable(s) described by <code>vars</code> using the combination operation specified by <code>combiner</code>. The reduction operation includes the original value(s) of the variable described by <code>vars</code>.</td>
</tr>
<tr>
<td><code>reducer&lt;Extent&gt;(span&lt;T, Extent&gt; vars, const T&amp; identity, BinaryOperation combiner, handler&amp; cgh)</code></td>
<td>Construct a <code>reducer</code> representing a reduction of the variable(s) described by <code>vars</code> using the combination operation specified by <code>combiner</code>. The value of identity may be used by the implementation to initialize temporary accumulation variables; using an identity value that is not the identity value of the combination operation specified by <code>combiner</code> results in undefined behavior. The reduction operation includes the original value(s) of the variable(s) described by <code>vars</code>.</td>
</tr>
</tbody>
</table>

End of table

### Table 4.89: Member functions of the `reducer` class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>void combine(const T&amp; partial)const</code></td>
<td>Combine the value of <code>partial</code> with the reduction variable associated with this <code>reducer</code>.</td>
</tr>
</tbody>
</table>

Continued on next page
### 4.10. EXPRESSING PARALLELISM THROUGH KERNELS

**Member function**

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>unspecified</strong> &amp;operator[](size_t index) const</td>
<td>Available only when: Dimensions &gt; 1. Returns an instance of an undefined intermediate type representing a <code>reducer</code> of the same type as this <code>reducer</code>, with the dimensionality Dimensions-1 and containing an implicit SYCL id with index Dimensions set to index. The intermediate type returned must provide all member functions and operators defined by the <code>reducer</code> class that are appropriate for the type it represents (including this subscript operator).</td>
</tr>
</tbody>
</table>

| T identity() const                        | Return the identity value of the combination operation associated with this `reducer`. Only available if the identity value is known to the implementation, or was specified explicitly in the call to `reduction` that returned this `reducer`. |

Table 4.89: Member functions of the `reducer` class.

**Operator**

<table>
<thead>
<tr>
<th>Operator</th>
<th>Description</th>
</tr>
</thead>
</table>
| template <typename T>   
  void operator+=(reducer<T,plus<T>,0>& accum,  
  const T& partial) | Equivalent to calling `accum.combine(partial)`. |
| template <typename T>   
  void operator*=(reducer<T,multiplies<T>,0>& accum,  
  const T& partial) | Equivalent to calling `accum.combine(partial)`. |
| template <typename T>   
  void operator|==(reducer<T,bit_or<T>,0>& accum,  
  const T& partial) | Equivalent to calling `accum.combine(partial)`. Only available for integral types. |
| template <typename T>   
  void operator&=(reducer<T,bit_and<T>,0>& accum,  
  const T& partial) | Equivalent to calling `accum.combine(partial)`. Only available for integral types. |
| template <typename T>   
  void operatorˆ=(reducer<T,bit_xor<T>,0>& accum,  
  const T& partial) | Equivalent to calling `accum.combine(partial)`. Only available for integral types. |
| template <typename T>   
  void operator++(reducer<T,plus<T>,0>& accum) | Equivalent to calling `accum.combine(1)`. Only available for integral types. |

Table 4.90: Operators of the `reducer` class.

### 4.10.3 Command group scope

A **command group scope**, as defined in Section 3.6.1, may execute a single command such as invoking a kernel, copying memory, or executing a host task. It is legal for a **command group scope** to statically contain more than one call to a command function, but any single execution of the command group function object may execute
no more than one command. The statements that call commands together with the statements that define the requirements for a kernel form the command group function object. The command group function object takes as a parameter an instance of the command group handler class which encapsulates all the member functions executed in the command group scope. The member functions and objects defined in this scope will define the requirements for the kernel execution or explicit memory operation, and will be used by the SYCL runtime to evaluate if the operation is ready for execution. Host code within a command group function object (typically setting up requirements) is executed once, before the command group submit call returns. This abstraction of the kernel execution unifies the data with its processing, and consequently allows more abstraction and flexibility in the parallel programming models that can be implemented on top of SYCL.

The command group function object and the handler class serve as an interface for the encapsulation of command group scope. A SYCL kernel function is defined as a function object. All the device data accesses are defined inside this group and any transfers are managed by the SYCL runtime. The rules for the data transfers regarding device and host data accesses are better described in the data management section (4.7), where buffers (4.7.2) and accessor (4.7.6) classes are described. The overall memory model of the SYCL application is described in Section 3.7.1.

It is possible to obtain events for the start of the command group function object, the kernel starting, and the command group completing. These events are most useful for profiling, because safe synchronization in SYCL requires synchronization on buffer availability, not on kernel completion. This is because the memory that data is stored in upon kernel completion is not rigidly specified. The events are provided at the submission of the command group function object to the queue to be executed on.

It is possible for a command group function object to fail to enqueue to a queue, or for it to fail to execute correctly. A user can therefore supply a secondary queue when submitting a command group to the primary queue. If the SYCL runtime fails to enqueue or execute a command group on a primary queue, it can attempt to run the command group on the secondary queue. The circumstances in which it is, or is not, possible for a SYCL runtime to fall-back from primary to secondary queue are unspecified in the specification. Even if a command group is run on the secondary queue, the requirement that host code within the command group is executed exactly once remains, regardless of whether the fallback queue is used for execution.

The command group handler class provides the interface for all of the member functions that are able to be executed inside the command group scope, and it is also provided as a scoped object to all of the data access requests. The command group handler class provides the interface in which every command in the command group scope will be submitted to a queue.

### 4.10.4 Command group handler class

A command group handler object can only be constructed by the SYCL runtime. All of the accessors defined in command group scope take as a parameter an instance of the command group handler, and all the kernel invocation functions are member functions of this class.

The constructors of the SYCL handler class are described in Table 4.91.

It is disallowed for an instance of the SYCL handler class to be moved or copied.
handler(___unspecified___);

public:

template <typename dataT, int dimensions, access::mode accessMode,
           access::target accessTarget, access::placeholder isPlaceholder>
void require(accessor<dataT, dimensions, accessMode, accessTarget,
              isPlaceholder> acc);

//-- Back-end interoperability interface
//--
template <typename T>
void set_arg(int argIndex, T && arg);

template <typename... Ts>
void set_args(Ts &&... args);

//-- Kernel dispatch API
//--
// Note: In all kernel dispatch functions, the template parameter
// "typename kernelName" is optional.
//--
template <typename KernelName, typename KernelType>
void single_task(const KernelType &kernelFunc);

template <typename KernelName, typename KernelType, int dimensions>
void parallel_for(range<dimensions> numWorkItems, const KernelType &kernelFunc);

template <typename KernelName, typename KernelType, int dimensions>
void parallel_for(range<dimensions> numWorkItems,
                  id<dimensions> workItemOffset, const KernelType &kernelFunc);

template <typename KernelName, typename WorkgroupFunctionType, int dimensions>
void parallel_for_work_group(range<dimensions> numWorkGroups,
                              const WorkgroupFunctionType &kernelFunc);

template <typename KernelName, typename WorkgroupFunctionType, int dimensions>
void parallel_for_work_group(range<dimensions> numWorkGroups,
                              range<dimensions> workGroupSize,
                              const WorkgroupFunctionType &kernelFunc);

void single_task(kernel syclKernel);

void parallel_for(range<dimensions> numWorkItems, kernel syclKernel);

void parallel_for(range<dimensions> numWorkItems,
                  id<dimensions> workItemOffset, kernel syclKernel);

void parallel_for(nd_range<dimensions> ndRange, kernel syclKernel);
//------ Explicit memory operation APIs

//
template <typename T_src, int dim_src, access::mode mode_src, access::target tgt_src, access::placeholder isPlaceholder,
type_name T_dest>
void copy(accessor<T_src, dim_src, mode_src, tgt_src, isPlaceholder> src,
std::shared_ptr<T_dest> dest);

template <typename T_src,
type_name T_dest, int dim_dest, access::mode mode_dest, access::target tgt_dest, access::placeholder isPlaceholder>
void copy(std::shared_ptr<T_src> src,
accessor<T_dest, dim_dest, mode_dest, tgt_dest, isPlaceholder> dest);

template <typename T_src, int dim_src, access::mode mode_src, access::target tgt_src, access::placeholder isPlaceholder,
type_name T_dest>
void copy(accessor<T_src, dim_src, mode_src, tgt_src, isPlaceholder> src,
T_dest *dest);

template <typename T_src,
type_name T_dest, int dim_dest, access::mode mode_dest, access::target tgt_dest, access::placeholder isPlaceholder>
void copy(const T_src *src,
accessor<T_dest, dim_dest, mode_dest, tgt_dest, isPlaceholder> dest);

template <typename T_src, int dim_src, access::mode mode_src, access::target tgt_src, access::placeholder isPlaceholder_src,
type_name T_dest, int dim_dest, access::mode mode_dest, access::target tgt_dest, access::placeholder isPlaceholder_dest>
void copy(accessor<T_src, dim_src, mode_src, tgt_src, isPlaceholder_src> src,
accessor<T_dest, dim_dest, mode_dest, tgt_dest, isPlaceholder_dest> dest);

void update_host(accessor<T, dim, mode, tgt, isPlaceholder> acc);

void fill(accessor<T, dim, mode, tgt, isPlaceholder> dest, const T& src);

void use_module(const module<module_state::executable> &execModule);

template <typename T>
void use_module(const module<module_state::executable> &execModule
T deviceImageSelector);

template<auto& S>
bool has_specialization_constant() const noexcept;

template<auto& S>
type_name std::remove_reference_t<decltype(S)::type get_specialization_constant>();
SYCL 2020 provisional

### Expressing Parallelism Through Kernels

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>handler(___unspecified___)</code></td>
<td>Unspecified implementation defined constructor.</td>
</tr>
</tbody>
</table>

End of table

Table 4.91: Constructors of the `handler` class.

### 4.10.5 Class `kernel_handler`

Functionality and queries that are unique to the invocation of a SYCL kernel function are made available at the kernel scope via a kernel handler. A kernel handler is associated with the SYCL kernel function that is being invoked and the module and device image used by the SYCL runtime. A kernel handler is represented by the class `kernel_handler`.

The kernel handler is optional, and is only used if the SYCL kernel function has a `kernel_handler` as an additional parameter, in which case the SYCL runtime will construct an instance of `kernel_handler` and pass it to the SYCL kernel function as an argument. A `kernel_handler` is not user constructible and can only be constructed by the SYCL runtime.

```cpp
class kernel_handler;
```

#### 4.10.5.1 Constructors

1. `kernel_handler(___unspecified___); // (1)`

   1. **Effects:** Unspecified private constructor for the SYCL runtime to use to construct a kernel handler.

#### 4.10.5.2 Member functions

1. `template<auto& S> bool has_specialization_constant() const noexcept; // (1)`

   1. **Returns:** true if any of the SYCL kernel functions represented by the associated module contains the specialization constant represented by the specialization_id at the address `S`, otherwise returns false.

2. `template<auto& S> typename std::remove_reference_t<decltype(S)>::type get_specialization_constant(); // (1)`

   1. **Returns:** The value of the specialization constant associated with the specialization_id at the address `S`, from the associated module, if the specialization constant has been set, otherwise returns the default value.

   **Effects:** If the associated module is associated with a host context or this->has_specialization_constant(S)() evaluates to false this member function is undefined.
4.10.6 SYCL functions for adding requirements

When an accessor is created from a command group handler, a requirement is implicitly added to the command group for the accessor's data. However, this does not happen when creating a placeholder accessor. In order to create a requirement for a placeholder accessor, code must call the handler::require() member function.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| template <typename dataT, int dimensions, access::mode accessMode, access::target accessTarget, access::placeholder isPlaceholder>
  void require(accessor<dataT, dimensions, accessMode, accessTarget, isPlaceholder> acc) | Requires access to the memory object associated with the accessor. The command group now has a requirement to gain access to the given memory object before executing the kernel. If the accessor has already been registered with the command group, calling this function has no effect. Throws exception with the errc::accessor_error error code if (acc.empty() == true). |

Table 4.92: Member functions of the handler class.

4.10.7 SYCL functions for invoking kernels

Kernels can be invoked as single tasks, basic data-parallel kernels, nd-range in work-groups, or hierarchical parallelism.

Each function takes an optional kernel name template parameter. The user may optionally provide a kernel name, otherwise an implementation defined name will be generated for the kernel.

All the functions for invoking kernels are member functions of the command group handler class 4.10.4, which is used to encapsulate all the member functions provided in a command group scope. Table 4.93 lists all the members of the handler class related to the kernel invocation.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| template <typename T>
  void set_arg(int argIndex, T &&arg) | Set a kernel argument for a kernel through the SYCL backend interoperability interface. The index value specifies which parameter of the low-level kernel is being set and arg specifies the kernel argument. Index 0 is the first parameter. The argument can be either a SYCL accessor, a SYCL sampler or a trivially copyable C++ type. Note it is invalid to set arguments to SYCL kernel function objects. |

Table 4.93: Member functions of the handler class.
### Member function | Description
---|---
`template <typename... Ts> void set_args(Ts &... args)` | Set all the given kernel args arguments for an kernel through the SYCL backend interoperability interface, as if `set_arg()` was used with each of them in the same order and increasing index always starting at 0.

`template <typename KernelName, typename KernelType> void single_task(const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type. Specification of a kernel name (`typename KernelName`), as described in Section 4.10.7, is optional. The callable `KernelType` can optionally take a `kernel_handler` in which case the SYCL runtime will construct an instance of `kernel_handler` and pass it to `KernelType`.

`template <typename KernelName, typename KernelType, int dimensions> void parallel_for(range<dimensions> numWorkItems, const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified range and given an item or integral type (e.g. `int`, `size_t`), if range is 1-dimensional, for indexing in the indexing space defined by range. Generic kernel functions are permitted, in that case the argument type is an `item`. Specification of a kernel name (`typename KernelName`), as described in Section 4.10.7, is optional. The callable `KernelType` can optionally take a `kernel_handler` as it's last parameter, in which case the SYCL runtime will construct an instance of `kernel_handler` and pass it to `KernelType`.

`template <typename KernelName, typename KernelType, int dimensions> void parallel_for(range<dimensions> numWorkItems, id<dimensions> workItemOffset, const KernelType &kernelFunc)` | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified range and offset and given an item or integral type (e.g. `int`, `size_t`), if range is 1-dimensional, for indexing in the indexing space defined by range. Generic kernel functions are permitted, in that case the argument type is an `item`. Specification of a kernel name (`typename KernelName`), as described in Section 4.10.7, is optional. The callable `KernelType` can optionally take a `kernel_handler` as it's last parameter, in which case the SYCL runtime will construct an instance of `kernel_handler` and pass it to `KernelType`.

Table 4.93: Member functions of the `handler` class.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `template <typename KernelName, typename KernelType, int dimensions>
  void parallel_for(
  nd_range<dimensions> executionRange,
  const KernelType &kernelFunc)`                                                | Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified nd-range and given an nd-item for indexing in the indexing space defined by the nd-range. Generic kernel functions are permitted, in that case the argument type is an nd-item. Specification of a kernel name (typename KernelName), as described in Section 4.10.7, is optional. The callable KernelType can optionally take a kernel_handler as it’s last parameter, in which case the SYCL runtime will construct an instance of kernel_handler and pass it to KernelType. |
| `template <typename KernelName, typename WorkgroupFunctionType, int dimensions>
  void parallel_for_work_group(
  range<dimensions> numWorkGroups,
  const WorkgroupFunctionType &kernelFunc)`                                    | Defines and invokes a hierarchical kernel as a lambda function encoding the body of each work-group to launch. Generic kernel functions are permitted, in that case the argument type is a group. May contain multiple calls to parallel_for_work_item(...) member functions representing the execution on each work-item. Launches num_work_groups work-groups of runtime-defined size. Described in detail in 4.10.7. The callable WorkgroupFunctionType can optionally take a kernel_handler as it’s last parameter, in which case the SYCL runtime will construct an instance of kernel_handler and pass it to WorkgroupFunctionType. |
| `template <typename KernelName, typename WorkgroupFunctionType, int dimensions>
  void parallel_for_work_group(
  range<dimensions> numWorkGroups,
  range<dimensions> workGroupSize,
  const WorkgroupFunctionType &kernelFunc)`                                    | Defines and invokes a hierarchical kernel as a lambda function encoding the body of each work-group to launch. Generic kernel functions are permitted, in that case the argument type is a group. May contain multiple calls to parallel_for_work_item member functions representing the execution on each work-item. Launches num_work_groups work-groups of work_group_size work-items each. Described in detail in 4.10.7. The callable WorkgroupFunctionType can optionally take a kernel_handler as it’s last parameter, in which case the SYCL runtime will construct an instance of kernel_handler and pass it to WorkgroupFunctionType. |

Continued on next page

Table 4.93: Member functions of the handler class.
### Member function |
**template <typename KernelName, typename KernelType, int dimensions, typename... Reductions>**
void parallel_reduce(
    range<dimensions> numWorkItems, Reductions... reductions, const KernelType &kernelFunc)

<table>
<thead>
<tr>
<th>Description</th>
</tr>
</thead>
</table>
| Defines and invokes a **SYCL kernel function** as a lambda function or a named function object type, for the specified range and given an id or item for indexing in the indexing space defined by range. Specification of a **kernel name** (**typename KernelName**), as described in Section 4.10.7, is optional. The callable **KernelType** can optionally take a **kernel_handler** as it’s last parameter, in which case the **SYCL runtime** will construct an instance of **kernel_handler** and pass it to **KernelType**.

The **reductions** parameter pack consists of 1 or more objects created by the **reduction** function. For each object in reductions, the kernel functor should take an additional parameter corresponding to that object’s **reducer** type. |

### Member function |
**template <typename KernelName, typename KernelType, int dimensions, typename... Reductions>**
void parallel_reduce(
    range<dimensions> numWorkItems,
    id<dimensions> workItemOffset, Reductions... reductions, const KernelType &kernelFunc)

<table>
<thead>
<tr>
<th>Description</th>
</tr>
</thead>
</table>
| Defines and invokes a **SYCL kernel function** as a lambda function or a named function object type, for the specified range and offset and given an id or item for indexing in the indexing space defined by range. Specification of a **kernel name** (**typename KernelName**), as described in Section 4.10.7, is optional. The callable **KernelType** can optionally take a **kernel_handler** as it’s last parameter, in which case the **SYCL runtime** will construct an instance of **kernel_handler** and pass it to **KernelType**.

The **reductions** parameter pack consists of 1 or more objects created by the **reduction** function. For each object in reductions, the kernel functor should take an additional parameter corresponding to that object’s **reducer** type. |

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Table 4.93: Member functions of the **handler** class.
### 4.10. EXPRESSING PARALLELISM THROUGH KERNELS

<p>|</p>
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>template &lt;typename KernelName, typename KernelType, int dimensions, typename... Reductions&gt; void parallel_reduce( nd_range&lt;dimensions&gt; executionRange, Reductions ... reductions, const KernelType &amp;kernelFunc)</td>
<td>Defines and invokes a SYCL kernel function as a lambda function or a named function object type, for the specified nd-range and given an nd-item for indexing in the indexing space defined by the nd-range. Specification of a kernel name (typename KernelName), as described in Section 4.10.7, is optional. The callable KernelType can optionally take a kernel_handler as its last parameter, in which case the SYCL runtime will construct an instance of kernel_handler and pass it to KernelType, as described in 4.10.7. The reductions parameter pack consists of 1 or more objects created by the reduction function. For each object in reductions, the kernel functor should take an additional parameter corresponding to that object’s reducer type.</td>
</tr>
<tr>
<td>void single_task(kernel syclKernel)</td>
<td>Invokes a pre-compiled kernel which executes exactly once.</td>
</tr>
<tr>
<td>template &lt;int dimensions&gt; void parallel_for( range&lt;dimensions&gt; numWorkItems, kernel syclKernel)</td>
<td>Invokes a pre-compiled kernel for the specified range and given an item or integral type (e.g int.size_t), if range is 1-dimensional, for indexing in the indexing space defined by range. Generic kernel functions are permitted, in that case the argument type is an item. Described in detail in 4.10.7.</td>
</tr>
<tr>
<td>template &lt;int dimensions&gt; void parallel_for( range&lt;dimensions&gt; numWorkItems, id&lt;dimensions&gt; workItemOffset, kernel syclKernel )</td>
<td>Invokes a pre-compiled kernel for the specified range and offset and given an item or integral type (e.g int.size_t), if range is 1-dimensional, for indexing in the indexing space defined by range. Generic kernel functions are permitted, in that case the argument type is an item. Described in detail in 4.10.7.</td>
</tr>
<tr>
<td>template &lt;int dimensions&gt; void parallel_for( nd_range&lt;dimensions&gt; ndRange, kernel syclKernel)</td>
<td>Invokes a pre-compiled kernel for the specified ndrange and given an nd-item for indexing in the indexing space defined by the nd_range. Generic kernel functions are permitted, in that case the argument type is an nd_item. Described in detail in 4.10.7.</td>
</tr>
</tbody>
</table>

Table 4.93: Member functions of the handler class.
4.10.7.1 single_task invoke

SYCL provides a simple interface to enqueue a kernel that will be sequentially executed on a device. Only one instance of the kernel will be executed. This interface is useful as a primitive for more complicated parallel algorithms, as it can easily create a chain of sequential tasks on a SYCL device with each of them managing its own data transfers.

This function can only be called inside a command group using the handler object created by the runtime. Any accessors that are used in a kernel should be defined inside the same command group.

Local accessors are disallowed for single task invocations.

```cpp
myQueue.submit([&](handler & cgh) {
    cgh.single_task(
        [=] () {
            // [kernel code]
        });
});
```

For single tasks, the kernel member function takes no parameters, as there is no need for index space classes in a unary index space.

A kernel_handler can optionally be passed as a parameter to the SYCL kernel function that is invoked by single_task for the purpose explained in Section 4.10.5.

```cpp
myQueue.submit([&](handler & cgh) {
    cgh.single_task(
        [=] (kernel_handler kh) {
            // [kernel code]
        });
});
```

4.10.7.2 parallel_for invoke

The parallel_for member function of the SYCL handler class provides an interface to define and invoke a SYCL kernel function in a command group, to execute in parallel execution over a 3 dimensional index space. There are three overloads of the parallel_for member function which provide variations of this interface, each with a different level of complexity and providing a different set of features.

For the simplest case, users need only provide the global range (the total number of work-items in the index space) via a SYCL range parameter, and the SYCL runtime will select a local range (the number of work-items in each work-group). The local range chosen by the SYCL runtime is entirely implementation defined. In this case the function object that represents the SYCL kernel function must take one of: 1) a single SYCL item parameter, 2) single generic parameter (template parameter or auto), 3) any other type implicitly converted from SYCL item, representing the currently executing work-item within the range specified by the range parameter.

The execution of the kernel function is the same whether the parameter to the SYCL kernel function is a SYCL id or a SYCL item. What differs is the functionality that is available to the SYCL kernel function via the respective interfaces.

Below is an example of invoking a SYCL kernel function with parallel_for using a lambda function, and passing a SYCL id parameter. In this case only the global id is available. This variant of parallel_for is designed for
when it is not necessary to query the global range of the index space being executed across, or the local (workgroup) size chosen by the implementation.

```cpp
myQueue.submit([&](handler & cgh) {
    accessor acc { myBuffer, cgh, write_only }

cgh.parallel_for(range<1>(numWorkItems),
    [=] (id<1> index) {
        acc[index] = 42.0f;
    });
});
```

Below is an example of invoking a SYCL kernel function with `parallel_for` using a lambda function and passing a SYCL `item` parameter. In this case both the global id and global range are queryable. This variant of `parallel_for` is designed for when it is necessary to query the global range within which the global id will vary. No information is queryable on the local (work-group) size chosen by the implementation.

```cpp
myQueue.submit([&](handler & cgh) {
    accessor acc { myBuffer, cgh, write_only }

cgh.parallel_for(range<1>(numWorkItems),
    [=] (item<1> item) {
        size_t index = item.get_linear_id();
        acc[index] = index;
    });
});
```

Below is an example of invoking a SYCL kernel function with `parallel_for` using a lambda function and passing `auto` parameter, treated as `item`. In this case both the global id and global range are queryable. The same effect can be achieved using class with templatized `operator()`. This variant of `parallel_for` is designed for when it is necessary to query the global range within which the global id will vary. No information is queryable on the local (work-group) size chosen by the implementation.

```cpp
myQueue.submit([&](handler & cgh) {
    auto acc = myBuffer.get_access<access::mode::write>(cgh);

cgh.parallel_for(range<1>(numWorkItems),
    [=] (auto item) {
        size_t index = item.get_linear_id();
        acc[index] = index;
    });
});
```

Below is an example of invoking a SYCL kernel function with `parallel_for` using a lambda function and passing integral type (e.g. `int, size_t`) parameter. This example is only valid when calling `parallel_for` with `range<1>`. In this case, only the global id is available. This variant of `parallel_for` is designed for when it is not necessary to query the global range of the index space being executed across, or the local (workgroup) size chosen by the implementation.
myQueue.submit([&](handler & cgh) {
    auto acc = myBuffer.get_access<access::mode::write>(cgh);
    cgh.parallel_for(range<1>(numWorkItems), [=] (size_t index) {
        // kernel argument type is size_t
        acc[index] = index;
    });
});

The `parallel_for` overload without an offset can be called with either a number or a braced-init-list with 1-3 elements. In that case the following calls are equivalent:

- `parallel_for(N, some_kernel)` has same effect as `parallel_for(range<1>(N), some_kernel)`
- `parallel_for({N}, some_kernel)` has same effect as `parallel_for(range<1>(N), some_kernel)`
- `parallel_for({N1, N2}, some_kernel)` has same effect as `parallel_for(range<2>(N1, N2), some_kernel)`
- `parallel_for({N1, N2, N3}, some_kernel)` has same effect as `parallel_for(range<3>(N1, N2, N3), some_kernel)`

Below is an example of invoking `parallel_for` with a number instead of an explicit `range` object.

myQueue.submit([&](handler & cgh) {
    auto acc = myBuffer.get_access<access::mode::write>(cgh);
    // parallel_for may be called with number (with numWorkItems)
    cgh.parallel_for(numWorkItems,
    [=] (auto item) {
        size_t index = item.get_linear_id();
        acc[index] = index;
    });
});

For SYCL kernel functions invoked via the above described overload of the `parallel_for` member function, it is disallowed to use local accessors or to use a work-group barrier.

The following two examples show how a kernel function object can be launched over a 3D grid, with 3 elements in each dimension. In the first case work-item ids range from 0 to 2 inclusive, and in the second case work-item ids run from 1 to 3.

myQueue.submit([&](handler & cgh) {
    cgh.parallel_for(  
        range<3>(3,3,3), // global range  
        [=] (item<3> it) {
            // [kernel code]  
        });
});
myQueue.submit([&](handler & cgh) {
    cgh.parallel_for(  
        range<3>(3,3,3), // global range  
    });
The last case of a parallel_for invocation enables low-level functionality of work-items and work-groups. This becomes valuable when an execution requires groups of work-items that communicate and synchronize. These are exposed in SYCL through parallel_for (nd_range,...) and the nd_item class. In this case, the developer needs to define the nd_range that the kernel will execute on in order to have fine grained control of the enqueuing of the kernel. This variation of parallel_for expects an nd_range, specifying both local and global ranges, defining the global number of work-items and the number in each cooperating work-group. The resulting function object is passed an nd_item instance making all the information available, as well as work-group barrier to synchronize between the work-items in the work-group.

The following example shows how sixty-four work-items may be launched in a three-dimensional grid with four in each dimension, and divided into eight work-groups. Each group of work-items synchronizes with a work-group barrier.

In all of these cases the underlying nd-range will be created and the kernel defined as a function object will be created and enqueued as part of the command group scope.

A kernel_handler can optionally be passed as a parameter to the SYCL kernel function that is invoked by both variants of parallel_for.

In all of these cases the underlying nd-range will be created and the kernel defined as a function object will be created and enqueued as part of the command group scope.

A kernel_handler can optionally be passed as a parameter to the SYCL kernel function that is invoked by both variants of parallel_for.
4.10.7.3 Parallel for hierarchical invoke

The hierarchical parallel kernel execution interface provides the same functionality as is available from the nd-range interface, but exposed differently. To execute the same sixty-four work-items in sixteen work-groups that we saw in the previous example, we execute an outer parallel_for_work_group call to create the groups. The member function handler::parallel_for_work_group is parameterized by the number of work-groups, such that the size of each group is chosen by the runtime, or by the number of work-groups and number of work-items for users who need more control.

The body of the outer parallel_for_work_group call consists of a lambda function or function object. The body of this function object contains code that is executed only once for the entire work-group. If the code has no side-effects and the compiler heuristic suggests that it is more efficient to do so, this code will be executed for each work-item.

Within this region any variable declared will have the semantics of local memory, shared between all work-items in the work-group. If the device compiler can prove that an array of such variables is accessed only by a single work-item throughout the lifetime of the work-group, for example if access is derived from the id of the work-item with no transformation, then it can allocate the data in private memory or registers instead.

To guarantee use of private per-work-item memory, the private_memory class can be used to wrap the data. This class simply constructs private data for a given group across the entire group. The id of the current work-item is passed to any access to grab the correct data.

The private_memory class has the following interface:

```cpp
namespace sycl {
    template <typename T, int Dimensions = 1>
    class private_memory {
        public:
            // Construct based directly off the number of work-items
            private_memory(const group<Dimensions> &);

            // Access the instance for the current work-item
            T &operator()(const h_item<Dimensions> &id);
    }
}
```

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>private_memory(const group&lt;Dimensions&gt; &amp;)</td>
<td>Place an object of type T in the underlying private memory of each work-item. The type T must be default constructible. The underlying constructor will be called for each work-item.</td>
</tr>
</tbody>
</table>

Table 4.94: Constructor of the private_memory class.
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<table>
<thead>
<tr>
<th>Member functions</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>T &amp;operator()(const h_item&lt;Dimensions&gt; &amp;id)</td>
<td>Retrieve a reference to the object for the work-items.</td>
</tr>
</tbody>
</table>

End of table

Table 4.95: Member functions of the private_memory class.

Private memory is allocated per underlying work-item, not per iteration of the parallel_for_work_item loop. The number of instances of a private memory object is only under direct control if a work-group size is passed to the parallel_for_work_group call. If the underlying work-group size is chosen by the runtime, the number of private memory instances is opaque to the program. Explicit private memory declarations should therefore be used with care and with a full understanding of which instances of a parallel_for_work_item loop will share the same underlying variable.

Also within the lambda body can be a sequence of calls to parallel_for_work_item. At the edges of these inner parallel executions the work-group synchronizes. As a result the pair of parallel_for_work_item calls in the code below is equivalent to the parallel execution with a work-group barrier in the earlier example.

```cpp
myQueue.submit([&](handler & cgh) {
    // Issue 8 work-groups of 8 work-items each
    cgh.parallel_for_work_group(
        range<3>(2, 2, 2), range<3>(2, 2, 2), [=](group<3> myGroup) {
            // [workgroup code]
            int myLocal; // this variable is shared between workitems
            // this variable will be instantiated for each work-item separately
            private_memory<int> myPrivate(myGroup);

            // Issue parallel work-items. The number issued per work-group is determined
            // by the work-group size range of parallel_for_work_group. In this case,
            // 8 work-items will execute the parallel_for_work_item body for each of the
            // 8 work-groups, resulting in 64 executions globally/total.
            myGroup.parallel_for_work_item([&](h_item<3> myItem) {
                // [work-item code]
                myPrivate(myItem) = 0;
            });

            // Implicit work-group barrier

            // Carry private value across loops
            myGroup.parallel_for_work_item([&](h_item<3> myItem) {
                // [work-item code]
                output[myItem.get_global_id()] = myPrivate(myItem);
            });
        });
});
```

It is valid to use more flexible dimensions of the work-item loops. In the following example we issue 8 work-groups but let the runtime choose their size, by not passing a work-group size to the parallel_for_work_group call. The parallel_for_work_item loops may also vary in size, with their execution ranges unrelated to the dimensions of the work-group, and the compiler generating an appropriate iteration space to fill the gap. In this
case, the h_item provides access to local ids and ranges that reflect both kernel and parallel_for_work_item invocation ranges.

```cpp
myQueue.submit([&](handler & cgh) {
    // Issue 8 work-groups. The work-group size is chosen by the runtime because unspecified
    cgh.parallel_for_work_group(
        range<3>(2, 2, 2), [=](group<3> myGroup) {
            // Launch a set of work-items for each work-group. The number of work-items is chosen
            // by the runtime because the work-group size was not specified to parallel_for_work_group
            // and a logical range is not specified to parallel_for_work_item.
            myGroup.parallel_for_work_item([=](h_item<3> myItem) {
                // [work-item code]
            });
            // Implicit work-group barrier
            // Launch 512 logical work-items that will be executed by the underlying work-group size
            // chosen by the runtime. myItem allows the logical and physical work-item IDs to be
            // queried. 512 logical work-items will execute for each work-group, and the parallel_for
            // body will therefore be executed 8*512 = 4096 times globally/total.
            myGroup.parallel_for_work_item(range<3>(8, 8, 8), [=](h_item<3> myItem) {
                // [work-item code]
            });
            // [workgroup code]
        });
    // [workgroup code]
});
```

This interface offers a more intuitive way for tiling parallel programming paradigms. In summary, the hierarchical model allows a developer to distinguish the execution at work-group level and at work-item level using the parallel_for_work_group and the nested parallel_for_work_item functions. It also provides this visibility to the compiler without the need for difficult loop fission such that host execution may be more efficient.

A kernel_handler can optionally be passed as a parameter to the SYCL kernel function that is invoked by any variant of parallel_for_work_group.

```cpp
myQueue.submit([&](handler & cgh) {
    // Issue 8 work-groups of 8 work-items each
    cgh.parallel_for_work_group(
        range<3>(2, 2, 2), range<3>(2, 2, 2), [=](group<3> myGroup,
            kernel_handler kh) {
            // [workgroup code]
            int myLocal; // this variable is shared between workitems
            // this variable will be instantiated for each work-item separately
            private_memory<int> myPrivate(myGroup);
            // Issue parallel work-items. The number issued per work-group is determined
            // by the work-group size range of parallel_for_work_group. In this case,
            // 8 work-items will execute the parallel_for_work_item body for each of the
            // 8 work-groups, resulting in 64 executions globally/total.
            myGroup.parallel_for_work_item([=](h_item<3> myItem) {
                // [work-item code]
            });
        });
    // [workgroup code]
});
```
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```cpp
myPrivate(myItem) = 0;
});

// Implicit work-group barrier

// Carry private value across loops
myGroup.parallel_for_work_item([&](h_item<3> myItem) {
    //[work-item code]
    output[myItem.get_global_id()] = myPrivate(myItem);
});

//[workgroup code]
```

4.10.8 SYCL functions for explicit memory operations

In addition to kernels, command group objects can also be used to perform manual operations on host and device memory by using the `copy` API of the command group handler. Manual copy operations can be seen as specialized kernels executing on the device, except that typically this operations will be implemented using the OpenCL host API (e.g., enqueue copy operations).

The SYCL memory objects involved in a copy operation are specified using accessors. Explicit copy operations have a source and a destination. When an accessor is the source of the operation, the destination can be a host pointer or another accessor. The source accessor can have either read or read_write access mode.

When an accessor is the destination of the explicit copy operation, the source can be a host pointer or another accessor. The destination accessor can have either write, read_write, discard_write, discard_read_write access modes.

When accessors are both the origin and the destination, the operation is executed on objects controlled by the SYCL runtime. The SYCL runtime is allowed to not perform an explicit in-copy operation if a different path to update the data is available according to the SYCL application memory model.

The most recent copy of the memory object may reside on any context controlled by the SYCL runtime, or on the host in a pointer controlled by the SYCL runtime. The SYCL runtime will ensure that data is copied to the destination once the command group has completed execution.

Whenever a host pointer is used as either the host or the destination of these explicit memory operations, it is the responsibility of the user for that pointer to have at least as much memory allocated as the accessor is giving access to, e.g.: if an accessor accesses a range of 10 elements of `int` type, the host pointer must at least have $10 \times \text{sizeof(int)}$ bytes of memory allocated.

A special case is the update_host member function. This member function only requires an accessor, and instructs the runtime to update the internal copy of the data in the host, if any. This is particularly useful when users use manual synchronization with host pointers, e.g. via mutex objects on the buffer constructors.

Table 4.96 describes the interface for the explicit copy operations.
### Member function

<table>
<thead>
<tr>
<th>Description</th>
<th>Syntax</th>
</tr>
</thead>
<tbody>
<tr>
<td>Copies the contents of the memory object accessed by src into the memory pointed to by dest. dest must have at least as many bytes as the range accessed by src.</td>
<td>template &lt;typename T_src, int dim_src, access::mode mode_src, access::target tgt_src, typename T_dest, access::placeholder isPlaceholder&gt; void copy(accessor&lt;T_src, dim_src, mode_src, tgt_src, isPlaceholder&gt; src, std::shared_ptr&lt;T_dest&gt; dest)</td>
</tr>
<tr>
<td>Copies the contents of the memory pointed to by src into the memory object accessed by dest. src must have at least as many bytes as the range accessed by dest.</td>
<td>template &lt;typename T_src, typename T_dest, int dim_dest, access::mode mode_dest, access::target tgt_dest, access::placeholder isPlaceholder&gt; void copy(std::shared_ptr&lt;T_src&gt; src, accessor&lt;T_dest, dim_dest, mode_dest, tgt_dest, isPlaceholder&gt; dest)</td>
</tr>
<tr>
<td>Copies the contents of the memory object accessed by src into the memory pointed to by dest. dest must have at least as many bytes as the range accessed by src.</td>
<td>template &lt;typename T_src, int dim_src, access::mode mode_src, access::target tgt_src, typename T_dest, access::placeholder isPlaceholder&gt; void copy(accessor&lt;T_src, dim_src, mode_src, tgt_src, isPlaceholder&gt; src, T_dest * dest)</td>
</tr>
<tr>
<td>Copies the contents of the memory pointed to by src into the memory object accessed by dest. src must have at least as many bytes as the range accessed by dest.</td>
<td>template &lt;typename T_src, typename T_dest, int dim_dest, access::mode mode_dest, access::target tgt_dest, access::placeholder isPlaceholder&gt; void copy(const T_src * src, accessor&lt;T_dest, dim_dest, mode_dest, tgt_dest, isPlaceholder&gt; dest)</td>
</tr>
<tr>
<td>Copies the contents of the memory object accessed by src into the memory object accessed by dest. src must have at least as many bytes as the range accessed by dest.</td>
<td>template &lt;typename T_src, int dim_src, access::mode mode_src, access::target tgt_src, access::placeholder isPlaceholder, typename T_dest, int dim_dest, access::mode mode_dest, access::target tgt_dest, access::placeholder isPlaceholder_dest&gt; void copy(accessor&lt;T_src, dim_src, mode_src, tgt_src, isPlaceholder_src&gt; src, accessor&lt;T_dest, dim_dest, mode_dest, tgt_dest, isPlaceholder_dest&gt; dest)</td>
</tr>
<tr>
<td>The contents of the memory object accessed via acc on the host are guaranteed to be up-to-date after this command group object execution is complete.</td>
<td>template &lt;typename T, int dim, access::mode mode, access::target tgt, access::placeholder isPlaceholder&gt; void update_host(accessor&lt;T, dim, mode, tgt, isPlaceholder&gt; acc)</td>
</tr>
<tr>
<td>Replicates the value of src into the memory object accessed by dest. T must be a scalar value or a SYCL vector type.</td>
<td>template &lt;typename T, int dim, access::mode mode, access::target tgt, access::placeholder isPlaceholder&gt; void fill(accessor&lt;T, dim, mode, tgt, isPlaceholder&gt; dest, const T&amp; src)</td>
</tr>
</tbody>
</table>

Table 4.96: Member functions of the handler class.

The listing below illustrates how to use explicit copy operations in SYCL. The example copies half of the contents of a `std::vector` into the device, leaving the rest of the contents of the buffer on the device unchanged.
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1. const size_t nElems = 10u;
2. // Create a vector and fill it with values 0, 1, 2, 3, 4, 5, 6, 7, 8, 9
3. std::vector<int> v { nElems };  
4. std::iota(std::begin(v), std::end(v), 0);  
5. // Create a buffer with no associated user storage  
6. sycl::buffer<int, 1> b { range<1>(nElems) };  

7. // Create a queue  
8. queue myQueue;  
9. myQueue.submit(
10.   [] (handler &cgh) {
11.     // Retrieve a ranged write accessor to a global buffer with access to the  
12.     // first half of the buffer  
13.     accessor acc { b, cgh, range<1>({nElems / 2}, id<1>({0}), write_only };  
14.     // Copy the first five elements of the vector into the buffer associated with  
15.     // the accessor  
16.     cgh.copy(v.data(), acc);
17.   });

4.10.9 Functions for using a module

1. void use_module(const module<module_state::executable> &execModule); // (1)  
2. template<
3.     typename T
4.     void use_module(const module<module_state::executable> &execModule // (2)
5.     T deviceImageSelector);

1. Effects: The command group associated with the handler will use device images of the module execModule in any kernel invocation commands for all SYCL kernel functions represented by the module. If the module contains multiple device images that are compatible with the device associated with handler, then the device image chosen is implementation defined.

Throws: errc::invalid_object_error if the context associated with the command group handler via its associated queue is different from the context associated with the module specified by execModule.

2. Effects: The command group associated with the handler will use device images of the module execModule in any kernel invocation commands for all SYCL kernel functions represented by the module. All device images in the module which are compatible with the device associated with handler will be passed to the device image selection function. The device image with the highest score will be chosen.

Throws: errc::invalid_object_error if no device image could be selected.

4.10.10 Functions for using specialization constants

1. template<
2.     auto & S>
3.     bool has_specialization_constant() const noexcept; // (1)
1. **Returns:** true if any of the SYCL kernel functions represented by the module associated with the command group handler contain the specialization constant represented by the specialization_id at the address S, otherwise returns false.

```cpp
template<auto& S>
typename std::remove_reference_t<decltype(S)>::type get_specialization_constant(); // (1)
```

1. **Returns:** From the module associated with the command group handler, the value of the specialization constant associated with the specialization_id at the address S if the specialization constant has been set, otherwise returns the default value.

**Throws:** errc::invalid_object_error if this->has_specialization_constant<S>() evaluates to false.

### 4.11 Host tasks

#### 4.11.1 Overview

A host task is a native C++ callable which is scheduled by the SYCL runtime. A host task is submitted to a queue via a command group by a host task command.

When a host task command is submitted to a queue it is scheduled based on its data dependencies with other commands including kernel invocation commands and asynchronous copies, resolving any requisites created by accessors attached to the command group as defined in Section 3.7.1.

Since a host task is invoked directly by the SYCL runtime rather than being compiled as a SYCL kernel function, it does not have the same restrictions as a SYCL kernel function, and can therefore contain any arbitrary C++ code. However, capturing or using any SYCL class with reference semantics (see Section 4.5.3) is undefined behaviour.

A host task can be enqueued on any queue including a host queue and the callable will be invoked directly by the SYCL runtime, regardless of which device the queue is associated with.

A host task is enqueued on a queue via the host_task member function of the handler class.

A host task can optionally be used to interoperate with the native backend objects associated with the queue executing the host task, the context that the queue is associated with, the device that the queue is associated with and the accessors that have been captured in the callable, via an optional interop_handle parameter.

This allows host task to be used for two purposes: either as a task which can perform arbitrary C++ code within the scheduling of the SYCL runtime or as a task which can perform interoperability at a point within the scheduling of the SYCL runtime.

For the former use case host accessors should be used to request that a buffer or image be made available on the host so that it can be accessed directly via the accessor.

For the later use case device accessors should be used to request that a buffer or image be made available on the device associated with the queue used to submit the host task so that it can be accessed via interoperability member functions provided by the interop_handle class.

Local accessors cannot be used within a host task.
### 4.11. HOST TASKS

The `interop_handle` class is an abstraction over the `queue` which is being used to invoke the `host task` and its associated `device` and `context`. It also represents the state of the SYCL runtime dependency model at the point the `host task` is invoked.

The `interop_handle` class provides access to the `native backend object` associated with the `queue`, `device`, `context`
and any buffers or images that are captured in the callable being invoked in order to allow a host task to be used for interoperability purposes.

An interop_handle cannot be constructed by user-code, only by the SYCL runtime.

```cpp
class interop_handle;
```

### 4.11.2.1 Constructors

```cpp
private:
    interop_handle(__unspecified__); // (1)

public:
    interop_handle() = delete; // (2)
```

1. Private implementation defined constructor with unspecified arguments so that the SYCL runtime can construct a interop_handle.

2. Explicitly deleted default constructor.

### 4.11.2.2 Template member functions get_native_*

```cpp
template <backend Backend, typename dataT, int dims, access::mode accMode, access::target accTarget, access::placeholder isPlaceholder>
backend_traits<Backend>::native_type<buffer>
get_native_mem(const accessor<dataT, dims, accMode, accTarget, // (1)
    isPlaceholder> &bufferAccessor) const;
```

```cpp
template <backend Backend, typename dataT, int dims, access::mode accMode, access::target accTarget, access::placeholder isPlaceholder>
backend_traits<Backend>::native_type<image>
get_native_mem(const accessor<dataT, dims, accMode, accTarget, // (2)
    isPlaceholder> &imageAccessor) const;
```

```cpp
template <backend Backend>
backend_traits<Backend>::native_type<queue> get_native_queue() const noexcept; // (3)
```

```cpp
template <backend Backend>
backend_traits<Backend>::native_type<device> get_native_device() const noexcept; // (4)
```

```cpp
template <backend Backend>
backend_traits<Backend>::native_type<context> get_native_context() const noexcept; // (5)
```

1. **Constraints:** Available only if the optional interoperability function `get_native` taking a buffer is available and if `accTarget` is `access::target::global_buffer` or `access::target::constant_buffer`.

Returns: The SYCL application interoperability native backend object associated with the accessor bufferAccessor. The native backend object returned must be in a state where it represents the memory in its current state within the SYCL runtime dependency model be capable of being used in a way appro-
4.11. HOST TASKS

priate for the associated SYCL backend. It is undefined behaviour to use the native backend object outside of the scope of the host task.

Throws: errc::invalid_object_error if the accessor bufferAccessor was not registered with the command group which contained the host task.

2. Constraints: Available only if the optional interoperability function get_native taking an unsampled_image or sampled_image is available and if accTarget is access::target::unsampled_image or access::target::sampled_image.

Returns: The SYCL application interoperability native backend object associated with the accessor imageAccessor. The native backend object returned must be in a state where it represents the memory in its current state within the SYCL runtime dependency model and is capable of being used in a way appropriate for the associated SYCL backend. It is undefined behaviour to use the native backend object outside of the scope of the host task.

Throws: errc::invalid_object_error if the accessor imageAccessor was not registered with the command group which contained the host task.

3. Constraints: Available only if the optional interoperability function get_native taking a queue is available.

Returns: The SYCL application interoperability native backend object associated with the queue that the host task was submitted to. If the command group was submitted with a secondary queue and the fall-back was triggered, the queue that is associated with the interop_handle must be the fall-back queue. The native backend object returned must be in a state where it is capable of being used in a way appropriate for the associated SYCL backend. It is undefined behaviour to use the native backend object outside of the scope of the host task.

4. Constraints: Available only if the optional interoperability function get_native taking a device is available.

Returns: The SYCL application interoperability native backend object associated with the device that is associated with the queue that the host task was submitted to. The native backend object returned must be in a state where it is capable of being used in a way appropriate for the associated SYCL backend. It is undefined behaviour to use the native backend object outside of the scope of the host task.

5. Constraints: Available only if the optional interoperability function get_native taking a context is available.

Returns: The SYCL application interoperability native backend object associated with the context that is associated with the queue that the host task was submitted to. The native backend object returned must be in a state where it is capable of being used in a way appropriate for the associated SYCL backend. It is undefined behaviour to use the native backend object outside of the scope of the host task.

4.11.3 Additions to the handler class

This section describes member functions in the command group handler class that are used with host tasks.

```cpp
1  class handler {
2    ...
3  
4  public:
```
template <typename T>
void host_task(T &hostTaskCallable); // (1)

Effects: Enqueues an implementation defined command to the SYCL runtime to invoke hostTaskCallable exactly once. The scheduling of the invocation of hostTaskCallable in relation to other commands enqueued to the SYCL runtime must be in accordance with the dependency model described in Section 3.7.1. Initialises an interop_handle object and passes it to hostTaskCallable when it is invoked if std::is_invocable_v<T, interop_handle> evaluates to true, otherwise invokes hostTaskCallable as a nullary function.

4.12 Kernel class

The kernel class is an abstraction of a kernel object in SYCL. In the most common case the kernel object will contain the compiled version of a kernel invoked inside a command group using one of the parallel interface functions as described in 4.10.7. The SYCL runtime will create a kernel object, when it needs to enqueue the kernel on a command queue.

In the case where a developer would like to pre-compile a kernel or compile and link it with an existing program, then the kernel object will be created and contain that kernel using the module class, as defined in 4.13.6. In both of the above cases, the developer cannot instantiate a kernel object but can instantiate a named function object type that they could use, or create a function object from a kernel member function using C++ features. The kernel class object needs a parallel_for(...) invocation or an explicitly built SYCL kernel instance, for this compilation of the kernel to be triggered.

The SYCL kernel class provides the common reference semantics (see Section 4.5.3).

The member functions of the SYCL kernel class are listed in Table 4.97. The additional common special member functions and common member functions are listed in Tables 4.1 and 4.2, respectively.
Member functions | Description
---|---
backend get_backend() const | Returns the backend identifying the SYCL backend associated with this kernel.
bool is_host() const | Returns true if this SYCL kernel is a host kernel.
context get_context() const | Return the context that this kernel is defined for. The value returned must be equal to that returned by get_info<info::kernel::context>().
module<module_state::executable> get_module() const | Returns the module that this kernel is part of. The value returned must be equal to that returned by get_info<info::kernel::module>().
template <info::kernel param> typename info::param_traits<info::kernel, param>::return_type get_info() const | Query information from the kernel object using the info::kernel descriptor.
template <info::kernel_device_specific param> typename info::param_traits<info::kernel_device_specific, param>::return_type get_info(const device &dev) const | Query information from a kernel using the info::kernel_device_specific descriptor for a specific device.

Table 4.97: Member functions of the kernel class.
4.12.1 Kernel information descriptors

A kernel can be queried for information using the get_info member function of the kernel class, specifying one of the info parameters enumerated in info::kernel. Every kernel (including a host kernel) must produce a valid value for each info parameter. The possible values for each info parameter and any restriction are defined in the specification of the SYCL backend associated with the kernel. All info parameters in info::kernel are specified in Table 4.98 and the synopsis for info::kernel is described in appendix A.5.

<table>
<thead>
<tr>
<th>Kernel Descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>info::kernel::function_name</td>
<td>std::string</td>
<td>Return the kernel function name.</td>
</tr>
<tr>
<td>info::kernel::num_args</td>
<td>uint32_t</td>
<td>Return the number of arguments of the extracted kernel.</td>
</tr>
<tr>
<td>info::kernel::context</td>
<td>context</td>
<td>Return the SYCL context associated with this SYCL kernel.</td>
</tr>
<tr>
<td>info::kernel::module</td>
<td>module&lt; module_state::executable&gt;</td>
<td>Return the SYCL module associated with this SYCL kernel.</td>
</tr>
<tr>
<td>info::kernel::attributes</td>
<td>std::string</td>
<td>Return any attributes specified on a kernel function (as defined in Section 5.7).</td>
</tr>
</tbody>
</table>

Table 4.98: Kernel class information descriptors.
A kernel can also be queried for device-specific information using the `get_info` member function of the `kernel` class, specifying one of the info parameters enumerated in `info::kernel_device_specific`. Every kernel (including a host kernel) must produce a valid value for each info parameter. The possible values for each info parameter and any restriction are defined in the specification of the SYCL backend associated with the kernel. All info parameters in `info::kernel_device_specific` are specified in Table 4.99. The synopsis for `info::kernel_device_specific` is described in appendix A.5.

<table>
<thead>
<tr>
<th>Device-specific Kernel Information Descriptors</th>
<th>Return type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>info::kernel_device_specific::global_work_size</code></td>
<td><code>range&lt;3&gt;</code></td>
<td>Returns the maximum global work size. Only valid if device is of device_type custom or the kernel is a built-in kernel.</td>
</tr>
<tr>
<td><code>info::kernel_device_specific::work_group_size</code></td>
<td><code>size_t</code></td>
<td>Returns the maximum work-group size that can be used to execute a kernel on a specific device.</td>
</tr>
<tr>
<td><code>info::kernel_device_specific::compile_work_group_size</code></td>
<td><code>range&lt;3&gt;</code></td>
<td>Returns the work-group size specified by the device compiler if applicable, otherwise returns (0, 0, 0).</td>
</tr>
<tr>
<td><code>info::kernel_device_specific::preferred_work_group_size_multiple</code></td>
<td><code>size_t</code></td>
<td>Returns a value, of which work-group size is preferred to be a multiple, for executing a kernel on a particular device. This is a performance hint. The value must be less than or equal to that returned by <code>info::kernel_device_specific::work_group_size</code>.</td>
</tr>
<tr>
<td><code>info::kernel_device_specific::private_mem_size</code></td>
<td><code>size_t</code></td>
<td>Returns the minimum amount of private memory, in bytes, used by each work-item in the kernel. This value may include any private memory needed by an implementation to execute the kernel, including that used by the language built-ins and variables declared inside the kernel in the private address space.</td>
</tr>
<tr>
<td><code>info::kernel_device_specific::max_num_sub_groups</code></td>
<td><code>uint32_t</code></td>
<td>Returns the maximum number of sub-groups for this kernel.</td>
</tr>
<tr>
<td><code>info::kernel_device_specific::compile_num_sub_groups</code></td>
<td><code>uint32_t</code></td>
<td>Returns the number of sub-groups specified by the kernel, or 0 (if not specified).</td>
</tr>
<tr>
<td><code>info::kernel_device_specific::max_sub_group_size</code></td>
<td><code>uint32_t</code></td>
<td>Returns the maximum sub-group size for this kernel.</td>
</tr>
<tr>
<td><code>info::kernel_device_specific::compile_sub_group_size</code></td>
<td><code>uint32_t</code></td>
<td>Returns the required sub-group size specified by the kernel, or 0 (if not specified).</td>
</tr>
</tbody>
</table>

End of table

Table 4.99: Device-specific kernel information descriptors.

Alternatively, a kernel can be queried for work-group information using the `get_work_group_info` member functions of the `kernel` class, specifying one of the info parameters enumerated in `info::kernel_work_group`. This query has been deprecated in SYCL 2020, and will likely be removed in a future version of SYCL.
### 4.13 Modules

#### 4.13.1 Overview

A **module** is a high-level abstraction which represents a set of **SYCL kernel functions** which are associated with a **context** and can be executed on a number of **devices**, where each **device** is associated with that same **context**.

The **SYCL kernel functions** represented by a **module** can exist within one or more **device images** of implementation defined file formats. Each **device image** within a **module** must contain the necessary symbols and meta-data for each **SYCL kernel function** that the containing **module** represents.

[Note: For example, a **module** associated with a **context** of an OpenCL **SYCL backend**, that represents the **SYCL kernel function** `foo` could contain two modules; one of SPIR-V and one of a vendor specific ISA, both containing `foo` in the relevant file format. — end note]

**Modules** are used to express the explicit compilation of **SYCL kernel functions** in order to then be invoked via a **kernel invocation command** such as `parallel_for`. Normally this compilation is done implicitly by the **kernel invocation command**, however it can be useful to perform the compilation manually in order to add custom properties to the compilation or to link **SYCL kernel functions** with other libraries.

A **module** can be obtained either by requesting the **module** associated with the current translation unit or via some **SYCL backend**-specific operation.
Once a module has been obtained there are a number of free functions for performing compilation, linking and joining.

A module can then be bound to a command group so that the SYCL kernel functions represented by the module are used in any kernel invocation commands. See Section 4.10.9 for more details.

### 4.13.2 Specialization constants

A SYCL kernel function can contain specialization constants which represent a constant variable where the value is not known until compilation of the SYCL kernel function. Any specialization constants of a given SYCL kernel function are exposed via an input module representing that SYCL kernel function, where the value of the specialization constants can be set. However, once a module has been compiled, resulting in an object module or executable module the value of specialization constants can no longer be changed.

Any specialization constants in a SYCL kernel function which are not set before that function is invoked will take a default value. This includes invoking a kernel invocation command such as `parallel_for` or retrieving an object module or executable module directly triggering implicit compilation.

As a module may contain more than one device image, some of these may natively support specialization constants and some may not, however all device images must set the value of specialization constants.

[Note: It is expected that a specialization constant is implemented either via an implementation-defined mechanism available to the file format of that device image such as SPIR-V specialization constants or by passing the value as an additional kernel argument to the SYCL kernel function via the SYCL runtime when it’s invoked. — end note]

A specialization id is an identifier which represents a reference to a specialization constant both in the SYCL application for setting the value prior to the compilation of an input module and in a SYCL kernel function for retrieving the value during invocation.

A module that is associated with a host context may not contain native-specialization constants, though it must still emulate all specialization constants the SYCL kernel functions contains.

### 4.13.3 Synopsis

```cpp
namespace sycl {
    template <typename T>
    class specialization_id {
        private:
            specialization_id(const specialization_id& rhs) = delete;
            specialization_id(specialization_id&& rhs) = delete;
            specialization_id &operator=(const specialization_id& rhs) = delete;
            specialization_id &operator=(specialization_id&& rhs) = delete;
        public:
            using value_type = T;
```
template<class... Args>
explicit constexpr specialization_id(Args&&... args);

enum class module_state {
  input,
  object,
  executable
};

template<module_state State>
class module {
private:
  module(__unspecified__);
public:
  using device_image_type = __unspecified__;
  using device_image_iterator = __unspecified__;
  module() = delete;

  context get_context() const noexcept;
  std::vector<device> get_devices() const noexcept;
  bool has_kernel(std::string kernelName) const noexcept;
  kernel get_kernel(std::string kernelName) const;
  std::vector<std::string> get_kernel_names() const;
  bool is_empty() const noexcept;
  device_image_iterator begin() const;
  device_image_iterator end() const;
  bool contains_specialization_constants() const noexcept;
  bool native_specialization_constant() const noexcept;

  template<auto& S>
  bool has_specialization_constant() const noexcept;

  template<auto& S>
  typename std::remove_reference_t<decltype(S)>::type value;

  template<auto& S>
  typename std::remove_reference_t<decltype(S)>::type get_specialization_constant() const;
};
4.13. Enum class `module_state`

A module can be in one of three different module states: input, object and executable. The module states reflect the current state of the device images and subsequently the SYCL kernel functions. The module states also alters the capabilities of the module.

The three module states are represented by an enum class called `module_state`.

```cpp
enum class module_state {
    input,
    object,
    executable
};
```

The values of each enumeration of `module_state` are implementation defined.
### 4.13.5 Class template `specialization_id`

A specialization id is represented by the class template `specialization_id` with a single template parameter `T` which specifies the unique name used to identify the associated specialization constant.

The template parameter `T` must be a forward-declarable type and `specialization_id` objects must be declared with automatic or static storage duration within a namespace or class scope.

```cpp
template <typename T>
class specialization_id;
```

#### 4.13.5.1 Constructors

```cpp
template<class... Args>
explicit constexpr specialization_id(Args&&... args);
```

1. **Constraints:** Available only when `std::is_constructible_v<T, Args...>` evaluates to `true`.
   
   *Effects:* Constructs a `specialization_id` containing an instance of `T` initialized with `args...`, which represents the default value of the specialization constant.

#### 4.13.5.2 Special member functions

```cpp
specialization_id(const specialization_id& rhs) = delete; // (1)
specialization_id(specialization_id&& rhs) = delete; // (2)
operator=(const specialization_id& rhs) = delete; // (3)
specialization_id &operator=(specialization_id&& rhs) = delete; // (4)
```

1. Deleted copy constructor.
2. Deleted move constructor.
3. Deleted copy assignment operator.
4. Deleted move assignment operator.

### 4.13.6 Class template module

A module is represented by the class template `module` with the single template parameter `State` of type `module_state` which specifies its module state.

There are three different module types, to reflect the three module states:

- An **input module** is a module of the input module state and represents SYCL kernel functions which are yet to be compiled such as a source or intermediate representation.

- A **object module** is a module of the object module state and represents SYCL kernel functions which have been compiled but are yet to be linked such as an intermediate object of the ISAs of the associated devices.
• A **executable module** is a **module** of the **executable module state** and represents SYCL kernel functions which have been compiled and linked such as an executable of the ISAs of the associated devices.

A **module** cannot be constructed by user-code, only by the SYCL runtime.

A **module** is permitted to be empty in which case it contains no **device images** and represents no SYCL kernel functions.

A **module** is considered to have reference semantics as specified in Section 4.5.3, therefore any **module** constructed as a copy of another and the **module** that was copied from are considered to be equal. Furthermore, two **modules** of the same **module state** are considered to be equal if they are associated with the same **context** and **devices** and contain the same **device images** and subsequently the same SYCL kernel function.

A **module** must contain a copy of the **context** and **devices** that are associated with it for the duration of its lifetime. This means that the destructor of the associated **context** or **devices** will not be invoked if the **module** is still alive in accordance with Section 4.5.3.

```cpp
1 template<module_state State>
2 class module;
```

### 4.13.6.1 Constructors

1. **Private implementation defined constructor with unspecified arguments so that the SYCL runtime can construct a module.**

2. Explicitly deleted default constructor.

### 4.13.6.2 Member functions

1. **Returns:** A **context** object representing the associated **context**.

2. **Returns:** A **std::vector** of **device** objects representing the associated **devices**.

3. **Constraints:** Available only when `State == module_state::executable`.
Returns: true if the module represents a SYCL kernel function with the value of string kernel name kernelName, otherwise returns false. Only available when the module is a executable module.

```cpp
kernel get_kernel(std::string kernelName) const; // (1)
```

1. Constraints: Available only when State == module_state::executable.

Returns: a kernel object representing the SYCL kernel function with the string kernel name with the value of kernelName if this->has_kernel(kernelName) evaluates to true, otherwise throws exception with the errc::invalid_object error code.

```cpp
std::vector<std::string> get_kernel_names() const; // (1)
```

1. Returns: A std::vector of std::string objects representing each of the SYCL kernel functions represented by the module.

```cpp
bool is_empty() const noexcept; // (1)
```

1. Returns: true if the module contains no device images, otherwise returns false.

```cpp
device_image_iterator begin() const; // (1)
2 3
device_image_iterator end() const; // (2)
```

1. Returns: An iterator of type device_image_iterator pointing to the beginning of a sequence of device images of type std::iterator_traits<device_image_iterator>::value_type.

2. Returns: An iterator of type device_image_iterator pointing to the end of a sequence of device images of type std::iterator_traits<device_image_iterator>::value_type.

```cpp
bool contains_specialization_constants() const noexcept; // (1)
```

1. Returns: true if any SYCL kernel function represented by the module contains a specialization constant, otherwise returns false.

```cpp
bool native_specialization_constant() const noexcept; // (1)
```

1. Returns: true if all of the specialization constants contained in the module support are native-specialization constants for all device images.

```cpp
template<auto& S>
bool has_specialization_constant() const noexcept; // (1)
```

1. Returns: true if any of the SYCL kernel functions represented by the module contains the specialization constant represented by the specialization_id at the address S, otherwise returns false.
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1 template<auto& S>
2 void set_specialization_constant(
3 typename std::remove_reference_t<decltype(S)>::type value); // (1)

1. **Constraints:** Available only when State == module_state::input.

   **Effects:** Sets the value of the specialization constant represented by the specialization_id at the address S in all SYCL kernel functions which contain that specialization constant and for all device images. If the specialization constant was already set, then the previous value is overwritten. If two or more SYCL kernel functions contain the same specialization constant they are assumed to be the same and will have the same value.

   **Throws:** errc::invalid_object_error if this->has_specialization_constant<S>() evaluates to false.

1 template<auto& S>
2 typename std::remove_reference_t<decltype(S)>::type get_specialization_constant() const; // (1)

1. **Returns:** the value of the specialization constant associated with the specialization_id at the address S if the specialization constant has been set, otherwise returns the default value.

   **Throws:** errc::invalid_object_error if this->has_specialization_constant<S>() evaluates to false.

### 4.13.7 Free functions

Modules can be compiled, linked, built or joined together using free functions which operate on modules of different module states.

When a module is being created as a results of one of these operations it is permitted to perform ad-hoc implementation-defined operations such as just-in-time compilation or translations to alter the file format of the device image in order to create a module of a resulting module state, however this is not required.

[Note: For example, if a SYCL kernel function was compiled by a device compiler to generate the file format SPIR-V, a module associated with a context of an OpenCL SYCL backend could be created as an input module using the SPIR-V file format directly or it could be created as an executable module implicitly triggering online compilation via the OpenCL runtime. — end note]

```cpp
namespace sycl {

module<module_state::executable>
build(const module<module_state::input> &inputModule,
     const property_list &propList = {}); // (1)

module<module_state::object>
compile(const module<module_state::input> &inputModule,
        const property_list &propList = {}); // (2)

module<module_state::executable>
link(const module<module_state::object> &objModule,
      const property_list &propList = {}); // (3)

module<module_state::executable>
link(const std::vector<module<module_state::object>> &objModules,
      const property_list &propList = {}); // (4)

```
const property_list &propList = {}); // (4)

template<module_state T>
module<T> join(const std::vector<module<T>>& modules); // (5)
} // namespace sycl

1. **Effects:** Performs implementation defined build operation(s), including compilation and linking, on the input module `inputModule`, applying any properties provided via `propList`.

   **Returns:** A `module<module_state::executable>` object containing the result of the build operation(s) performed. The module returned must represent the same SYCL kernel functions and be associated with the same context and devices as that of `inputModule`, however the device images may differ.

   **Throws:** `errc::build_error` if none of the devices associated with the module have `aspect::online_compiler`.

   **Throws:** `errc::build_error` if the build operation(s) fail.

2. **Effects:** Performs implementation defined compilation operation(s) on the input module `inputModule`, applying any properties provided via `propList`.

   **Returns:** A `module<module_state::object>` object containing the result of the compilation operation(s) performed. The module returned must represent the same SYCL kernel functions and be associated with the same context and devices as that of `inputModule`, however the device images may differ.

   **Throws:** `errc::compile_error` if none of the devices associated with the module have `aspect::online_compiler`.

   **Throws:** `errc::compile_error` if the compilation operation(s) fail.

3. **Effects:** Performs implementation defined linking operation(s) on the input module `objModule`, applying any properties provided via `propList`. If two or more modules contain the same SYCL kernel functions it is assumed that they are the same so one of them is selected and the rest are discarded. The one which is selected is implementation defined.

   **Returns:** A `module<module_state::executable>` object containing the result of the linking operation(s) performed. The module returned must represent the same SYCL kernel functions and be associated with the same context and devices as that of `objModule`, however the device images may differ.

   **Throws:** `errc::link_error` if none of the devices associated with the module have `aspect::online_linker`.

   **Throws:** `errc::link_error` if the compilation operation(s) fail.

4. **Preconditions:** Each module in `moduleObjects` must be associated with the same context and devices.

   **Effects:** Performs implementation defined linking operation(s) on the input modules in `moduleObjects`, applying any properties provided via `propList`. If two or more modules contain the same SYCL kernel functions it is assumed that they are the same so one of them is selected and the rest are discarded. The one which is selected is implementation defined.

   **Returns:** A `module<module_state::executable>` object containing the result of the linking operation(s)
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performed. The module returned must represent the same SYCL kernel functions and be associated with the same context and devices as that of objModules, however the device images may differ.

Throws: errc::link_error if none of the devices associated with the module have aspect::online_linker.

Throws: errc::link_error if the compilation operation(s) fail.

Throws: errc::invalid_object_error if objModules.empty() evaluates to true or any module in objModules is associated with a different context or devices than another module in objModules.

5. Preconditions: Each module in modules must be associated with the same context and devices.

Effects: Performs implementation defined joining operation(s) on the modules in modules, applying any properties provided via propList. If two or more modules contain the same SYCL kernel functions it is assumed that they are the same so one of them is selected and the rest are discarded. The one which is selected is implementation defined.

Returns: A module object of module_state T, containing the result of the linking operation(s) performed. The module returned must represent all of the combined SYCL kernel functions associated with each module in modules and must be associated with the same context and devices as that of modules, however the device images may differ.

Throws: errc::link_error if the joining operation(s) fail.

Throws: errc::invalid_object_error if modules.empty() evaluates to true or any module in modules is associated with a different context or devices than another module in modules.

4.13.8 Namespace this_module

The namespace this_module provides additional free functions which can be used for querying and retrieving modules available to the current translation unit.

4.13.8.1 Type traits

1. Template variable that takes a type T specifying the type kernel name of a SYCL kernel function in the current translation unit and whose value is the corresponding string kernel name.

4.13.8.2 Free functions

1. bool has_any_module(context ctxt); // (1)

3. template<module_state S> // (2)
4. bool has_module_in(context ctxt);

6. template<module_state S> // (3)
7. module<T> get(context ctxt);
1. Returns: true if there is a module of any module state, available within the current translation unit that is compatible with the context ctxt, otherwise returns false. Must not perform any compilation, linking, building or joining operation(s).

2. Returns: true if there is a module of the module state specified by the module_state value S, available or retrievable within the current translation unit that is compatible with the context ctxt, otherwise returns false. Must not perform any compilation, linking, building or joining operation(s).

3. Effects: May perform implementation defined compilation, linking or building operations in order to transform a module that is available in another module state into the module state specified by the module_state value S

Returns: A module of module_state S representing a module associated with the current translation unit and the context ctxt if this_module::has_module_in<S>() evaluates to true, otherwise returns a module that is empty. If the returned module is not empty, it must represent the set of SYCL kernel functions available to the current translation unit and may also contain a further implementation defined set of SYCL kernel functions.

[Note: A module returned from module<State>::get may contain additional SYCL kernel functions to those available in the current translation unit in order to facilitate different single-source compilation methods. This does not impact user code as link and join are required to assume any duplicate SYCL kernel functions are the same. — end note]

4.14 Defining kernels

In SYCL, functions that are executed on a SYCL device are referred to as SYCL kernel functions. A kernel containing such a SYCL kernel function is enqueued on a device queue in order to be executed on that particular device.

The return type of the SYCL kernel function is void, and all memory accesses between host and device are through the accessor class (Section 4.7.6) or through USM pointers (Section 4.8).

There are three ways of defining kernels: as named function objects, as lambda functions, or through backend-specific interoperability interfaces for modules and kernels, where available.

4.14.1 Defining kernels as named function objects

A kernel can be defined as a named function object type. These function objects provide the same functionality as any C++ function object, with the restriction that they need to follow C++ rules to be trivially copyable. The kernel function can be templated via templating the kernel function object type. For details on restrictions for kernel naming, please refer to Section 5.2.

The operator() member function must be const-qualified, and it may take different parameters depending on the data accesses defined for the specific kernel. If the operator() function writes to any of the member variables, the behavior is undefined.

The following example defines a SYCL kernel function, RandomFiller, which initializes a buffer with a random number. The random number is generated during the construction of the function object while processing the command group. The operator() member function of the function object receives an item object. This member function will be called for each work item of the execution range. The value of the random number will be assigned to each element of the buffer. In this case, the accessor and the scalar random number are members of
the function object and therefore will be parameters to the device kernel. Usual restrictions of passing parameters to kernels apply.

```cpp
class RandomFiller {
    public:
    RandomFiller(accessor<int> ptr)
        : ptr_{ ptr } {
        std::random_device hwRand;
        std::uniform_int_distribution<> r { 1, 100 };  
        randomNum_ = r(hwRand);
    }
    void operator()(item<1> item) const { ptr_[item.get_id()] = get_random(); }
    int get_random() { return randomNum_; }

    private:
    accessor<int> ptr_;  
    int randomNum_; 
};
```

### 4.14.2 Defining kernels as lambda functions

In C++, function objects can be defined using lambda functions. Kernels may be defined as lambda functions in SYCL. The name of a lambda function in SYCL may optionally be specified by passing it as a template parameter to the invoking member function, and in that case, the lambda name is a C++ \textit{typename}. If the lambda function relies on template arguments, then if specified, the name of the lambda function must contain those template arguments. The class used for the name of a lambda function is only used for naming purposes and is not required to be defined. For details on restrictions for kernel naming, please refer to 5.2.

The kernel function for the lambda function is the lambda function itself. The kernel lambda must use copy for all of its captures (i.e. \[=\]), and the lambda must not use the \texttt{mutable} specifier.

```cpp
class MyKernel;
myQueue.submit([&](handler& cmdGroup) {
    cmdGroup.single_task<class MyKernel>([=]() {
        // [kernel code]
    });
});
```
4.14.3 Defining kernels using modules

In case the developer needs to specify compiler flags or special linkage options for a kernel, then a kernel object can be used, as described in 4.13.6.2. The SYCL kernel function is defined as a named function object 4.14.1 or lambda function 4.14.2. The user can obtain a module object for the kernel with the get_kernel member function. This member function is templated by the kernel name, so that the user can specify the kernel whose associated kernel they wish to obtain.

The following example illustrates how an application can pre-compile a kernel. The code defines the kernel as a lambda function, and pre-compiles it by creating a module object for all the kernels defined in the current translation unit. This ensures that the kernel is compiled a priori of its invocation via parallel_for. For more details on the module object and its related APIs, see Section 4.13.

```cpp
sycl::queue myQueue;
auto myContext = myQueue.get_context();

// Calling the get() function will pre-compile all kernels in this translation
// unit for the device in "myContext".
auto myModule = sycl::this_module::get<sycl::module_state::executable>(myContext);

auto myRange = sycl::nd_range<2>(range<2>(1024, 1024),range<2>(64, 64));

myQueue.submit([&](sycl::handler& cgh) {
  // Calling use_module() causes the parallel_for() below to use the pre-compiled
  // kernel from "myModule".
  cgh.use_module(myModule);
  cgh.parallel_for(myRange, [=](sycl::nd_item<2> index) {
    // kernel code
  });
});
```

In the following example, the SYCL kernel function performs a convolution and uses specialization constants to set the values of the coefficients.

```cpp
#include <CL/sycl.hpp>
using namespace sycl;
using coeff_t = std::array<std::array<float, 3>, 3>;

// Read coefficients from somewhere.
coeff_t get_coefficients();

// Identify the specialization constant.
specialization_id<coeff_t> coeff_id;

void do_conv(buffer<float, 2> in, buffer<float, 2> out) {
  queue myQueue;
  myQueue.submit([&](handler &cgh) {
    accessor in_acc { in, cgh, read_only }; 
    accessor out_acc { out, cgh, write_only }; 
  });
}```
4.14. DEFINING KERNELS

```cpp
// Set the coefficient of the convolution as constant.
// This will build a specific kernel the coefficient available as literals.
cgh.set_specialization_constant<coeff_id>(get_coefficients());

cgh.parallel_for<class Convolution>(
in.get_range(), [=](item<2> item_id, kernel_handler h) {
    float acc = 0;
    coeff_t coeff = h.get_specialization_constant<coeff_id>();
    for (int i = -1; i <= 1; i++) {
        if (item_id[0] + i < 0 || item_id[0] + i >= in_acc.get_range()[0]) continue;
        for (int j = -1; j <= 1; j++) {
            if (item_id[1] + j < 0 || item_id[1] + j >= in_acc.get_range()[1]) continue;
            // the underlying JIT can see all the values of the array returned by coeff.get().
            acc += coeff[i + 1][j + 1] *
            in_acc[item_id[0] + i][item_id[1] + j];
        }
    }
    out_acc[item_id] = acc;
});
myQueue.wait();
```

4.14.4 Rules for parameter passing to kernels

In a case where a kernel is a named function object or a lambda function, any member variables encapsulated within the function object or variables captured by the lambda function must be treated according to the following rules:

- Any accessor must be passed as an argument to the device kernel in a form that allows the device kernel to access the data in the specified way.
- The SYCL runtime and compiler(s) must produce the necessary conversions to enable accessor arguments from the host to be converted to the correct type of parameter on the device.
- The device compiler(s) must validate that the layout of any data shared between the host and the device(s) (e.g. value kernel arguments or data accessed through an accessor or USM) is compatible with the layout of that data on the host. If there is a layout mismatch that the implementation cannot or will not correct for (to make the layouts compatible), then the device compiler must issue an error and compilation must fail.
- A local accessor provides access to work-group-local memory. The accessor is not constructed with any buffer, but instead constructed with a size and base data type. The runtime must ensure that the work-group-local memory is allocated per work-group and available to be used by the kernel via the local accessor.
- C++ trivially copyable types must be passed by value to the kernel.
- C++ non-trivially copyable types must not be passed as arguments to a kernel that is compiled for a device.
- It is illegal to pass a buffer or image (instead of an accessor class) as an argument to a kernel. Generation of a compiler error in this illegal case is optional.
• Sampler objects (sampler) can be passed as parameters to kernels.

• It is illegal to pass a pointer or reference argument to a kernel. Generation of a compiler error in this illegal case is optional.

• Any aggregate types such as structs or classes should follow the rules above recursively. It is not necessary to separate struct or class members into separate kernel parameters if all members of the aggregate type are unaffected by the rules above.

4.15 Error handling

4.15.1 Error handling rules

Error handling in a SYCL application (host code) uses C++ exceptions. If an error occurs, it will be thrown by the API function call and may be caught by the user through standard C++ exception handling mechanisms.

SYCL applications are asynchronous in the sense that host and device code executions are decoupled from one another except at specific points. For example, device code executions often begin when dependencies in the SYCL task graph are satisfied, which occurs asynchronously from host code execution. As a result of this the errors that occur on a device cannot be thrown directly from a host API call, because the call enqueueing a device action has typically already returned by the time that the error occurs. Such errors are not detected until the error-causing task executes or tries to execute, and we refer to these as asynchronous errors.

4.15.1.1 Asynchronous error handler

The queue and context classes can optionally take an asynchronous handler object async_handler on construction, which is a callable such as a function class or lambda, with an exception_list as a parameter. Invocation of an async_handler may be triggered by the queue member functions queue::wait_and_throw() or queue::throw_asynchronous(), by the event member function event::wait_and_throw(), or automatically on destruction of a queue or context that contains unconsumed asynchronous errors. When invoked, an async_handler is called and receives an exception_list argument containing a list of exception objects representing any unconsumed asynchronous errors associated with the queue or context.

When an asynchronous error instance has been passed to an async_handler, then that instance of the error has been consumed for handling and is not reported on any subsequent invocations of the async_handler.

The async_handler may be a named function object type, a lambda function or a std::function. The exception_list object passed to the async_handler is constructed by the SYCL runtime.

4.15.1.2 Behavior without an async_handler

If an asynchronous error occurs in a queue or context that has no user-supplied asynchronous error handler object async_handler, then an implementation defined default async_handler is called to handle the error in the same situations that a user-supplied async_handler would be, as defined in 4.15.1.1. The default async_handler must in some way report all errors passed to it, when possible, and must then invoke std::terminate or equivalent.

4.15.1.3 Priorities of async_handlers

If the SYCL runtime can associate an asynchronous error with a specific queue, then:

• If the queue was constructed with an async_handler, that handler is invoked to handle the error.
• Otherwise if the context enclosed by the queue was constructed with an `async_handler`, that handler is invoked to handle the error.

• Otherwise when no handler was passed to either queue or context on construction, then a default handler is invoked to handle the error, as described by 4.15.1.2.

• All handler invocations in this list occur at times as defined by 4.15.1.1.

If the SYCL runtime cannot associate an asynchronous error with a specific queue, then:

• If the context in which the error occurred was constructed with an `async_handler`, then that handler is invoked to handle the error.

• Otherwise when no handler was passed to the associated context on construction, then a default handler is invoked to handle the error, as described by 4.15.1.2.

• All handler invocations in this paragraph occur at times as defined by 4.15.1.1.

### 4.15.1.4 Asynchronous errors with a secondary queue

If an asynchronous error occurs when running or enqueuing a command group which has a secondary queue specified, then the command group may be enqueued to the secondary queue instead of the primary queue. The error handling in this case is also configured using the `async_handler` provided for both queues. If there is no `async_handler` given on any of the queues, then no asynchronous error reporting is done and no exceptions are thrown. If the primary queue fails and there is an `async_handler` given at this queue’s construction, which populates the `exception_list` parameter, then any errors will be added and can be thrown whenever the user chooses to handle those exceptions. Since there were errors on the primary queue and a secondary queue was given, then the execution of the kernel is re-scheduled to the secondary queue and any error reporting for the kernel execution on that queue is done through that queue, in the same way as described above. The secondary queue may fail as well, and the errors will be thrown if there is an `async_handler` and either `wait_and_throw()` or `throw()` are called on that queue. The command group function object event returned by that function will be relevant to the queue where the kernel has been enqueued.

Below is an example of catching a SYCL exception and printing out the error message.

```cpp
void catch_any_errors(sycl::context const& ctx) {
    try {
        do_something_to_invoke_error(ctx);
    } catch(sycl::exception const& e) {
        std::cerr << e.what();
    }
}
```

Below is an example of catching a SYCL exception with the invalid_object error code and printing out the error message.

```cpp
void catch_invalid_object_errors(sycl::context const& ctx) {
    try {
        do_something_to_invoke_error(ctx);
    } catch(sycl::exception const& e) {
```

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6     if(e.code() == sycl::errc::invalid_object) {
7         std::cerr << "Invalid object error: " << e.what();
8     }
9     else {
10         throw;
11     }
12 }
13 }

Below is an example of catching a SYCL exception, checking for the SYCL backend by inspecting the category and handling the OpenCL SYCL backend error codes if the category is that of the OpenCL SYCL backend otherwise checking the standard error code.

1 void catch_backend_errors(sycl::context const& ctx) {
2     try {
3         do_something_to_invoke_error(ctx);
4     }
5     catch(sycl::exception const& e) {
6         if(e.category() == sycl::error_category_for<sycl::backend::opencl>()) {
7             switch(e.code().value()) {
8                 case CL_INVALID_PROGRAM:
9                     std::cerr << "OpenCL invalid program error: " << e.what();
10 /* ...*/
11                 }
12             else {
13                 throw;
14             }
15         }
16         else {
17             if(e.code() == sycl::errc::invalid_object) {
18                 std::cerr << "Invalid object error: " << e.what();
19             }
20             else {
21                 throw;
22             }
23         }
24     }
25 }

4.15.2 Exception class interface

1 namespace sycl {
2
3     using async_handler = std::function<void(sycl::exception_list)>;
4
5     class exception : public virtual std::exception {
6         public:
7             exception(std::error_code ec, const std::string& what_arg);
8             exception(std::error_code ec, const char * what_arg);
9             exception(const sycl::error_code& ec);
10             exception(int ev, const std::error_category& ecat, const std::string& what_arg);
11             exception(int ev, const std::error_category& ecat, const char* what_arg);

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exception(int ev, const std::error_category& ecat);

exception(context ctx, std::error_code ec, const std::string& what_arg);
exception(context ctx, std::error_code ec, const char* what_arg);
exception(context ctx, std::error_code ec);
exception(context ctx, int ev, const std::error_category& ecat, const std::string& what_arg);
exception(context ctx, int ev, const std::error_category& ecat, const char* what_arg);
exception(context ctx, int ev, const std::error_category& ecat);

const std::error_code& code() const noexcept;
const std::error_category& category() const noexcept;

bool has_context() const noexcept;
context get_context() const;
}

class exception_list {
// Used as a container for a list of asynchronous exceptions
public:
  using value_type = std::exception_ptr;
  using reference = value_type&;
  using const_reference = const value_type&;
  using size_type = std::size_t;
  using iterator = /*unspecified*/;
  using const_iterator = /*unspecified*/;
  
  size_type size() const;
  iterator begin() const; // first asynchronous exception
  iterator end() const; // refer to past-the-end last asynchronous exception
};

enum class errc {
  runtime_error = /* implementation-defined */,
  kernel = /* implementation-defined */,
  accessor = /* implementation-defined */,
  nd_range = /* implementation-defined */,
  event = /* implementation-defined */,
  invalid_parameter = /* implementation-defined */,
  compile_program = /* implementation-defined */,
  link_program = /* implementation-defined */,
  invalid_object = /* implementation-defined */,
  memory_allocation = /* implementation-defined */,
  platform = /* implementation-defined */,
  profiling = /* implementation-defined */,
  feature_not_supported = /* implementation-defined */
};

template<backend b>
using errc_for = typename backend_traits<b>::errc;

std::error_condition make_error_condition(errc e) noexcept;
std::error_code make_error_code(errc e) noexcept;
const std::error_category& sycl_category() noexcept;
The SYCL `exception_list` class is also available in order to provide a list of synchronous and asynchronous exceptions.

Errors can occur both in the SYCL library and SYCL host side, or may come directly from a SYCL backend. The member functions on these exceptions provide the corresponding information. SYCL backends can provide additional exception class objects as long as they derive from `sycl::exception` object, or any of its derived classes.

A specialization of `std::is_error_condition_enum` must be defined for `sycl::errc` inheriting from `std::true_type`.

A specialization of `std::is_error_code_enum` must be defined for `sycl::errc` and `backend_traits<Backend>::errc` inheriting from `std::true_type` for each `Backend`, where `backend` is each enumeration of the enum class `backend`.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>exception(std::error_code ec, const std::string&amp; what_arg)</code></td>
<td>Constructs an <code>exception</code>. The string returned by <code>what()</code> is guaranteed to contain <code>what_arg</code> as a substring.</td>
</tr>
<tr>
<td><code>exception(std::error_code ec, const char* what_arg)</code></td>
<td>Constructs an <code>exception</code>. The string returned by <code>what()</code> is guaranteed to contain <code>what_arg</code> as a substring.</td>
</tr>
<tr>
<td><code>exception(std::error_code ec)</code></td>
<td>Constructs an <code>exception</code>.</td>
</tr>
<tr>
<td><code>exception(int ev, const std::error_category&amp; ecat, const std::string&amp; what_arg)</code></td>
<td>Constructs an <code>exception</code> with the error code <code>ev</code> and the underlying error category <code>ecat</code>. The string returned by <code>what()</code> is guaranteed to contain <code>what_arg</code> as a substring.</td>
</tr>
<tr>
<td><code>exception(int ev, const std::error_category&amp; ecat, const char* what_arg)</code></td>
<td>Constructs an <code>exception</code> with the error code <code>ev</code> and the underlying error category <code>ecat</code>. The string returned by <code>what()</code> is guaranteed to contain <code>what_arg</code> as a substring.</td>
</tr>
<tr>
<td><code>exception(int ev, const std::error_category&amp; ecat)</code></td>
<td>Constructs an <code>exception</code> with the error code <code>ev</code> and the underlying error category <code>ecat</code>.</td>
</tr>
</tbody>
</table>

Table 4.101: Member functions of the SYCL `exception` class.
4.15. ERROR HANDLING

Table 4.101: Member functions of the SYCL `exception` class.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>exception(context ctx, std::error_code ec, const std::string&amp; what_arg)</code></td>
<td>Constructs an <code>exception</code> with an associated SYCL context <code>ctx</code>. The string returned by <code>what()</code> is guaranteed to contain <code>what_arg</code> as a substring.</td>
</tr>
<tr>
<td><code>exception(context ctx, std::error_code ec, const char* what_arg)</code></td>
<td>Constructs an <code>exception</code> with an associated SYCL context <code>ctx</code>. The string returned by <code>what()</code> is guaranteed to contain <code>what_arg</code> as a substring.</td>
</tr>
<tr>
<td><code>exception(context ctx, std::error_code ec)</code></td>
<td>Constructs an <code>exception</code> with an associated SYCL context <code>ctx</code>.</td>
</tr>
<tr>
<td><code>exception(context ctx, int ev, const std::error_category&amp; ecat, const std::string&amp; what_arg)</code></td>
<td>Constructs an <code>exception</code> with an associated SYCL context <code>ctx</code>, the error code <code>ev</code> and the underlying error category <code>ecat</code>. The string returned by <code>what()</code> is guaranteed to contain <code>what_arg</code> as a substring.</td>
</tr>
<tr>
<td><code>exception(context ctx, int ev, const std::error_category&amp; ecat, const char* what_arg)</code></td>
<td>Constructs an <code>exception</code> with an associated SYCL context <code>ctx</code>, the error code <code>ev</code> and the underlying error category <code>ecat</code>. The string returned by <code>what()</code> is guaranteed to contain <code>what_arg</code> as a substring.</td>
</tr>
<tr>
<td><code>exception(context ctx, int ev, const std::error_category&amp; ecat)</code></td>
<td>Constructs an <code>exception</code> with an associated SYCL context <code>ctx</code>, the error code <code>ev</code> and the underlying error category <code>ecat</code>.</td>
</tr>
<tr>
<td><code>const std::error_code&amp; code()</code> const noexcept</td>
<td>Returns the error code stored inside the exception.</td>
</tr>
<tr>
<td><code>const std::error_category&amp; category()</code> const noexcept</td>
<td>Returns the error category of the error code stored inside the exception.</td>
</tr>
<tr>
<td><code>const char * what()</code> const</td>
<td>Returns an implementation defined non-null constant C-style string that describes the error that triggered the exception.</td>
</tr>
<tr>
<td><code>bool has_context()</code> const</td>
<td>Returns <code>true</code> if this SYCL <code>exception</code> has an associated SYCL <code>context</code> and <code>false</code> if it does not.</td>
</tr>
<tr>
<td><code>context get_context()</code> const</td>
<td>Returns the SYCL <code>context</code> that is associated with this SYCL <code>exception</code> if one is available. Must throw an <code>exception</code> with the <code>errc::invalid_object_error</code> error code if this SYCL <code>exception</code> does not have a SYCL <code>context</code>.</td>
</tr>
</tbody>
</table>
### Member function

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>iterator end() const</td>
<td>Returns an iterator to the end of the list of asynchronous exceptions.</td>
</tr>
</tbody>
</table>

End of table

Table 4.102: Member functions of the `exception_list`.

### Standard SYCL Error Codes

<table>
<thead>
<tr>
<th>Standard SYCL Error Codes</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>runtime_error</td>
<td>Generic runtime error.</td>
</tr>
<tr>
<td>kernel_error</td>
<td>Error that occurred before or while enqueuing the SYCL kernel.</td>
</tr>
<tr>
<td>nd_range_error</td>
<td>Error regarding the SYCL <code>nd_range</code> specified for the SYCL kernel</td>
</tr>
<tr>
<td>accessor_error</td>
<td>Error regarding the SYCL <code>accessor</code> objects defined.</td>
</tr>
<tr>
<td>event_error</td>
<td>Error regarding associated SYCL <code>event</code> objects.</td>
</tr>
<tr>
<td>invalid_parameter_error</td>
<td>Error regarding parameters to the SYCL kernel, it may apply to any captured parameters to the kernel lambda.</td>
</tr>
<tr>
<td>compile_program_error</td>
<td>Error while compiling the SYCL kernel to a SYCL device.</td>
</tr>
<tr>
<td>link_program_error</td>
<td>Error while linking the SYCL kernel to a SYCL device.</td>
</tr>
<tr>
<td>invalid_object_error</td>
<td>Error regarding any memory objects being used inside the kernel</td>
</tr>
<tr>
<td>memory_allocation_error</td>
<td>Error on memory allocation on the SYCL device for a SYCL kernel.</td>
</tr>
<tr>
<td>platform_error</td>
<td>The SYCL platform will trigger this exception on error.</td>
</tr>
<tr>
<td>profiling_error</td>
<td>The SYCL runtime will trigger this error if there is an error when profiling info is enabled.</td>
</tr>
<tr>
<td>feature_not_supported</td>
<td>Exception thrown when an optional feature or extension is used in a kernel but it’s not available on the device the SYCL kernel is being enqueued on.</td>
</tr>
</tbody>
</table>

End of table

Table 4.103: Values of the SYCL `errc` enum.

### SYCL Error Code Helpers

<table>
<thead>
<tr>
<th>SYCL Error Code Helpers</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>std::error_condition make_error_condition(errc e)</code> noexcept;</td>
<td>Constructs an error condition using <code>e</code> and <code>sycl_category()</code>.</td>
</tr>
<tr>
<td><code>std::error_code make_error_code(errc e) noexcept;</code></td>
<td>Constructs an error code using <code>e</code> and <code>sycl_category()</code>.</td>
</tr>
</tbody>
</table>

End of table

Table 4.104: SYCL error code helper functions.
4.16 Data types

SYCL as a C++ programming model supports the C++ core language data types, and it also provides the ability for all SYCL applications to be executed on SYCL compatible devices. The scalar and vector data types that are supported by the SYCL system are defined below. More details about the SYCL device compiler support for fundamental and OpenCL interoperability types are found in 5.5.

4.16.1 Scalar data types

The fundamental C++ data types which are supported in SYCL are described in Table 5.1. Note these types are fundamental and therefore do not exist within the sycl namespace.

Additional scalar data types which are supported by SYCL within the sycl namespace are described in Table 4.105.

<table>
<thead>
<tr>
<th>Scalar data type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>byte</td>
<td>An unsigned 8-bit integer. This is deprecated in SYCL 2020 since C++17 std::byte can be used instead.</td>
</tr>
<tr>
<td>half</td>
<td>A 16-bit floating-point. The half data type must conform to the IEEE 754-2008 half precision storage format. A SYCL feature_not_supported exception must be thrown if the half type is used in a SYCL kernel function which executes on a SYCL device that does not have aspect::fp16.</td>
</tr>
</tbody>
</table>

Table 4.105: Additional scalar data types supported by SYCL.

4.16.2 Vector types

SYCL provides a cross-platform class template that works efficiently on SYCL devices as well as in host C++ code. This type allows sharing of vectors between the host and its SYCL devices. The vector supports member functions that allow construction of a new vector from a swizzled set of component elements.

vec<typename dataT, int numElements> is a vector type that compiles down to a SYCL backend built-in vector types on SYCL devices, where possible, and provides compatible support on the host or when it is not possible. The vec class is templated on its number of elements and its element type. The number of elements parameter, numElements, can be one of: 1, 2, 3, 4, 8 or 16. Any other value shall produce a compilation failure. The element type parameter, dataT, must be one of the basic scalar types supported in device code.

The SYCL vec class template provides interoperability with the underlying vector type defined by vector_t which is available only when compiled for the device. The SYCL vec class can be constructed from an instance of vector_t and can implicitly convert to an instance of vector_t in order to support interoperability with native SYCL backend functions from a SYCL kernel function.

An instance of the SYCL vec class template can also be implicitly converted to an instance of the data type when the number of elements is 1 in order to allow single element vectors and scalars to be convertible with each other.
4.16.2.1 Vec interface

The constructors, member functions and non-member functions of the SYCL vec class template are listed in Tables 4.106, 4.107 and 4.108 respectively.

```cpp
namespace sycl {

enum class rounding_mode {
    automatic,
    rte,
    rtz,
    rtp,
    rtn
};

struct elem {
    static constexpr int x = 0;
    static constexpr int y = 1;
    static constexpr int z = 2;
    static constexpr int w = 3;
    static constexpr int r = 0;
    static constexpr int g = 1;
    static constexpr int b = 2;
    static constexpr int a = 3;
    static constexpr int s0 = 0;
    static constexpr int s1 = 1;
    static constexpr int s2 = 2;
    static constexpr int s3 = 3;
    static constexpr int s4 = 4;
    static constexpr int s5 = 5;
    static constexpr int s6 = 6;
    static constexpr int s7 = 7;
    static constexpr int s8 = 8;
    static constexpr int s9 = 9;
    static constexpr int sA = 10;
    static constexpr int sB = 11;
    static constexpr int sC = 12;
    static constexpr int sD = 13;
    static constexpr int sE = 14;
    static constexpr int sF = 15;
};

template <typename dataT, int numElements>
class vec {
public:
    using element_type = dataT;

#ifdef __SYCL_DEVICE_ONLY__
    using vector_t = __unspecified__;
#endif

    vec();
    explicit vec(const dataT &arg);

dataT operator[](int i);

};

}```
4.16. DATA TYPES

template <typename... argTN>
vec(const argTN&... args);
vec(const vec<dataT, numElements> &rhs);

#ifdef __SYCL_DEVICE_ONLY__
vec(vector_t openclVector);
operator vector_t() const;
#endif

// Available only when: numElements == 1
operator dataT() const;
static constexpr int get_count();
static constexpr size_t get_size();

template <typename convertT, rounding_mode roundingMode = rounding_mode::automatic>
vec<convertT, numElements> convert() const;

template <typename asT>
asT as() const;

// Available only when numElements <= 4.
// XYZW_ACCESS is: x, y, z, w, subject to numElements.
__swizzled_vec__ XYZW_ACCESS() const;

// Available only numElements == 4.
// RGBA_ACCESS is: r, g, b, a.
__swizzled_vec__ RGBA_ACCESS() const;

// INDEX_ACCESS is: s0, s1, s2, s3, s4, s5, s6, s7, s8, s9, sA, sB, sC, sD, sE, sF, subject to numElements.
__swizzled_vec__ INDEX_ACCESS() const;

#ifdef SYCL_SIMPLE_SWIZZLES
// Available only when numElements <= 4.
// XYZW_SWIZZLE is all permutations with repetition of: x, y, z, w, subject to numElements.
__swizzled_vec__ XYZW_SWIZZLE() const;

// Available only when numElements == 4.
// RGBA_SWIZZLE is all permutations with repetition of: r, g, b, a.
__swizzled_vec__ RGBA_SWIZZLE() const;
#endif // ifdef SYCL_SIMPLE_SWIZZLES

// Available only when: numElements > 1.
__swizzled_vec__ lo() const;
__swizzled_vec__ hi() const;
__swizzled_vec__ odd() const;
__swizzled_vec__ even() const;

// load and store member functions

// load and store member functions

void load(size_t offset, multi_ptr<dataT, addressSpace, IsDecorated> ptr);
void store(size_t offset, multi_ptr<dataT, addressSpace, IsDecorated> ptr) const;

// subscript operator

dataT &operator[](int index);
const dataT &operator[](int index) const;

// OP is: +, -, *, /, %

/* If OP is % available, only when: dataT != float && dataT != double && dataT != half. */
friend vec operatorOP(const vec &lhs, const vec &rhs) { /* ... */ }
fraction vec operatorOP(const vec &lhs, const dataT &rhs) { /* ... */ }

// OP is: +=, -=, *=, /=, %=

/* If OP is %= available, only when: dataT != float && dataT != double && dataT != half. */
friend vec &operatorOP(vec &lhs, const vec &rhs) { /* ... */ }
friend vec &operatorOP(vec &lhs, const dataT &rhs) { /* ... */ }

// OP is: ++, --

friend vec &operatorOP(vec &lhs) { /* ... */ }
friend vec operatorOP(vec &lhs, int) { /* ... */ }

// OP is: &, |, ^

/* Available only when: dataT != float && dataT != double && dataT != half. */
friend vec &operatorOP(vec &lhs, const vec &rhs) { /* ... */ }
friend vec &operatorOP(vec &lhs, const dataT &rhs) { /* ... */ }

// OP is: &&, ||

friend vec<RET, numElements> operatorOP(const vec &lhs, const vec &rhs) { /* ... */ }
friend vec<RET, numElements> operatorOP(const vec &lhs, const dataT &rhs) { /* ... */ }

// OP is: <<, >>

/* Available only when: dataT != float && dataT != double && dataT != half. */
friend vec &operatorOP(vec &lhs, const vec &rhs) { /* ... */ }
friend vec &operatorOP(vec &lhs, const dataT &rhs) { /* ... */ }

// OP is: <<<<, >>>

/* Available only when: dataT != float && dataT != double && dataT != half. */
friend vec &operatorOP(vec &lhs, const vec &rhs) { /* ... */ }
friend vec &operatorOP(vec &lhs, const dataT &rhs) { /* ... */ }

// OP is: <<<<, >>>>

/* Available only when: dataT != float && dataT != double && dataT != half. */
friend vec &operatorOP(vec &lhs, const vec &rhs) { /* ... */ }
friend vec &operatorOP(vec &lhs, const dataT &rhs) { /* ... */ }

CHAPTER 4. SYCL PROGRAMMING INTERFACE
&& dataT != half. */
friend vec &operatorOP(vec &lhs, const vec &rhs) { /* ... */ }
friend vec &operatorOP(vec &lhs, const dataT &rhs) { /* ... */ }

// OP is: ==, !=, <, >, <=, >=
friend vec<RET, numElements> operatorOP(const vec &lhs, const vec &rhs) { /* ... */ }
friend vec<RET, numElements> operatorOP(const vec &lhs, const dataT &rhs) { /* ... */ }

vec &operator=(const vec<dataT, numElements> &rhs);
vec &operator=(const dataT &rhs);

// OP is: +, -, /*, %
// operator% is only available when: dataT != float && dataT != double &&
dataT != half. */
friend vec operatorOP(const dataT &lhs, const vec &rhs) { /* ... */ }

// OP is: &, |, ^
// Available only when: dataT != float && dataT != double &&
dataT != half. */
friend vec operatorOP(const dataT &lhs, const vec &rhs) { /* ... */ }

// OP is: &&, ||
// Available only when: dataT != float && dataT != double &&
dataT != half. */
friend vec<RET, numElements> operatorOP(const dataT &lhs, const vec &rhs) { /* ... */ }

// OP is: <<, >>
// Available only when: dataT != float && dataT != double &&
dataT != half. */
friend vec operatorOP(const dataT &lhs, const vec &rhs) { /* ... */ }

friend vec<RET, numElements> operatorOP(const dataT &lhs, const vec &rhs) { /* ... */ }

};

// Deduction guides
// Available only when: (std::is_same_v<T, U> && ...)
template <class T, class... U>
vec(T, U...) -> vec<T, sizeof...(U) + 1>;

} // namespace sycl

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>vec()</td>
<td>Default construct a vector with element type dataT and with numElements dimensions by default construction of each of its elements.</td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.106: Constructors of the SYCL vec class template.
### Constructors of the SYCL `vec` class template

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>explicit vec(const dataT &amp;arg)</code></td>
<td>Construct a vector of element type <code>dataT</code> and <code>numElements</code> dimensions by setting each value to <code>arg</code> by assignment.</td>
</tr>
<tr>
<td><code>template &lt;typename... argTN&gt; vec(const argTN&amp;... args)</code></td>
<td>Construct a SYCL <code>vec</code> instance from any combination of scalar and SYCL <code>vec</code> parameters of the same element type, providing the total number of elements for all parameters sum to <code>numElements</code> of this <code>vec</code> specialization.</td>
</tr>
<tr>
<td><code>vec(const vec&lt;dataT, numElements&gt; &amp;rhs)</code></td>
<td>Construct a vector of element type <code>dataT</code> and <code>numElements</code> dimensions by copy from another similar vector.</td>
</tr>
<tr>
<td><code>vec(vector_t openclVector)</code></td>
<td>Available only when: compiled for the device. Constructs a SYCL <code>vec</code> instance from an instance of the underlying OpenCL vector type defined by <code>vector_t</code>.</td>
</tr>
</tbody>
</table>

Table 4.106: Constructors of the SYCL `vec` class template.

### Member functions for the SYCL `vec` class template

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>operator vector_t()const</code></td>
<td>Available only when: compiled for the device. Converts this SYCL <code>vec</code> instance to the underlying OpenCL vector type defined by <code>vector_t</code>.</td>
</tr>
<tr>
<td><code>operator dataT()const</code></td>
<td>Available only when: <code>numElements == 1</code>. Converts this SYCL <code>vec</code> instance to an instance of <code>dataT</code> with the value of the single element in this SYCL <code>vec</code> instance. The SYCL <code>vec</code> instance shall be implicitly convertible to the same data types, to which <code>dataT</code> is implicitly convertible. Note that conversion operator shall not be templated to allow standard conversion sequence for implicit conversion.</td>
</tr>
<tr>
<td><code>static constexpr int get_count()</code></td>
<td>Returns the number of elements of this SYCL <code>vec</code>.</td>
</tr>
<tr>
<td><code>static constexpr size_t get_size()</code></td>
<td>Returns the size of this SYCL <code>vec</code> in bytes. 3-element vector size matches 4-element vector size to provide interoperability with OpenCL vector types. The same rule applies to vector alignment as described in 4.16.2.6.</td>
</tr>
</tbody>
</table>

Table 4.107: Member functions for the SYCL `vec` class template.
### Table 4.107: Member functions for the SYCL vec class template.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template&lt;typename convertT, rounding_mode roundingMode = rounding_mode::automatic&gt; vec&lt;convertT, numElements&gt; convert() const</code></td>
<td>Converts this SYCL vec to a SYCL vec of a different element type specified by <code>convertT</code> using the rounding mode specified by <code>roundingMode</code>. The new SYCL vec type must have the same number of elements as this SYCL vec. The different rounding modes are described in Table 4.109.</td>
</tr>
<tr>
<td><code>template&lt;typename asT&gt; asT as() const</code></td>
<td>Bitwise reinterprets this SYCL vec as a SYCL vec of a different element type and number of elements specified by <code>asT</code>. The new SYCL vec type must have the same storage size in bytes as this SYCL vec.</td>
</tr>
<tr>
<td><code>template&lt;int... swizzleIndexes&gt; __swizzled_vec__ swizzle() const</code></td>
<td>Return an instance of the implementation defined intermediate class template <code>__swizzled_vec__</code> representing an index sequence which can be used to apply the swizzle in a valid expression as described in 4.16.2.4.</td>
</tr>
<tr>
<td><code>__swizzled_vec__ XYZW_ACCESS() const</code></td>
<td>Available only when: <code>numElements &lt;= 4</code>. Returns an instance of the implementation defined intermediate class template <code>__swizzled_vec__</code> representing an index sequence which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. Where <code>XYZW_ACCESS</code> is: <code>x</code> for <code>numElements == 1</code>, <code>x, y</code> for <code>numElements == 2</code>, <code>x, y, z</code> for <code>numElements == 3</code> and <code>x, y, z, w</code> for <code>numElements == 4</code>.</td>
</tr>
<tr>
<td><code>__swizzled_vec__ RGBA_ACCESS() const</code></td>
<td>Available only when: <code>numElements == 4</code>. Returns an instance of the implementation defined intermediate class template <code>__swizzled_vec__</code> representing an index sequence which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. Where <code>RGBA_ACCESS</code> is: <code>r, g, b, a</code>.</td>
</tr>
</tbody>
</table>

Continued on next page
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>swizzled_vec</strong> INDEX_ACCESS() const</td>
<td>Returns an instance of the implementation defined intermediate class template <strong>swizzled_vec</strong> representing an index sequence which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. Where INDEX_ACCESS is: s0 for numElements == 1, s0, s1 for numElements == 2, s0, s1, s2 for numElements == 3, s0, s1, s2, s3 for numElements == 4, s0, s1, s2, s3, s4, s5, s6, s7, s8 for numElements == 8 and s0, s1, s2, s3, s4, s5, s6, s7, s8, s9, sA, sB, sC, sD, sE, sF for numElements == 16.</td>
</tr>
<tr>
<td><strong>swizzled_vec</strong> XYZW_SWIZZLE() const</td>
<td>Available only when numElements &lt;= 4, and when the macro SYCL_SIMPLE_SWIZZLES is defined before including sycl.hpp. Returns an instance of the implementation defined intermediate class template <strong>swizzled_vec</strong> representing an index sequence which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. Where XYZW_SWIZZLE is all permutations with repetition, of any subset with length greater than 1, of x, y for numElements == 2, x, y, z for numElements == 3 and x, y, z, w for numElements == 4. For example a four element vec provides permutations including xyzw, xyyx and xz.</td>
</tr>
<tr>
<td><strong>swizzled_vec</strong> RGBA_SWIZZLE() const</td>
<td>Available only when numElements == 4, and when the macro SYCL_SIMPLE_SWIZZLES is defined before including sycl.hpp. Returns an instance of the implementation defined intermediate class template <strong>swizzled_vec</strong> representing an index sequence which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. Where RGBA_SWIZZLE is all permutations with repetition, of any subset with length greater than 1, of r, g, b, a. For example a four element vec provides permutations including rbga, rggg and rb.</td>
</tr>
</tbody>
</table>

Table 4.107: Member functions for the SYCL vec class template.
### Member function

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>swizzled_vec</strong> lo() const</td>
<td>Available only when: numElements &gt; 1. Return an instance of the implementation defined intermediate class template <strong>swizzled_vec</strong> representing an index sequence made up of the lower half of this SYCL vec which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. When numElements == 3, this SYCL vec is treated as though numElements == 4 with the fourth element undefined.</td>
</tr>
<tr>
<td><strong>swizzled_vec</strong> hi() const</td>
<td>Available only when: numElements &gt; 1. Return an instance of the implementation defined intermediate class template <strong>swizzled_vec</strong> representing an index sequence made up of the upper half of this SYCL vec which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. When numElements == 3, this SYCL vec is treated as though numElements == 4 with the fourth element undefined.</td>
</tr>
<tr>
<td><strong>swizzled_vec</strong> odd() const</td>
<td>Available only when: numElements &gt; 1. Return an instance of the implementation defined intermediate class template <strong>swizzled_vec</strong> representing an index sequence made up of the odd indexes of this SYCL vec which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. When numElements == 3, this SYCL vec is treated as though numElements == 4 with the fourth element undefined.</td>
</tr>
<tr>
<td><strong>swizzled_vec</strong> even() const</td>
<td>Available only when: numElements &gt; 1. Return an instance of the implementation defined intermediate class template <strong>swizzled_vec</strong> representing an index sequence made up of the even indexes of this SYCL vec which can be used to apply the swizzle in a valid expression as described in 4.16.2.4. When numElements == 3, this SYCL vec is treated as though numElements == 4 with the fourth element undefined.</td>
</tr>
</tbody>
</table>

**template**<access::address_space addressSpace, access::decorated IsDecorated>

    void load(size_t offset, multi_ptr<const dataT, addressSpace, IsDecorated> ptr)

Loads the values at the address of ptr offset in elements of type dataT by numElements * offset, into the components of this SYCL vec.

**template**<access::address_space addressSpace, access::decorated IsDecorated>

    void store(size_t offset, multi_ptr<dataT, addressSpace, IsDecorated> ptr) const

Stores the components of this SYCL vec into the values at the address of ptr offset in elements of type dataT by numElements * offset.

---

Table 4.107: Member functions for the SYCL vec class template.
Table 4.107: Member functions for the SYCL vec class template.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `dataT &operator[](int index)` | Returns a reference to the element stored within this SYCL vec at the index specified by `index`.
| `const dataT &operator[](int index) const` | Returns a const reference to the element stored within this SYCL vec at the index specified by `index`.
| `vec &operator=(const vec &rhs)` | Assign each element of the rhs SYCL vec to each element of this SYCL vec and return a reference to this SYCL vec.
| `vec &operator=(const dataT &rhs)` | Assign each element of the rhs scalar to each element of this SYCL vec and return a reference to this SYCL vec.

Table 4.108: Hidden friend functions of the vec class template.

<table>
<thead>
<tr>
<th>Hidden friend function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `vec operatorOP(const vec &lhs, const vec &rhs)` | If `OP` is `%` available, only when: `dataT != float && dataT != double && dataT != half`. Construct a new instance of the SYCL vec class template with the same template parameters as `lhs vec` with each element of the new SYCL vec instance the result of an element-wise `OP` arithmetic operation between each element of `lhs vec` and each element of the rhs SYCL vec.

Where `OP` is: `+`, `−`, `*`, `/`, `%`.

| `vec operatorOP(const vec &lhs, const dataT &rhs)` | If `OP` is `%` available, only when: `dataT != float && dataT != double && dataT != half`. Construct a new instance of the SYCL vec class template with the same template parameters as `lhs vec` with each element of the new SYCL vec instance the result of an element-wise `OP` arithmetic operation between each element of `lhs vec` and the rhs scalar.

Where `OP` is: `+`, `−`, `*`, `/`, `%`.

Continued on next page
### Hidden friend functions

<table>
<thead>
<tr>
<th>Description</th>
<th>vec &amp;operatorOP(vec &amp;lhs, const vec &amp;rhs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>If OP is %= available, only when: dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half. Perform an in-place element-wise OP arithmetic operation between each element of lhs vec and each element of the rhs SYCL vec and return lhs vec. Where OP is: +=, -=, *=, /=, %=.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Description</th>
<th>vec &amp;operatorOP(vec &amp;lhs, const dataT &amp;rhs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>If OP is %= available, only when: dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half. Perform an in-place element-wise OP arithmetic operation between each element of lhs vec and rhs scalar and return lhs vec. Where OP is: +=, -=, *=, /=, %=.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Description</th>
<th>vec &amp;operatorOP(vec &amp;v)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Perform an in-place element-wise OP prefix arithmetic operation on each element of lhs vec, assigning the result of each element to the corresponding element of lhs vec and return lhs vec. Where OP is: ++, --.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Description</th>
<th>vec operatorOP(vec &amp;v, int)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Perform an in-place element-wise OP postfix arithmetic operation on each element of lhs vec, assigning the result of each element to the corresponding element of lhs vec and returns a copy of lhs vec before the operation is performed. Where OP is: ++, --.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Description</th>
<th>vec operatorOP(vec &amp;v)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Construct a new instance of the SYCL vec class template with the same template parameters as this SYCL vec with each element of the new SYCL vec instance the result of an element-wise OP unary arithmetic operation on each element of this SYCL vec. Where OP is: +, -.</td>
<td></td>
</tr>
</tbody>
</table>

---

Table 4.108: Hidden friend functions of the vec class template.
### Hidden friend function

| vec operatorOP(const vec &lhs, const vec &rhs) | Available only when: dataT != float && dataT != double && dataT != half. Construct a new instance of the SYCL vec class template with the same template parameters as lhs vec with each element of the new SYCL vec instance the result of an element-wise OP bitwise operation between each element of lhs vec and each element of the rhs SYCL vec. Where OP is: &, |, `^`.

| vec operatorOP(const vec &lhs, const dataT &rhs) | Available only when: dataT != float && dataT != double && dataT != half. Construct a new instance of the SYCL vec class template with the same template parameters as lhs vec with each element of the new SYCL vec instance the result of an element-wise OP bitwise operation between each element of lhs vec and the rhs scalar. Where OP is: &, |, `^`.


---

Table 4.108: Hidden friend functions of the vec class template.
### Hidden friend function

| vec<RET, numElements> operatorOP(const vec &lhs, 
| const vec &rhs) | Construct a new instance of the SYCL vec class template with the same template parameters as 1hs vec with each element of the new SYCL vec instance the result of an element-wise OP logical operation between each element of 1hs vec and each element of the rhs SYCL vec.

The dataT template parameter of the constructed SYCL vec, RET, varies depending on the dataT template parameter of this SYCL vec. For a SYCL vec with dataT of type int8_t or uint8_t RET must be int8_t. For a SYCL vec with dataT of type int16_t, uint16_t or half RET must be int16_t. For a SYCL vec with dataT of type int32_t, uint32_t or float RET must be int32_t. For a SYCL vec with dataT of type int64_t, uint64_t or double RET must be int64_t.

Where OP is: &&, ||.

| vec<RET, numElements> operatorOP(const vec &lhs, 
| const dataT &rhs) | Construct a new instance of the SYCL vec class template with the same template parameters as this SYCL vec with each element of the new SYCL vec instance the result of an element-wise OP logical operation between each element of 1hs vec and the rhs scalar.

The dataT template parameter of the constructed SYCL vec, RET, varies depending on the dataT template parameter of this SYCL vec. For a SYCL vec with dataT of type int8_t or uint8_t RET must be int8_t. For a SYCL vec with dataT of type int16_t, uint16_t or half RET must be int16_t. For a SYCL vec with dataT of type int32_t, uint32_t or float RET must be int32_t. For a SYCL vec with dataT of type int64_t, uint64_t or double RET must be uint64_t.

Where OP is: &&, ||.

---

Table 4.108: Hidden friend functions of the vec class template.
### Hidden friend function

| Description                                                                 | Available only when: dataT != float && dataT != double && dataT != half. Construct a new instance of the SYCL vec class template with the same template parameters as lhs vec with each element of the new SYCL vec instance the result of an element-wise OP bitshift operation between each element of lhs vec and each element of the rhs SYCL vec. If OP is >>, dataT is a signed type and lhs vec has a negative value any vacated bits viewed as an unsigned integer must be assigned the value 1, otherwise any vacated bits viewed as an unsigned integer must be assigned the value 0. Where OP is: <<, >>. |
| vec operatorOP(const vec &lhs, const vec &rhs)                                                                 |
| vec operatorOP(const vec &lhs, const dataT &rhs)                                                                 |
| vec &operatorOP(vec &lhs, const vec &rhs)                                                                 |

| Table 4.108: Hidden friend functions of the vec class template. |

Continued on next page
### Hidden friend function

<table>
<thead>
<tr>
<th>Description</th>
<th>vec &amp;operatorOP(vec &amp;lhs, const dataT &amp;rhs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Available only when: dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half. Perform an in-place element-wise OP bit-shift operation between each element of lhs vec and the rhs scalar and returns a reference to this SYCL vec. If OP is &gt;&gt;=, dataT is a signed type and lhs vec has a negative value any vacated bits viewed as an unsigned integer must be assigned the value 1, otherwise any vacated bits viewed as an unsigned integer must be assigned the value 0. Where OP is: &lt;&lt;=, &gt;&gt;=.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Description</th>
<th>vec&lt;RET, numElements&gt; operatorOP(const vec &amp;lhs, const vec &amp;rhs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Construct a new instance of the SYCL vec class template with the element type RET with each element of the new SYCL vec instance the result of an element-wise OP relational operation between each element of lhs vec and each element of the rhs SYCL vec. Each element of the SYCL vec that is returned must be -1 if the operation results in true and 0 if the operation results in false or either this SYCL vec or the rhs SYCL vec is a NaN. The dataT template parameter of the constructed SYCL vec, RET, varies depending on the dataT template parameter of this SYCL vec. For a SYCL vec with dataT of type int8_t or uint8_t RET must be int8_t. For a SYCL vec with dataT of type int16_t, uint16_t or half RET must be int16_t. For a SYCL vec with dataT of type int32_t, uint32_t or float RET must be int32_t. For a SYCL vec with dataT of type int64_t, uint64_t or double RET must be uint64_t. Where OP is: ==, !=, &lt;, &gt;, &lt;=, &gt;=.</td>
<td></td>
</tr>
</tbody>
</table>

Table 4.108: Hidden friend functions of the vec class template.
Hidden friend function | Description |
--- | --- |
`vec<RET, numElements> operatorOP(const vec &lhs, const dataT &rhs)` | Construct a new instance of the SYCL `vec` class template with the `dataT` parameter of `RET` with each element of the new SYCL `vec` instance the result of an element-wise `OP` relational operation between each element of `lhs vec` and the `rhs` scalar. Each element of the SYCL `vec` that is returned must be `-1` if the operation results in `true` and `0` if the operation results in `false` or either `lhs vec` or the `rhs` SYCL `vec` is a NaN. The `dataT` template parameter of the constructed SYCL `vec`, `RET`, varies depending on the `dataT` template parameter of this SYCL `vec`. For a SYCL `vec` with `dataT` of type `int8_t` or `uint8_t` `RET` must be `int8_t`. For a SYCL `vec` with `dataT` of type `int16_t`, `uint16_t` or `half` `RET` must be `int16_t`. For a SYCL `vec` with `dataT` of type `int32_t`, `uint32_t` or `float` `RET` must be `int32_t`. For a SYCL `vec` with `dataT` of type `int64_t`, `uint64_t` or `double` `RET` must be `uint64_t`. Where `OP` is: `==, !=, <, >, <=, >=`.

| `vec operatorOP(const dataT &lhs, const vec &rhs)` | If `OP` is `%` available, only when: `dataT != float && dataT != double && dataT != half`. Construct a new instance of the SYCL `vec` class template with the same template parameters as the `rhs` SYCL `vec` with each element of the new SYCL `vec` instance the result of an element-wise `OP` arithmetic operation between the `lhs` scalar and each element of the `rhs` SYCL `vec`. Where `OP` is: `+, -, *, /, %.` |

| `vec operatorOP(const dataT &lhs, const vec &rhs)` | Available only when: `dataT != float && dataT != double && dataT != half`. Construct a new instance of the SYCL `vec` class template with the same template parameters as the `rhs` SYCL `vec` with each element of the new SYCL `vec` instance the result of an element-wise `OP` bitwise operation between the `lhs` scalar and each element of the `rhs` SYCL `vec`. Where `OP` is: `&`, `|`, `^`. |

Table 4.108: Hidden friend functions of the `vec` class template.
Hidden friend function | Description
---|---
`vec<RET, numElements> operatorOP(const dataT &lhs, const vec &rhs)` | Available only when: `dataT != float` & `dataT != double` & `dataT != half`. Construct a new instance of the SYCL `vec` class template with the same template parameters as the rhs SYCL `vec` with each element of the new SYCL `vec` instance the result of an element-wise `OP` logical operation between the lhs scalar and each element of the rhs SYCL `vec`. The `dataT` template parameter of the constructed SYCL `vec`, `RET`, varies depending on the `dataT` template parameter of this SYCL `vec`. For a SYCL `vec` with `dataT` of type `int8_t` or `uint8_t` `RET` must be `int8_t`. For a SYCL `vec` with `dataT` of type `int16_t`, `uint16_t` or `half` `RET` must be `int16_t`. For a SYCL `vec` with `dataT` of type `int32_t`, `uint32_t` or `float` `RET` must be `int32_t`. For a SYCL `vec` with `dataT` of type `int64_t`, `uint64_t` or `double` `RET` must be `int64_t`. Where `OP` is: `&&`, `||`.

`vec operatorOP(const dataT &lhs, const vec &rhs)` | Construct a new instance of the SYCL `vec` class template with the same template parameters as the rhs SYCL `vec` with each element of the new SYCL `vec` instance the result of an element-wise `OP` bitshift operation between the lhs scalar and each element of the rhs SYCL `vec`. If `OP` is `>>`, `dataT` is a signed type and this SYCL `vec` has a negative value any vacated bits viewed as an unsigned integer must be assigned the value 1, otherwise any vacated bits viewed as an unsigned integer must be assigned the value 0. Where `OP` is: `<<`, `>>`.

Table 4.108: Hidden friend functions of the `vec` class template.
### Hidden friend function

| vec<RET, numElements> operatorOP(const dataT &lhs, const vec &rhs) |  
|---|---|
| Available only when: dataT != float && dataT != double && dataT != half. Construct a new instance of the SYCL vec class template with the element type RET with each element of the new SYCL vec instance the result of an element-wise OP relational operation between the lhs scalar and each element of the rhs SYCL vec. Each element of the SYCL vec that is returned must be -1 if the operation results in true and 0 if the operation results in false or either this SYCL vec or the rhs SYCL vec is a NaN. The dataT template parameter of the constructed SYCL vec, RET, varies depending on the dataT template parameter of this SYCL vec. For a SYCL vec with dataT of type int8_t or uint8_t RET must be int8_t. For a SYCL vec with dataT of type int16_t, uint16_t or half RET must be int16_t. For a SYCL vec with dataT of type int32_t, uint32_t or float RET must be int32_t. For a SYCL vec with dataT of type int64_t, uint64_t or double RET must be int64_t. Where OP is: ==, !=, <, >, <=, >=. |

| vec &operator~(const vec &v) |  
|---|---|
| Available only when: dataT != float && dataT != double && dataT != half. Construct a new instance of the SYCL vec class template with the same template parameters as v vec with each element of the new SYCL vec instance the result of an element-wise OP bitwise operation on each element of v vec. |

Table 4.108: Hidden friend functions of the vec class template.
### Hidden friend function

<table>
<thead>
<tr>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Construct a new instance of the SYCL vec class template with the same template parameters as v vec with each element of the new SYCL vec instance the result of an element-wise OP logical operation on each element of v vec. Each element of the SYCL vec that is returned must be -1 if the operation results in true and 0 if the operation results in false or this SYCL vec is a NaN.</td>
</tr>
</tbody>
</table>

The dataT template parameter of the constructed SYCL vec, RET, varies depending on the dataT template parameter of this SYCL vec. For a SYCL vec with dataT of type int8_t or uint8_t RET must be int8_t. For a SYCL vec with dataT of type int16_t, uint16_t or half RET must be int16_t. For a SYCL vec with dataT of type int32_t, uint32_t or float RET must be int32_t. For a SYCL vec with dataT of type int64_t, uint64_t or double RET must be int64_t.

<table>
<thead>
<tr>
<th>Hidden friend function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>vec&lt;RET, numElements&gt; operator!(const vec &amp;v)</td>
<td>Construct a new instance of the SYCL vec class template with the same template parameters as v vec with each element of the new SYCL vec instance the result of an element-wise OP logical operation on each element of v vec. Each element of the SYCL vec that is returned must be -1 if the operation results in true and 0 if the operation results in false or this SYCL vec is a NaN.</td>
</tr>
</tbody>
</table>

The dataT template parameter of the constructed SYCL vec, RET, varies depending on the dataT template parameter of this SYCL vec. For a SYCL vec with dataT of type int8_t or uint8_t RET must be int8_t. For a SYCL vec with dataT of type int16_t, uint16_t or half RET must be int16_t. For a SYCL vec with dataT of type int32_t, uint32_t or float RET must be int32_t. For a SYCL vec with dataT of type int64_t, uint64_t or double RET must be int64_t.

| End of table |

#### 4.16.2.2 Aliases

The SYCL programming API provides all permutations of the type alias:

using <type><elems> = vec<<storage-type>, <elems>>

where <elems> is 2, 3, 4, 8 and 16, and pairings of <type> and <storage-type> for integral types are char and int8_t, uchar and uint8_t, short and int16_t, ushort and uint16_t, int and int32_t, uint and uint32_t, long and int64_t, ulong and uint64_t, and for floating point types are both half, float and double.

For example uint4 is the alias to vec<uint64_t, 4> and float16 is the alias to vec<float, 16>.

#### 4.16.2.3 Swizzles

Swizzle operations can be performed in two ways. Firstly by calling the swizzle member function template, which takes a variadic number of integer template arguments between 0 and numElements-1, specifying swizzle indexes. Secondly by calling one of the simple swizzle member functions defined in 4.107 as XYZW_SWIZZLE and RGBA_SWIZZLE. Note that the simple swizzle functions are only available for up to 4 element vectors and are only available when the macro SYCL_SIMPLE_SWIZZLES is defined before including sycl.hpp.

In both cases the return type is always an instance of __swizzled_vec__, an implementation defined temporary class representing a swizzle of the original SYCL vec instance. Both kinds of swizzle member functions must not perform the swizzle operation themselves, instead the swizzle operation must be performed by the returned
instance of __swizzled_vec__ when used within an expression, meaning if the returned __swizzled_vec__ is never used in an expression no swizzle operation is performed.

Both the swizzle member function template and the simple swizzle member functions allow swizzle indexes to be repeated.

A series of static constexpr values are provided within the elem struct to allow specifying named swizzle indexes when calling the swizzle member function template.

4.16.2.4 Swizzled vec class

The __swizzled_vec__ class must define an unspecified temporary which provides the entire interface of the SYCL vec class template, including swizzled member functions, with the additions and alterations described below:

- The __swizzled_vec__ class template must be readable as an r-value reference on the RHS of an expression. In this case the swizzle operation is performed on the RHS of the expression and then the result is applied to the LHS of the expression.

- The __swizzled_vec__ class template must be assignable as an l-value reference on the LHS of an expression. In this case the RHS of the expression is applied to the original SYCL vec which the __swizzled_vec__ represents via the swizzle operation. Note that a __swizzled_vec__ that is used in an l-value expression may not contain any repeated element indexes. For example: \( f4.xxxx() = fx.wzyx() \) would not be valid.

- The __swizzled_vec__ class template must be convertible to an instance of SYCL vec with the type dataT and number of elements specified by the swizzle member function, if numElements > 1, and must be convertible to an instance of type dataT, if numElements == 1.

- The __swizzled_vec__ class template must be non-copyable, non-moveable, non-user constructible and may not be bound to a l-value or escape the expression it was constructed in. For example auto x = f4.x () would not be valid.

- The __swizzled_vec__ class template should return __swizzled_vec__ & for each operator inhered from the vec class template interface which would return vec<dataT, numElements> &.

4.16.2.5 Rounding modes

The various rounding modes that can be used in the as member function template are described in Table 4.109.

<table>
<thead>
<tr>
<th>Rounding mode</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>automatic</td>
<td>Default rounding mode for the SYCL vec class element type. rtz (round toward zero) for integer types and rte (round to nearest even) for floating-point types.</td>
</tr>
<tr>
<td>rte</td>
<td>Round to nearest even.</td>
</tr>
<tr>
<td>rtz</td>
<td>Round toward zero.</td>
</tr>
<tr>
<td>rtp</td>
<td>Round toward positive infinity.</td>
</tr>
<tr>
<td>rtm</td>
<td>Round toward negative infinity.</td>
</tr>
</tbody>
</table>

End of table

Table 4.109: Rounding modes for the SYCL vec class template.
4.16.2.6 Memory layout and alignment

The elements of an instance of the SYCL vec class template are stored in memory sequentially and contiguously and are aligned to the size of the element type in bytes multiplied by the number of elements:

$$\text{sizeof}(\text{dataT}) \cdot \text{numElements}$$  \hspace{1cm} (4.6)

The exception to this is when the number of element is three in which case the SYCL vec is aligned to the size of the element type in bytes multiplied by four:

$$\text{sizeof}(\text{dataT}) \cdot 4$$  \hspace{1cm} (4.7)

This is true for both host and device code in order to allow for instances of the vec class template to be passed to SYCL kernel functions.

4.16.2.7 Considerations for endianness

As SYCL supports both big-endian and little-endian on OpenCL devices, users must take care to ensure kernel arguments are processed correctly. This is particularly true for SYCL vec arguments as the order in which a SYCL vec is loaded differs between big-endian and little-endian.

Users should consult vendor documentation for guidance on how to handle kernel arguments in these situations.

4.16.2.8 Performance note

The usage of the subscript operator[] may not be efficient on some devices.

4.16.3 Math array types

SYCL provides a marray<

type name dataT, std::size_t numElements>

class template to represent a contiguous fixed-size container. This type allows sharing of containers between the host and its SYCL devices.

The marray class is templated on its element type and number of elements. The number of elements parameter, numElements, is a positive value of the std::size_t type. The element type parameter, dataT, must be a Numeric type as it is defined by C++ standard.

An instance of the marray class template can also be implicitly converted to an instance of the data type when the number of elements is 1 in order to allow single element arrays and scalars to be convertible with each other.

Logical and comparison operators for marray class template return marray<bool, numElements>.

4.16.3.1 Math array interface

The constructors, member functions and non-member functions of the SYCL marray class template are listed in Tables 4.110, 4.111 and 4.112 respectively.

```cpp
1 namespace sycl {
2 3 template <typename dataT, std::size_t numElements>
4 class marray {
```
public:
using value_type = dataT;
using reference = dataT&;
using const_reference = const dataT&;
using iterator = dataT*;
using const_iterator = const dataT*;
marray();
explicit marray(const dataT &arg);
template <typename... argTN>
marray(const argTN&... args);
marray(const marray<dataT, numElements> &rhs);
marray(marray<dataT, numElements> &&rhs);

// Available only when: numElements == 1
operator dataT() const;
static constexpr std::size_t size();

// subscript operator
reference operator[](std::size_t index);
const_reference operator[](std::size_t index) const;
marray &operator=(const marray<dataT, numElements> &rhs);
marray &operator=(const dataT &rhs);

// iterator functions
iterator begin(marray &v);
const_iterator begin(const marray &v);
iterator end(marray &v);
const_iterator end(const marray &v);

// OP is: +, -, *, /, %
/* If OP is % available, only when: dataT != float && dataT != double && dataT != half. */
marray operatorOP(const marray &lhs, const marray &rhs) { /* ... */ }
marray operatorOP(const marray &lhs, const dataT &rhs) { /* ... */ }

// OP is: +=, -=, *=, /=, %=
/* If OP is %= available, only when: dataT != float && dataT != double && dataT != half. */
marray operatorOP(marray &lhs, const marray &rhs) { /* ... */ }
marray operatorOP(marray &lhs, const dataT &rhs) { /* ... */ }

// OP is: ++, --
marray &operatorOP(marray &lhs) { /* ... */ }
marray operatorOP(marray &lhs, int) { /* ... */ }

// OP is: +, -
marray operatorOP(marray &lhs) const { /* ... */ }

// OP is: &,

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/* Available only when: dataT != float && dataT != double && dataT != half. */
marray operatorOP(const marray &lhs, const marray &rhs) { /* ... */ }
marray operatorOP(const marray &lhs, const dataT &rhs) { /* ... */ }

// OP is: &&, ||
marray<bool, numElements> operatorOP(const marray &lhs, const marray &rhs) { /* ... */ }
marray<bool, numElements> operatorOP(const marray& lhs, const dataT &rhs) { /* ... */ }

// OP is: <<, >>
marray<bool, numElements> operatorOP(const marray &lhs, const marray &rhs) { /* ... */ }
marray<bool, numElements> operatorOP(const marray &lhs, const dataT &rhs) { /* ... */ }

// OP is: ==, !=, <, >, <=, >=
marray<bool, numElements> operatorOP(const marray &lhs, const marray &rhs) { /* ... */ }
marray<bool, numElements> operatorOP(const marray &lhs, const dataT &rhs) { /* ... */ }

marray operator∼(const marray &v) { /* ... */ }
marray<bool, numElements> operator!(const marray &v) { /* ... */ }

} // namespace sycl
### Constructor

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>marray()</code></td>
<td>Default construct an array with element type <code>dataT</code> and with <code>numElements</code> dimensions by default construction of each of its elements.</td>
</tr>
<tr>
<td><code>explicit marray(const dataT &amp;arg)</code></td>
<td>Construct an array of element type <code>dataT</code> and <code>numElements</code> dimensions by setting each value to <code>arg</code> by assignment.</td>
</tr>
<tr>
<td><code>template &lt;typename... argTN&gt; marray(const argTN&amp;... args)</code></td>
<td>Construct a SYCL <code>marray</code> instance from any combination of scalar and SYCL <code>marray</code> parameters of the same element type, providing the total number of elements for all parameters sum to <code>numElements</code> of this <code>marray</code> specialization.</td>
</tr>
<tr>
<td><code>marray(const marray&lt;dataT, numElements&gt; &amp;rhs)</code></td>
<td>Construct an array of element type <code>dataT</code> and number of elements <code>numElements</code> by copy from another similar vector.</td>
</tr>
</tbody>
</table>

Table 4.110: Constructors of the SYCL `marray` class template.

### Member function

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>operator dataT()const</code></td>
<td>Available only when: <code>numElements == 1</code>. Converts this SYCL <code>marray</code> instance to an instance of <code>dataT</code> with the value of the single element in this SYCL <code>marray</code> instance. The SYCL <code>marray</code> instance shall be implicitly convertible to the same data types, to which <code>dataT</code> is implicitly convertible. Note that conversion operator shall not be templated to allow standard conversion sequence for implicit conversion.</td>
</tr>
<tr>
<td><code>static constexpr std::size_t size()</code></td>
<td>Returns the size of this SYCL <code>marray</code> in bytes. 3-element vector size matches 4-element vector size to provide interoperability with OpenCL vector types. The same rule applies to vector alignment as described in 4.16.2.6.</td>
</tr>
<tr>
<td><code>dataT &amp;operator[](std::size_t index)</code></td>
<td>Returns a reference to the element stored within this SYCL <code>marray</code> at the index specified by <code>index</code>.</td>
</tr>
<tr>
<td><code>const dataT &amp;operator[](std::size_t index) const</code></td>
<td>Returns a const reference to the element stored within this SYCL <code>marray</code> at the index specified by <code>index</code>.</td>
</tr>
<tr>
<td><code>marray &amp;operator=(const marray &amp;rhs)</code></td>
<td>Assign each element of the <code>rhs</code> SYCL <code>marray</code> to each element of this SYCL <code>marray</code> and return a reference to this SYCL <code>marray</code>.</td>
</tr>
</tbody>
</table>

Table 4.111: Member functions for the SYCL `marray` class template.
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<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>marray &amp;operator=(const dataT &amp;rhs)</code></td>
<td>Assign each element of the rhs scalar to each element of this SYCL <code>marray</code> and return a reference to this SYCL <code>marray</code>.</td>
</tr>
</tbody>
</table>

End of table

| Table 4.111: Member functions for the SYCL `marray` class template. |

<table>
<thead>
<tr>
<th>Non-member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>iterator begin(<code>marray &amp;v</code>)</td>
<td>Returns an iterator referring to the first element stored within the <code>v</code> <code>marray</code>.</td>
</tr>
<tr>
<td>const_iterator begin(<code>const marray &amp;v</code>)</td>
<td>Returns a const iterator referring to the first element stored within the <code>v</code> <code>marray</code>.</td>
</tr>
<tr>
<td>iterator end(<code>marray &amp;v</code>)</td>
<td>Returns an iterator referring to the one past the last element stored within the <code>v</code> <code>marray</code>.</td>
</tr>
<tr>
<td>const_iterator end(<code>const marray &amp;v</code>)</td>
<td>Returns a const iterator referring to the one past the last element stored within the <code>v</code> <code>marray</code>.</td>
</tr>
<tr>
<td><code>marray operatorOP(const marray &amp;lhs, const marray &amp;rhs)</code></td>
<td>If OP is % available, only when: <code>dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half</code>. Construct a new instance of the SYCL <code>marray</code> class template with the same template parameters as <code>lhs</code> <code>marray</code> with each element of the new SYCL <code>marray</code> instance the result of an element-wise OP arithmetic operation between each element of <code>lhs</code> <code>marray</code> and each element of the rhs SYCL <code>marray</code>. Where OP is: +, -, *, /, %.</td>
</tr>
</tbody>
</table>

| `
| marray operatorOP(const marray &lhs, const dataT &rhs)` | If OP is % available, only when: `dataT != float && dataT != double && dataT != half`. Construct a new instance of the SYCL `marray` class template with the same template parameters as `lhs` `marray` with each element of the new SYCL `marray` instance the result of an element-wise OP arithmetic operation between each element of `lhs` `marray` and the rhs scalar. Where OP is: +, -, *, /, %. |

Continued on next page

| Table 4.112: Non-member functions of the `marray` class template. |

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Non-member function | Description
--- | ---
marray &operator\(\text{OP}\)(marray &lhs, const marray &rhs) | If \(\text{OP}\) is %\= available, only when: \(\text{dataT} \neq \text{float} \&\& \text{dataT} \neq \text{double} \&\& \text{dataT} \neq \text{half}.
Perform an in-place element-wise \(\text{OP}\) arithmetic operation between each element of lhs marray and each element of the rhs SYCL marray and return lhs marray.
Where \(\text{OP}\) is: +=, -=, *=, /\=, %=.

marray &operator\(\text{OP}\)(marray &lhs, const dataT &rhs) | If \(\text{OP}\) is %\= available, only when: \(\text{dataT} \neq \text{float} \&\& \text{dataT} \neq \text{double} \&\& \text{dataT} \neq \text{half}.
Perform an in-place element-wise \(\text{OP}\) arithmetic operation between each element of lhs marray and rhs scalar and return lhs marray.
Where \(\text{OP}\) is: +=, -=, *=, /\=, %=.

marray &operator\(\text{OP}\)(marray &v) | Perform an in-place element-wise \(\text{OP}\) prefix arithmetic operation on each element of lhs marray, assigning the result of each element to the corresponding element of lhs marray and return lhs marray.
Where \(\text{OP}\) is: ++, --.

marray operator\(\text{OP}\)(marray &v, int) | Perform an in-place element-wise \(\text{OP}\) post-fix arithmetic operation on each element of lhs marray, assigning the result of each element to the corresponding element of lhs marray and returns a copy of lhs marray before the operation is performed.
Where \(\text{OP}\) is: ++, --.

marray operator\(\text{OP}\)(marray &v) | Construct a new instance of the SYCL marray class template with the same template parameters as this SYCL marray with each element of the new SYCL marray instance the result of an element-wise \(\text{OP}\) unary arithmetic operation on each element of this SYCL marray.
Where \(\text{OP}\) is: +, -.

Table 4.112: Non-member functions of the marray class template.
<table>
<thead>
<tr>
<th>Non-member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>marray operatorOP(const marray &amp;lhs, const marray &amp; rhs)</code></td>
<td>Available only when: <code>dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half</code>. Construct a new instance of the SYCL <code>marray</code> class template with the same template parameters as <code>lhs marray</code> with each element of the new SYCL <code>marray</code> instance the result of an element-wise OP bitwise operation between each element of <code>lhs marray</code> and each element of the <code>rhs SYCL marray</code>. Where OP is: <code>&amp;</code>, `</td>
</tr>
<tr>
<td><code>marray operatorOP(const marray &amp;lhs, const dataT &amp; rhs)</code></td>
<td>Available only when: <code>dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half</code>. Construct a new instance of the SYCL <code>marray</code> class template with the same template parameters as <code>lhs marray</code> with each element of the new SYCL <code>marray</code> instance the result of an element-wise OP bitwise operation between each element of <code>lhs marray</code> and the <code>rhs scalar</code>. Where OP is: <code>&amp;</code>, `</td>
</tr>
<tr>
<td><code>marray&lt;bool, numElements&gt; operatorOP(const marray &amp; lhs, const marray &amp; rhs)</code></td>
<td>Construct a new instance of the <code>marray</code> class template with <code>dataT = bool</code> and same <code>numElements</code> as <code>lhs marray</code> with each element of the new <code>marray</code> instance the result of an element-wise OP logical operation between each element of <code>lhs marray</code> and each element of the <code>rhs marray</code>. Where OP is: <code>&amp;&amp;</code>, `</td>
</tr>
</tbody>
</table>

Table 4.112: Non-member functions of the `marray` class template.
### Non-member function

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>marray&lt;</code>bool, numElements<code>&gt; operatorOP(const marray &amp; lhs, const dataT &amp;rhs)</code></td>
<td>Construct a new instance of the <code>marray</code> class template with <code>dataT = bool</code> and same <code>numElements</code> as <code>lhs marray</code> with each element of the new <code>marray</code> instance the result of an element-wise OP logical operation between each element of <code>lhs marray</code> and the <code>rhs</code> scalar. Where OP is: <code>&amp;&amp;</code>, `</td>
</tr>
<tr>
<td><code>marray operatorOP(const marray &amp;lhs, const marray &amp;rhs)</code></td>
<td>Available only when: <code>dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half</code>. Construct a new instance of the SYCL <code>marray</code> class template with the same template parameters as <code>lhs marray</code> with each element of the new SYCL <code>marray</code> instance the result of an element-wise OP bitshift operation between each element of <code>lhs marray</code> and each element of the <code>rhs</code> SYCL <code>marray</code>. If OP is <code>&gt;&gt;</code>, <code>dataT</code> is a signed type and <code>lhs marray</code> has a negative value any vacated bits viewed as an unsigned integer must be assigned the value 1, otherwise any vacated bits viewed as an unsigned integer must be assigned the value 0. Where OP is: <code>&lt;&lt;</code>, <code>&gt;&gt;</code>.</td>
</tr>
<tr>
<td><code>marray operatorOP(const marray &amp;lhs, const dataT &amp;rhs)</code></td>
<td>Available only when: <code>dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half</code>. Construct a new instance of the SYCL <code>marray</code> class template with the same template parameters as <code>lhs marray</code> with each element of the new SYCL <code>marray</code> instance the result of an element-wise OP bitshift operation between each element of <code>lhs marray</code> and the <code>rhs</code> scalar. If OP is <code>&gt;&gt;</code>, <code>dataT</code> is a signed type and <code>lhs marray</code> has a negative value any vacated bits viewed as an unsigned integer must be assigned the value 1, otherwise any vacated bits viewed as an unsigned integer must be assigned the value 0. Where OP is: <code>&lt;&lt;</code>, <code>&gt;&gt;</code>.</td>
</tr>
</tbody>
</table>

Table 4.112: Non-member functions of the `marray` class template.
<table>
<thead>
<tr>
<th>Non-member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `marray &operatorO
 pa(marray &lhs, const marray &rhs)` | Available only when: `dataT != float && dataT != double && dataT != half`. Perform an in-place element-wise OP bit-shift operation between each element of `lhs marray` and the `rhs` SYCL `marray` and returns `lhs marray`. If `OP` is `>>=`, `dataT` is a signed type and `lhs marray` has a negative value any vacated bits viewed as an unsigned integer must be assigned the value 1, otherwise any vacated bits viewed as an unsigned integer must be assigned the value 0. Where `OP` is: `<<=`, `>>=`. |
| `marray &operatorO
 pa(marray &lhs, const dataT &rhs)` | Available only when: `dataT != float && dataT != double && dataT != half`. Perform an in-place element-wise OP bit-shift operation between each element of `lhs marray` and the `rhs` scalar and returns a reference to this SYCL `marray`. If `OP` is `>>=`, `dataT` is a signed type and `lhs marray` has a negative value any vacated bits viewed as an unsigned integer must be assigned the value 1, otherwise any vacated bits viewed as an unsigned integer must be assigned the value 0. Where `OP` is: `<<=`, `>>=`. |
| `marray<bool, numElements> operatorO
 pa(const marray& lhs, const marray &rhs)` | Construct a new instance of the `marray` class template with `dataT = bool` and same `numElements` as `lhs marray` with each element of the new `marray` instance the result of an element-wise OP relational operation between each element of `lhs marray` and each element of the `rhs marray`. Corresponding element of the `marray` that is returned must be `false` if the operation results is a NaN. Where `OP` is: `==`, `!=`, `<`, `>`, `<=`, `>=`. |

Table 4.112: Non-member functions of the `marray` class template.
<table>
<thead>
<tr>
<th>Non-member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>marray&lt;bool, numElements&gt; operatorOP(const marray &amp; lhs, const dataT &amp;rhs)</code></td>
<td>Construct a new instance of the <code>marray</code> class template with <code>dataT = bool</code> and same <code>numElements</code> as <code>lhs marray</code> with each element of the new <code>marray</code> instance the result of an element-wise OP relational operation between each element of <code>lhs marray</code> and the <code>rhs</code> scalar. Corresponding element of the <code>marray</code> that is returned must be <code>false</code> if the operation results is a NaN. Where OP is: <code>==, !=, &lt;, &gt;, &lt;=, &gt;=</code>.</td>
</tr>
<tr>
<td><code>marray operatorOP(const dataT &amp;lhs, const marray &amp;rhs)</code></td>
<td>If OP is % available, only when: <code>dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half</code>. Construct a new instance of the SYCL marray class template with the same template parameters as the <code>rhs</code> SYCL marray with each element of the new SYCL marray instance the result of an element-wise OP arithmetic operation between the <code>lhs</code> scalar and each element of the <code>rhs</code> SYCL marray. Where OP is: <code>+,-,*,-,%.</code></td>
</tr>
<tr>
<td><code>marray operatorOP(const dataT &amp;lhs, const marray &amp;rhs)</code></td>
<td>Available only when: <code>dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half</code>. Construct a new instance of the SYCL marray class template with the same template parameters as the <code>rhs</code> SYCL marray with each element of the new SYCL marray instance the result of an element-wise OP bitwise operation between the <code>lhs</code> scalar and each element of the <code>rhs</code> SYCL marray. Where OP is: `&amp;,</td>
</tr>
<tr>
<td><code>marray&lt;RET, numElements&gt; operatorOP(const dataT &amp;lhs, const marray &amp;rhs)</code></td>
<td>Available only when: <code>dataT != float &amp;&amp; dataT != double &amp;&amp; dataT != half</code>. Construct a new instance of the marray class template with <code>dataT = bool</code> and same <code>numElements</code> as <code>lhs marray</code> with each element of the new marray instance the result of an element-wise OP logical operation between the <code>lhs</code> scalar and each element of the <code>rhs marray</code>. Where OP is: `&amp;&amp;,</td>
</tr>
</tbody>
</table>

Table 4.112: Non-member functions of the `marray` class template.
### Non-member function

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `marray operatorOP(const dataT &lhs, const marray &rhs)` | Construct a new instance of the SYCL `marray` class template with the same template parameters as the `rhs` SYCL `marray` with each element of the new SYCL `marray` instance the result of an element-wise `OP` bitshift operation between the `lhs` scalar and each element of the `rhs` SYCL `marray`. If `OP` is `>>`, `dataT` is a signed type and this SYCL `marray` has a negative value any vacated bits viewed as an unsigned integer must be assigned the value 1, otherwise any vacated bits viewed as an unsigned integer must be assigned the value 0. Where `OP` is: `<<, >>`.

| `marray<bool, numElements> operatorOP(const dataT &lhs, const marray &rhs)` | Available only when: `dataT != float && dataT != double && dataT != half`. Construct a new instance of the `marray` class template with `dataT = bool` and same `numElements` as `lhs marray` with each element of the new SYCL `marray` instance the result of an element-wise `OP` relational operation between the `lhs` scalar and each element of the `rhs marray`. Corresponding element of the `marray` that is returned must be `false` if the operation results is a NaN. Where `OP` is: `==, !=, <, >, <=, >=`.

| `marray &operator~(const marray &v)` | Available only when: `dataT != float && dataT != double && dataT != half`. Construct a new instance of the SYCL `marray` class template with the same template parameters as `v marray` with each element of the new SYCL `marray` instance the result of an element-wise `OP` bitwise operation on each element of `v marray`.

Continued on next page
<table>
<thead>
<tr>
<th>Non-member function</th>
<th>Description</th>
</tr>
</thead>
</table>
| `marray<bool, numElements> operator!(const marray &v)` | Construct a new instance of the `marray` class template with dataT = `bool` and same numElements as `v` `marray` with each element of the new `marray` instance the result of an element-wise logical `!` operation on each element of `v` `marray`.  

The dataT template parameter of the constructed SYCL `marray`, RET, varies depending on the dataT template parameter of this SYCL `marray`. For a SYCL `marray` with dataT of type `int8_t` or `uint8_t` RET must be `int8_t`. For a SYCL `marray` with dataT of type `int16_t`, `uint16_t` or `half` RET must be `int16_t`. For a SYCL `marray` with dataT of type `int32_t`, `uint32_t` or `float` RET must be `int32_t`. For a SYCL `marray` with dataT of type `int64_t`, `uint64_t` or `double` RET must be `int64_t`. |

Table 4.112: Non-member functions of the `marray` class template.

### 4.16.3.2 Aliases

The SYCL programming API provides all permutations of the type alias:

```cpp
using m<type><elems> = marray<<storage-type>, <elems>>
```

where `<elems>` is 2, 3, 4, 8 and 16, and pairings of `<type>` and `<storage-type>` for integral types are `char` and `int8_t`, `uchar` and `uint8_t`, `short` and `int16_t`, `ushort` and `uint16_t`, `int` and `int32_t`, `uint` and `uint32_t`, `long` and `int64_t`, `ulong` and `uint64_t`, for floating point types are both `half`, `float` and `double`, and for boolean type `bool`.

For example `muint4` is the alias to `marray<uint64_t, 4>` and `mfloat16` is the alias to `marray<float, 16>`.

### 4.16.3.3 Memory layout and alignment

The elements of an instance of the `marray` class template as if stored in `std::array<dataT, numElements>`.

### 4.17 Synchronization and atomics

The available features are:

- Accessor classes: Accessor classes specify acquisition and release of buffer and image data structures to provide points at which underlying queue synchronization primitives must be generated.

- Atomic operations: SYCL devices support a restricted subset of C++ atomics and SYCL uses the library syntax from the next C++ specification to make this available.
Fences: Fence primitives are made available to order loads and stores. They are exposed through the `atomic_fence` function. Fences can have acquire semantics, release semantics or both.

Barriers: Barrier primitives are made available to synchronize sets of work-items within individual groups. They are exposed through the `group_barrier` function.

Hierarchical parallel dispatch: In the hierarchical parallelism model of describing computations, synchronization within the work-group is made explicit through multiple instances of the `parallel_for_work_item` function call, rather than through the use of explicit `work-group barrier` operations.

Device event: they are used inside SYCL kernel functions to wait for asynchronous operations within a SYCL kernel function to complete.

### 4.17.1 Barriers and fences

A group barrier or mem-fence provides memory ordering semantics over both the local address space and global address space. All memory operations initiated before the group barrier or mem-fence operation will be completed before any memory operation after the group barrier or mem-fence.

```cpp
namespace sycl {

void atomic_fence(memory_order order, memory_scope scope);
}
```

The effects of a call to `atomic_fence` depend on the value of the `order` parameter:

- `memory_order::relaxed`: No effect
- `memory_order::acquire`: Acquire fence
- `memory_order::release`: Release fence
- `memory_order::acq_rel`: Both an acquire fence and a release fence
- `memory_order::seq_cst`: A sequentially consistent acquire and release fence

A group barrier acts as both an acquire fence and a release fence: all work-items in the group execute a release fence prior to synchronizing at the barrier, and all work-items in the group execute an acquire fence afterwards.

### 4.17.2 device_event class

The SYCL `device_event` class encapsulates a single SYCL device event which is available only within SYCL kernel functions and can be used to wait for asynchronous operations within a SYCL kernel function to complete. A SYCL device event can map to a SYCL backend event or just be a host event for a kernel running on the SYCL host device.

All member functions of the `device_event` class must not throw a SYCL exception.

A synopsis of the SYCL `device_event` class is provided below. The constructors and member functions of the SYCL `device_event` class are listed in Table 4.114 and 4.113 respectively.
namespace sycl {

class device_event {

    device_event(__unspecified__); 

public:

    void wait() noexcept;

};

} // namespace sycl

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>void wait() noexcept</td>
<td>Waits for the asynchronous operation associated with this SYCL device_event to complete.</td>
</tr>
</tbody>
</table>

Table 4.113: Member functions of the SYCL device_event class.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>device_event(<strong>unspecified</strong>)</td>
<td>Unspecified implementation defined constructor.</td>
</tr>
</tbody>
</table>

Table 4.114: Constructors of the device_event class.

### 4.17.3 Atomic references

The SYCL specification provides atomic operations based on the library syntax from the next C++ specification. The set of supported orderings is specific to a device, but every device is guaranteed to support at least memory_order::relaxed. Since different devices have different capabilities, there is no default ordering in SYCL and the default order used by each instance of sycl::atomic_ref is set by a template argument. If the default order is set to memory_order::relaxed, all memory order arguments default to memory_order::relaxed. If the default order is set to memory_order::acq_rel, memory order arguments default to memory_order::acquire for load operations, memory_order::release for store operations and memory_order::acq_rel for read-modify-write operations. If the default order is set to memory_order::seq_cst, all memory order arguments default to memory_order::seq_cst.

The SYCL atomic library may map directly to the underlying C++ library in SYCL application code, and must interact safely with the host C++ atomic library when used in host code. The SYCL library must be used in device code to ensure that only the limited subset of functionality is available. SYCL device compilers should give a compilation error on use of the std::atomic and std::atomic_ref classes and functions in device code.

The template parameter addressSpace is permitted to be access::address_space::generic_space, access::address_space::global_space or access::address_space::local_space.

The data type T is permitted to be int, unsigned int, long, unsigned long, long long, unsigned long long, float or double. For floating-point types, the member functions of the atomic_ref class may be emulated, and may use a different floating-point environment to those defined by info::device::single_fp_config and info::device::double_fp_config (i.e. floating-point atomics may use different rounding modes and may have
different exception behavior).

As detailed in Tables 4.116, 4.117, 4.118 and 4.119, not all devices support atomic operations for 64-bit data types. The member functions of the `atomic_ref` class are required to compile even if the device does not support 64-bit data types, however they are only guaranteed to execute if the `device` has that support. If a member function is called with a 64-bit data type and the `device` does not have the necessary support, the SYCL runtime must throw an exception with the `errc::feature_not_supported` error code.

The atomic types are defined as follows.

```cpp
namespace sycl {
enum class memory_order : /* unspecified */ {  
    relaxed, acquire, release, acq_rel, seq_cst
};
inline constexpr memory_order memory_order_relaxed = memory_order::relaxed;
inline constexpr memory_order memory_order_acquire = memory_order::acquire;
inline constexpr memory_order memory_order_release = memory_order::release;
inline constexpr memory_order memory_order_acq_rel = memory_order::acq_rel;
inline constexpr memory_order memory_order_seq_cst = memory_order::seq_cst;
enum class memory_scope : /* unspecified */ {  
    work_item, sub_group, work_group, device, system
};
inline constexpr memory_scope memory_scope_work_item = memory_scope::work_item;
inline constexpr memory_scope memory_scope_sub_group = memory_scope::sub_group;
inline constexpr memory_scope memory_scope_work_group = memory_scope::work_group;
inline constexpr memory_scope memory_scope_device = memory_scope::device;
inline constexpr memory_scope memory_scope_system = memory_scope::system;
// Exposition only
template <memory_order ReadModifyWriteOrder>
struct memory_order_traits;

template <>
struct memory_order_traits<memory_order::relaxed> {  
    static constexpr memory_order read_order = memory_order::relaxed;
    static constexpr memory_order write_order = memory_order::relaxed;
};

template <>
struct memory_order_traits<memory_order::acq_rel> {  
    static constexpr memory_order read_order = memory_order::acquire;
    static constexpr memory_order write_order = memory_order::release;
};

template <>
struct memory_order_traits<memory_order::seq_cst> {  
    static constexpr memory_order read_order = memory_order::seq_cst;
    static constexpr memory_order write_order = memory_order::seq_cst;
};

template <typename T, memory_order DefaultOrder, memory_scope DefaultScope, access::address_space Space = access::address_space::generic_space>
class atomic_ref {
```
public:

using value_type = T;

static constexpr size_t required_alignment = /* implementation-defined */;
static constexpr bool is_always_lock_free = /* implementation-defined */;
static constexpr memory_order default_read_order = memory_order_traits<DefaultOrder>::read_order;
static constexpr memory_order default_write_order = memory_order_traits<DefaultOrder>::write_order;
static constexpr memory_scope default_scope = DefaultScope;

bool is_lock_free() const noexcept;

explicit atomic_ref(T&);
atomic_ref(const atomic_ref&) noexcept;
atomic_ref& operator=(const atomic_ref&) = delete;

void store(T operand,
memory_order order = default_write_order,
memory_scope scope = default_scope) const noexcept;

T operator=(T desired) const noexcept;

T load(memory_order order = default_read_order,
memory_scope scope = default_scope) const noexcept;

operator T() const noexcept;

T exchange(T operand,
memory_order order = default_read_modify_write_order,
memory_scope scope = default_scope) const noexcept;

bool compare_exchange_weak(T &expected, T desired,
memory_order success,
memory_order failure,
memory_scope scope = default_scope) const noexcept;

bool compare_exchange_strong(T &expected, T desired,
memory_order success,
memory_order failure,
memory_scope scope = default_scope) const noexcept;

// Partial specialization for integral types

template <memory_order DefaultOrder, memory_scope DefaultScope, access::address_space Space =
access::address_space::generic_space>
class atomic_ref<Integral, DefaultOrder, DefaultScope, Space> {
    /* All other members from atomic_ref<T> are available */
    using difference_type = value_type;

    Integral fetch_add(Integral operand,
        memory_order order = default_read_modify_write_order,
        memory_scope scope = default_scope) const noexcept;

    Integral fetch_sub(Integral operand,
        memory_order order = default_read_modify_write_order,
        memory_scope scope = default_scope) const noexcept;

    Integral fetch_and(Integral operand,
        memory_order order = default_read_modify_write_order,
        memory_scope scope = default_scope) const noexcept;

    Integral fetch_or(Integral operand,
        memory_order order = default_read_modify_write_order,
        memory_scope scope = default_scope) const noexcept;

    Integral fetch_min(Integral operand,
        memory_order order = default_read_modify_write_order,
        memory_scope scope = default_scope) const noexcept;

    Integral fetch_max(Integral operand,
        memory_order order = default_read_modify_write_order,
        memory_scope scope = default_scope) const noexcept;

    Integral operator++(int) const noexcept;

    Integral operator--(int) const noexcept;

    Integral operator++() const noexcept;

    Integral operator--() const noexcept;

    Integral operator+=(Integral) const noexcept;

    Integral operator-=(Integral) const noexcept;

    Integral operator&=(Integral) const noexcept;

    Integral operator|=(Integral) const noexcept;

    Integral operatorˆ=(Integral) const noexcept;

    // Partial specialization for floating-point types
    template <memory_order DefaultOrder, memory_scope DefaultScope, access::address_space Space = access::address_space::generic_space>
    class atomic_ref<Floating, DefaultOrder, DefaultScope, Space> {

        using difference_type = value_type;

        Floating fetch_add(Floating operand,
            memory_order order = default_read_modify_write_order,
            memory_scope scope = default_scope) const noexcept;

    };

} // namespace sycl

// Partial specialization for floating-point types

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Floating fetch_sub(Floating operand,
    memory_order order = default_read_modify_write_order,
    memory_scope scope = default_scope) const noexcept;

Floating fetch_min(Floating operand,
    memory_order order = default_read_modify_write_order,
    memory_scope scope = default_scope) const noexcept;

Floating fetch_max(Floating operand,
    memory_order order = default_read_modify_write_order,
    memory_scope scope = default_scope) const noexcept;

Floating operator+=(Floating) const noexcept;
Floating operator-=(Floating) const noexcept;

// Partial specialization for pointers
template <typename T, memory_order DefaultOrder, memory_scope DefaultScope,
    access::address_space Space = access::address_space::generic_space>
class atomic_ref<T*, DefaultOrder, DefaultScope, Space> {

using value_type = T*;
using difference_type = ptrdiff_t;
static constexpr size_t required_alignment = /* implementation-defined */;
static constexpr bool is_always_lock_free = /* implementation-defined */;
static constexpr memory_order default_read_order = memory_order_traits<DefaultOrder>::read_order;
static constexpr memory_order default_write_order = memory_order_traits<DefaultOrder>::write_order;
static constexpr memory_order default_read_modify_write_order = DefaultOrder;
static constexpr memory_scope default_scope = DefaultScope;

bool is_lock_free() const noexcept;

explicit atomic_ref(T*&);
atomic_ref(const atomic_ref&) noexcept;
atomic_ref& operator=(const atomic_ref&) = delete;

void store(T* operand,
    memory_order order = default_write_order,
    memory_scope scope = default_scope) const noexcept;

T* operator=(T* desired) const noexcept;

T* load(memory_order order = default_read_order,
    memory_scope scope = default_scope) const noexcept;

operator T*() const noexcept;

T* exchange(T* operand,
    memory_order order = default_read_modify_write_order,
    memory_scope scope = default_scope) const noexcept;

bool compare_exchange_weak(T* &expected, T* desired,
4.17. SYNCHRONIZATION AND ATOMICS

```cpp
memory_order success,
memory_order failure,
memory_scope scope = default_scope) const noexcept;

bool compare_exchange_weak(T* &expected, T* desired,
memory_order order = default_read_modify_write_order,
memory_scope scope = default_scope) const noexcept;

bool compare_exchange_strong(T* &expected, T* desired,
memory_order success,
memory_order failure,
memory_scope scope = default_scope) const noexcept;

bool compare_exchange_strong(T* &expected, T* desired,
memory_order order = default_read_modify_write_order,
memory_scope scope = default_scope) const noexcept;

T* fetch_add(difference_type,
memory_order order = default_read_modify_write_order,
memory_scope scope = default_scope) const noexcept;

T* fetch_sub(difference_type,
memory_order order = default_read_modify_write_order,
memory_scope scope = default_scope) const noexcept;

T* operator++(int) const noexcept;
T* operator--(int) const noexcept;
T* operator++() const noexcept;
T* operator--() const noexcept;
T* operator+=(difference_type) const noexcept;
T* operator-=(difference_type) const noexcept;
}
``` // namespace sycl

The constructors and member functions for instances of the SYCL `atomic_ref` class using any compatible type are listed in Tables 4.115 and 4.116 respectively. Additional member functions for integral, floating-point and pointer types are listed in Tables 4.117, 4.118 and 4.119 respectively.

The static member `required_alignment` describes the minimum required alignment in bytes of an object that can be referenced by an `atomic_ref<T>`, which must be at least `alignof(T)`.

The static member `is_always_lock_free` is true if all atomic operations for type `T` are always lock-free. A SYCL implementation is not guaranteed to support atomic operations that are not lock-free.

The static members `default_read_order`, `default_write_order` and `default_read_modify_write_order` reflect the default memory order values for each type of atomic operation, consistent with the `DefaultOrder` template.

The atomic operations and member functions behave as described in the C++ specification, barring the restrictions discussed above. Note that care must be taken when using atomics to implement synchronization routines due to the lack of forward progress guarantees between work-items in SYCL. No work-item may be dependent on another work-item to make progress if the code is to be portable.
### Constructor

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>atomic_ref(T&amp; ref)</code></td>
<td>Constructs an instance of SYCL <code>atomic_ref</code> which is associated with the reference ref.</td>
</tr>
</tbody>
</table>

End of table

Table 4.115: Constructors of the SYCL `atomic_ref` class template.

### Member function

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>bool is_lock_free()</code> const</td>
<td>Return <code>true</code> if the atomic operations provided by this <code>atomic_ref</code> are lock-free.</td>
</tr>
<tr>
<td><code>void store(T operand, memory_order order = default_write_order, memory_scope scope = default_scope)const</code></td>
<td>Atomically stores operand to the object referenced by this <code>atomic_ref</code>. The memory order of this atomic operation must be memory_order::relaxed, memory_order::release or memory_order::seq_cst. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomics.</td>
</tr>
<tr>
<td><code>T operator=(T desired)const</code></td>
<td>Equivalent to <code>store(desired)</code>. Returns desired.</td>
</tr>
<tr>
<td><code>T load(memory_order order = default_read_order memory_odrer scope = default_scope)const</code></td>
<td>Atomically loads the value of the object referenced by this <code>atomic_ref</code>. The memory order of this atomic operation must be memory_order::relaxed, memory_order::acquire, or memory_order::seq_cst. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomics.</td>
</tr>
<tr>
<td><code>operator T()const</code></td>
<td>Equivalent to <code>load()</code>.</td>
</tr>
<tr>
<td><code>T exchange(T operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope)const</code></td>
<td>Atomically replaces the value of the object referenced by this <code>atomic_ref</code> with value operand and returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomics.</td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.116: Member functions available on any object of type `atomic_ref<T>`. 
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>bool compare_exchange_weak(T &amp;expected, T desired, memory_order success, memory_order failure, memory_scope scope = default_scope) const</code></td>
<td>Atomically compares the value of the object referenced by this <code>atomic_ref</code> against the value of expected. If the values are equal, attempts to replace the value of the referenced object with the value of desired; otherwise assigns the original value of the referenced object to expected. Returns <code>true</code> if the comparison operation and replacement operation were successful. The failure memory order of this atomic operation must be <code>memory_order::relaxed</code>, <code>memory_order::acquire</code> or <code>memory_order::seq_cst</code>. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_base_atomics</code>.</td>
</tr>
<tr>
<td><code>bool compare_exchange_weak(T &amp;expected, T desired, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope) const</code></td>
<td>Equivalent to <code>compare_exchange_weak(expected, desired, order, order, scope)</code>.</td>
</tr>
<tr>
<td><code>bool compare_exchange_strong(T &amp;expected, T desired, memory_order success, memory_order failure, memory_scope scope = default_scope) const</code></td>
<td>Atomically compares the value of the object referenced by this <code>atomic_ref</code> against the value of expected. If the values are equal, replaces the value of the referenced object with the value of desired; otherwise assigns the original value of the referenced object to expected. Returns <code>true</code> if the comparison operation was successful. The failure memory order of this atomic operation must be <code>memory_order::relaxed</code>, <code>memory_order::acquire</code> or <code>memory_order::seq_cst</code>. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_base_atomics</code>.</td>
</tr>
<tr>
<td><code>bool compare_exchange_strong(T &amp;expected, T desired, memory_order order = default_read_modify_write_order) const</code></td>
<td>Equivalent to <code>compare_exchange_strong(expected, desired, order, order, scope)</code>.</td>
</tr>
</tbody>
</table>

Table 4.116: Member functions available on any object of type `atomic_ref<T>`. 
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T fetch_add(T operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope) const</code></td>
<td>Atomically adds operand to the value of the object referenced by this <code>atomic_ref</code> and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_base_atomics</code>.</td>
</tr>
<tr>
<td><code>T operator+=(T operand) const</code></td>
<td>Equivalent to <code>fetch_add(operand)</code>.</td>
</tr>
<tr>
<td><code>T operator++(int) const</code></td>
<td>Equivalent to <code>fetch_add(1)</code>.</td>
</tr>
<tr>
<td><code>T operator++() const</code></td>
<td>Equivalent to <code>fetch_add(1)+ 1</code>.</td>
</tr>
<tr>
<td><code>T fetch_sub(T operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope) const</code></td>
<td>Atomically subtracts operand from the value of the object referenced by this <code>atomic_ref</code> and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_base_atomics</code>.</td>
</tr>
<tr>
<td><code>T operator-=(T operand) const</code></td>
<td>Equivalent to <code>fetch_sub(operand)</code>.</td>
</tr>
<tr>
<td><code>T operator--(int) const</code></td>
<td>Equivalent to <code>fetch_sub(1)</code>.</td>
</tr>
<tr>
<td><code>T operator--() const</code></td>
<td>Equivalent to <code>fetch_sub(1)- 1</code>.</td>
</tr>
<tr>
<td><code>T fetch_and(T operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope) const</code></td>
<td>Atomically performs a bitwise AND between operand and the value of the object referenced by this <code>atomic_ref</code>, and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_extended_atomics</code>.</td>
</tr>
<tr>
<td><code>T operator&amp;=(T operand) const</code></td>
<td>Equivalent to <code>fetch_and(operand)</code>.</td>
</tr>
<tr>
<td><code>T fetch_or(T operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope) const</code></td>
<td>Atomically performs a bitwise OR between operand and the value of the object referenced by this <code>atomic_ref</code>, and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_extended_atomics</code>.</td>
</tr>
<tr>
<td>`T operator</td>
<td>=(T operand) const`</td>
</tr>
<tr>
<td><code>T fetch_xor(T operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope) const</code></td>
<td>Atomically performs a bitwise XOR between operand and the value of the object referenced by this <code>atomic_ref</code>, and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_extended_atomics</code>.</td>
</tr>
<tr>
<td><code>T operatorˆ=(T operand) const</code></td>
<td>Equivalent to <code>fetch_xor(operand)</code>.</td>
</tr>
</tbody>
</table>

Table 4.117: Additional member functions available on an object of type `atomic_ref<T>` for integral T.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T fetch_min(T operand,</code></td>
<td>Atomically computes the minimum of operand and the value of the object referenced by this <code>atomic_ref</code>, and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_extended_atomics</code>.</td>
</tr>
<tr>
<td><code>memory_order order =</code></td>
<td></td>
</tr>
<tr>
<td><code>default_read_modify_write_order,</code></td>
<td></td>
</tr>
<tr>
<td><code>memory_scope scope = default_scope) const</code></td>
<td></td>
</tr>
</tbody>
</table>

| `T fetch_max(T operand,` | Atomically computes the maximum of operand and the value of the object referenced by this `atomic_ref`, and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have `aspect::int64_extended_atomics`. |
| `memory_order order =` | |
| `default_read_modify_write_order,` | |
| `memory_scope scope = default_scope) const` | |

End of table

Table 4.117: Additional member functions available on an object of type `atomic_ref<T>` for integral `T`.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T fetch_add(T operand,</code></td>
<td>Atomically adds operand to the value of the object referenced by this <code>atomic_ref</code> and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_base_atomics</code>.</td>
</tr>
<tr>
<td><code>memory_order order =</code></td>
<td></td>
</tr>
<tr>
<td><code>default_read_modify_write_order,</code></td>
<td></td>
</tr>
<tr>
<td><code>memory_scope scope = default_scope) const</code></td>
<td></td>
</tr>
<tr>
<td><code>T operator+=(T operand)const</code></td>
<td>Equivalent to <code>fetch_add(operand)</code>.</td>
</tr>
</tbody>
</table>

| `T fetch_sub(T operand,` | Atomically subtracts operand from the value of the object referenced by this `atomic_ref` and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have `aspect::int64_base_atomics`. |
| `memory_order order =` | |
| `default_read_modify_write_order,` | |
| `memory_scope scope = default_scope) const` | |
| `T operator-=(T operand)const` | Equivalent to `fetch_sub(operand)`. |

| `T fetch_min(T operand,` | Atomically computes the minimum of operand and the value of the object referenced by this `atomic_ref`, and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have `aspect::int64_extended_atomics`. |
| `memory_order order =` | |
| `default_read_modify_write_order,` | |
| `memory_scope scope = default_scope) const` | |

Continued on next page

Table 4.118: Additional member functions available on an object of type `atomic_ref<T>` for floating-point `T`. 
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T fetch_max(T operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope)const</code></td>
<td>Atomically computes the maximum of operand and the value of the object referenced by this <code>atomic_ref</code>, and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit data types on devices that have aspect::int64_extended_atomics.</td>
</tr>
</tbody>
</table>

Table 4.118: Additional member functions available on an object of type `atomic_ref<T>` for floating-point T.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T* fetch_add(ptrdiff_t operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope)const</code></td>
<td>Atomically adds operand to the value of the object referenced by this <code>atomic_ref</code> and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit pointers on devices that have aspect::int64_base_atomics.</td>
</tr>
<tr>
<td><code>T* operator+= (ptrdiff_t operand)const</code></td>
<td>Equivalent to <code>fetch_add(operand)</code>.</td>
</tr>
<tr>
<td><code>T* operator++(int) const</code></td>
<td>Equivalent to <code>fetch_add(1)</code>.</td>
</tr>
<tr>
<td><code>T* operator++() const</code></td>
<td>Equivalent to <code>fetch_add(1)+ 1</code>.</td>
</tr>
<tr>
<td><code>T* fetch_sub(ptrdiff_t operand, memory_order order = default_read_modify_write_order, memory_scope scope = default_scope)const</code></td>
<td>Atomically subtracts operand from the value of the object referenced by this <code>atomic_ref</code> and assigns the result to the value of the referenced object. Returns the original value of the referenced object. This function is only supported for 64-bit pointers on devices that have aspect::int64_base_atomics.</td>
</tr>
<tr>
<td><code>T* operator-= (ptrdiff_t operand)const</code></td>
<td>Equivalent to <code>fetch_sub(operand)</code>.</td>
</tr>
<tr>
<td><code>T* operator--(int) const</code></td>
<td>Equivalent to <code>fetch_sub(1)</code>.</td>
</tr>
<tr>
<td><code>T* operator--() const</code></td>
<td>Equivalent to <code>fetch_sub(1)- 1</code>.</td>
</tr>
</tbody>
</table>

Table 4.119: Additional member functions available on an object of type `atomic_ref<T*>`.

### 4.17.4 Atomic types (deprecated)

The atomic types and operations on atomic types provided by SYCL 1.2.1 are deprecated in SYCL 2020, and will be removed in a future version of SYCL. The types and operations are made available in the `cl::sycl::` namespace for backwards compatibility.

The constructors and member functions for the `cl::sycl::atomic` class are listed in Tables 4.120 and 4.121 respectively.

```cpp
namespace cl {
```

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namespace sycl {
  /* Deprecated in SYCL 2020 */
  enum class memory_order : int {
    relaxed
  };

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace = access::address_space::global_space>
  class atomic {
    public:
      template <typename pointerT, access::decorated IsDecorated>
      atomic(multi_ptr<pointerT, addressSpace, IsDecorated> ptr);

      void store(T operand, memory_order memoryOrder = memory_order::relaxed);

      T load(memory_order memoryOrder = memory_order::relaxed) const;

      T exchange(T operand, memory_order memoryOrder = memory_order::relaxed);

      /* Available only when: T != float */
      bool compare_exchange_strong(T &expected, T desired,
                                  memory_order successMemoryOrder = memory_order::relaxed,
                                  memory_order failMemoryOrder = memory_order::relaxed);

      /* Available only when: T != float */
      T fetch_add(T operand, memory_order memoryOrder = memory_order::relaxed);

      /* Available only when: T != float */
      T fetch_sub(T operand, memory_order memoryOrder = memory_order::relaxed);

      /* Available only when: T != float */
      T fetch_and(T operand, memory_order memoryOrder = memory_order::relaxed);

      /* Available only when: T != float */
      T fetch_or(T operand, memory_order memoryOrder = memory_order::relaxed);

      /* Available only when: T != float */
      T fetch_xor(T operand, memory_order memoryOrder = memory_order::relaxed);

      /* Available only when: T != float */
      T fetch_min(T operand, memory_order memoryOrder = memory_order::relaxed);

      /* Available only when: T != float */
      T fetch_max(T operand, memory_order memoryOrder = memory_order::relaxed);
  };
}
The global functions are as follows and described in Table 4.122.

```cpp
namespace cl {
namespace sycl {
  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  void atomic_store(atomic<T, addressSpace> object, T operand, memory_order memoryOrder = memory_order::relaxed);

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  T atomic_load(atomic<T, addressSpace> object, memory_order memoryOrder = memory_order::relaxed);

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  T atomic_exchange(atomic<T, addressSpace> object, T operand, memory_order memoryOrder = memory_order::relaxed);

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  bool atomic_compare_exchange_strong(atomic<T, addressSpace> object, T &expected, T desired,
               memory_order successMemoryOrder = memory_order::relaxed,
               memory_order failMemoryOrder = memory_order::relaxed);

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  T atomic_fetch_add(atomic<T, addressSpace> object, T operand, memory_order memoryOrder = memory_order::relaxed);

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  T atomic_fetch_sub(atomic<T, addressSpace> object, T operand, memory_order memoryOrder = memory_order::relaxed);

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  T atomic_fetch_and(atomic<T, addressSpace> object, T operand, memory_order memoryOrder = memory_order::relaxed);

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  T atomic_fetch_or(atomic<T, addressSpace> object, T operand, memory_order memoryOrder = memory_order::relaxed);

  /* Deprecated in SYCL 2020 */
  template <typename T, access::address_space addressSpace>
  T atomic_fetch_xor(atomic<T, addressSpace> object, T operand, memory_order memoryOrder = memory_order::relaxed);
}
}
```

CHAPTER 4. SYCL PROGRAMMING INTERFACE
template <typename T, access::address_space addressSpace>
T atomic_fetch_min(atomic<T, addressSpace> object, T operand, memory_order memoryOrder =
memory_order::relaxed);

template <typename T, access::address_space addressSpace>
T atomic_fetch_max(atomic<T, addressSpace> object, T operand, memory_order memoryOrder =
memory_order::relaxed);

} // namespace sycl
} // namespace cl

Table 4.120: Constructors of the cl::sycl::atomic class template.

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>template&lt;typename pointerT&gt;</td>
<td>atomic(multi_ptr&lt;pointerT, addressSpace&gt; ptr)</td>
</tr>
<tr>
<td></td>
<td>Deprecated in SYCL 2020. Permitted data types for pointerT are any valid scalar data type which is the same size in bytes as T. Constructs an instance of SYCL atomic which is associated with the pointer ptr, converted to a pointer of data type T.</td>
</tr>
</tbody>
</table>

Table 4.121: Member functions available on an object of type cl::sycl::atomic<T>.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>void store(T operand, memory_order memoryOrder = memory_order::relaxed)</td>
<td>Deprecated in SYCL 2020. Atomically stores the value operand at the address of the multi_ptr associated with this SYCL atomic. The memory order of this atomic operation must be memory_order::relaxed. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomics.</td>
</tr>
<tr>
<td>T load(memory_order memoryOrder = memory_order::relaxed) const</td>
<td>Deprecated in SYCL 2020. Atomically loads the value at the address of the multi_ptr associated with this SYCL atomic. Returns the value at the address of the multi_ptr associated with this SYCL atomic before the call. The memory order of this atomic operation must be memory_order::relaxed. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomics.</td>
</tr>
<tr>
<td>Member function</td>
<td>Description</td>
</tr>
<tr>
<td>-----------------------------------------------------</td>
<td>-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>T exchange(T operand, memory_order memoryOrder = memory_order::relaxed)</td>
<td>Deprecated in SYCL 2020. Atomically replaces the value at the address of the multi_ptr associated with this SYCL atomic with value operand and returns the value at the address of the multi_ptr associated with this SYCL atomic before the call. The memory order of this atomic operation must be memory_order::relaxed. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomsics.</td>
</tr>
<tr>
<td>bool compare_exchange_strong(T &amp;expected, T desired, memory_order successMemoryOrder = memory_order::relaxed, memory_order failMemoryOrder = memory_order::relaxed)</td>
<td>Deprecated in SYCL 2020. Available only when: T != float. Atomically compares the value at the address of the multi_ptr associated with this SYCL atomic against the value of expected. If the values are equal, replaces value at address of the multi_ptr associated with this SYCL atomic with the value of desired; otherwise assigns the original value at the address of the multi_ptr associated with this SYCL atomic to expected. Returns true if the comparison operation was successful. The memory order of this atomic operation must be memory_order::relaxed for both success and fail. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomsics.</td>
</tr>
<tr>
<td>T fetch_add(T operand, memory_order memoryOrder = memory_order::relaxed)</td>
<td>Deprecated in SYCL 2020. Available only when: T != float. Atomically adds the value operand to the value at the address of the multi_ptr associated with this SYCL atomic and assigns the result to the value at the address of the multi_ptr associated with this SYCL atomic. Returns the value at the address of the multi_ptr associated with this SYCL atomic before the call. The memory order of this atomic operation must be memory_order::relaxed. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomsics.</td>
</tr>
</tbody>
</table>

Table 4.121: Member functions available on an object of type cl::sycl::atomic<T>.
<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T fetch_sub(T operand, memory_order memoryOrder = memory_order::relaxed)</code></td>
<td>Deprecated in SYCL 2020. Available only when: <code>T != float</code>. Atomically subtracts the value <code>operand</code> to the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic and assigns the result to the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic. Returns the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic before the call. The memory order of this atomic operation must be <code>memory_order::relaxed</code>. This function is only supported for 64-bit data types on devices that have aspect::int64_base_atomics.</td>
</tr>
<tr>
<td><code>T fetch_and(T operand, memory_order memoryOrder = memory_order::relaxed)</code></td>
<td>Deprecated in SYCL 2020. Available only when: <code>T != float</code>. Atomically performs a bitwise AND between the value <code>operand</code> and the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic and assigns the result to the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic. Returns the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic before the call. The memory order of this atomic operation must be <code>memory_order::relaxed</code>. This function is only supported for 64-bit data types on devices that have aspect::int64_extended_atomics.</td>
</tr>
<tr>
<td><code>T fetch_or(T operand, memory_order memoryOrder = memory_order::relaxed)</code></td>
<td>Deprecated in SYCL 2020. Available only when: <code>T != float</code>. Atomically performs a bitwise OR between the value <code>operand</code> and the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic and assigns the result to the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic. Returns the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic before the call. The memory order of this atomic operation must be <code>memory_order::relaxed</code>. This function is only supported for 64-bit data types on devices that have aspect::int64_extended_atomics.</td>
</tr>
</tbody>
</table>

Table 4.121: Member functions available on an object of type `cl::sycl::atomic<T>`. 
Table 4.121: Member functions available on an object of type `cl::sycl::atomic<T>`.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T fetch_xor(T operand, memory_order memoryOrder = memory_order::relaxed)</code></td>
<td>Deprecated in SYCL 2020. Available only when: <code>T != float</code>. Atomically performs a bitwise XOR between the value <code>operand</code> and the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic and assigns the result to the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic. Returns the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic before the call. The memory order of this atomic operation must be <code>memory_order::relaxed</code>. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_extended_atomics</code>.</td>
</tr>
<tr>
<td><code>T fetch_min(T operand, memory_order memoryOrder = memory_order::relaxed)</code></td>
<td>Deprecated in SYCL 2020. Atomically computes the minimum of the value <code>operand</code> and the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic and assigns the result to the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic. Returns the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic before the call. The memory order of this atomic operation must be <code>memory_order::relaxed</code>. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_extended_atomics</code>.</td>
</tr>
<tr>
<td><code>T fetch_max(T operand, memory_order memoryOrder = memory_order::relaxed)</code></td>
<td>Deprecated in SYCL 2020. Available only when: <code>T != float</code>. Atomically computes the maximum of the value <code>operand</code> and the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic and assigns the result to the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic. Returns the value at the address of the <code>multi_ptr</code> associated with this SYCL atomic before the call. The memory order of this atomic operation must be <code>memory_order::relaxed</code>. This function is only supported for 64-bit data types on devices that have <code>aspect::int64_extended_atomics</code>.</td>
</tr>
</tbody>
</table>
### Functions

<table>
<thead>
<tr>
<th>Description</th>
<th>Syntax</th>
</tr>
</thead>
<tbody>
<tr>
<td>Deprecated in SYCL 2020. Equivalent to calling object.load(memoryOrder).</td>
<td>template &lt;typename T, access::address_space addressSpace&gt; T atomic_load(atomic&lt;T, addressSpace&gt; object, memory_order memoryOrder = memory_order::relaxed)</td>
</tr>
<tr>
<td>Deprecated in SYCL 2020. Equivalent to calling object.store(operand, memoryOrder).</td>
<td>template &lt;typename T, access::address_space addressSpace&gt; void atomic_store(atomic&lt;T, addressSpace&gt; object, T operand, memory_order memoryOrder = memory_order::relaxed)</td>
</tr>
<tr>
<td>Deprecated in SYCL 2020. Equivalent to calling object.exchange(operand, memoryOrder).</td>
<td>template &lt;typename T, access::address_space addressSpace&gt; T atomic_exchange(atomic&lt;T, addressSpace&gt; object, T operand, memory_order memoryOrder = memory_order::relaxed)</td>
</tr>
<tr>
<td>Deprecated in SYCL 2020. Equivalent to calling object.compare_exchange_strong(expected, desired, successMemoryOrder, failMemoryOrder).</td>
<td>template &lt;typename T, access::address_space addressSpace&gt; bool atomic_compare_exchange_strong(atomic&lt;T, addressSpace&gt; object, T &amp;expected, T desired, memory_order successMemoryOrder = memory_order::relaxed, memory_order failMemoryOrder = memory_order::relaxed)</td>
</tr>
<tr>
<td>Deprecated in SYCL 2020. Equivalent to calling object.fetch_add(operand, memoryOrder).</td>
<td>template &lt;typename T, access::address_space addressSpace&gt; T atomic_fetch_add(atomic&lt;T, addressSpace&gt; object, T operand, memory_order memoryOrder = memory_order::relaxed)</td>
</tr>
<tr>
<td>Deprecated in SYCL 2020. Equivalent to calling object.fetch_sub(operand, memoryOrder).</td>
<td>template &lt;typename T, access::address_space addressSpace&gt; T atomic_fetch_sub(atomic&lt;T, addressSpace&gt; object, T operand, memory_order memoryOrder = memory_order::relaxed)</td>
</tr>
<tr>
<td>Deprecated in SYCL 2020. Equivalent to calling object.fetch_add(operand, memoryOrder).</td>
<td>template &lt;typename T, access::address_space addressSpace&gt; T atomic_fetch_and(atomic&lt;T&gt; operand, T object, memory_order memoryOrder = memory_order::relaxed)</td>
</tr>
</tbody>
</table>

Table 4.122: Global functions available on atomic types.
### Functions

<table>
<thead>
<tr>
<th>Functions</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename T, access::address_space addressSpace&gt; T atomic_fetch_or(atomic&lt;T, addressSpace&gt; object, T operand,)</code></td>
<td>Deprecated in SYCL 2020. Equivalent to calling <code>object.fetch_or(operand, memoryOrder)</code>.</td>
</tr>
<tr>
<td><code>template &lt;typename T, access::address_space addressSpace&gt; T atomic_fetch_xor(atomic&lt;T, addressSpace&gt; object, T operand,)</code></td>
<td>Deprecated in SYCL 2020. Equivalent to calling <code>object.fetch_xor(operand, memoryOrder)</code>.</td>
</tr>
<tr>
<td><code>template &lt;typename T, access::address_space addressSpace&gt; T atomic_fetch_min(atomic&lt;T, addressSpace&gt; object, T operand,)</code></td>
<td>Deprecated in SYCL 2020. Equivalent to calling <code>object.fetch_min(operand, memoryOrder)</code>.</td>
</tr>
<tr>
<td><code>template &lt;typename T, access::address_space addressSpace&gt; T atomic_fetch_max(atomic&lt;T, addressSpace&gt; object, T operand,)</code></td>
<td>Deprecated in SYCL 2020. Equivalent to calling <code>object.fetch_max(operand, memoryOrder)</code>.</td>
</tr>
</tbody>
</table>

End of table

Table 4.122: Global functions available on atomic types.

## 4.18 Stream class

The SYCL `stream` class is a buffered output stream that allows outputting the values of built-in, vector and SYCL types to the console. The implementation of how values are streamed to the console is left as an implementation detail.

The way in which values are output by an instance of the SYCL `stream` class can also be altered using a range of manipulators.

There are two limits that are relevant for the `stream` class. The `totalBufferSize` limit specifies the maximum size of the overall character stream that can be output during a kernel invocation, and the `workItemBufferSize` limit specifies the maximum size of the character stream that can be output within a work item before a flush must be performed. Both of these limits are specified in bytes. The `totalBufferSize` limit must be sufficient to contain the characters output by all stream statements during execution of a kernel invocation (the aggregate of outputs from all work items), and the `workItemBufferSize` limit must be sufficient to contain the characters output within a work item between stream flush operations.

If the `totalBufferSize` or `workItemBufferSize` limits are exceeded, it is implementation defined whether the streamed characters exceeding the limit are output, or silently ignored/discarded, and if output it is implementation defined whether those extra characters exceeding the `workItemBufferSize` limit count toward the `totalBufferSize` limit. Regardless of this implementation defined behavior of output exceeding the limits, no
undefined or erroneous behavior is permitted of an implementation when the limits are exceeded. Unused characters within workItemBufferSize (any portion of the workItemBufferSize capacity that has not been used at the time of a stream flush) do not count toward the totalBufferSize limit, in that only characters flushed count toward the totalBufferSize limit.

The SYCL stream class provides the common reference semantics (see Section 4.5.3).

### 4.18.1 Stream class interface

The constructors and member functions of the SYCL stream class are listed in Tables 4.125, 4.126, and 4.127 respectively. The additional common special member functions and common member functions are listed in Tables 4.1 and 4.2, respectively.

The operand types that are supported by the SYCL stream class operator<<() operator are listed in Table 4.123.

The manipulators that are supported by the SYCL stream class operator<<() operator are listed in Table 4.124.

```cpp
namespace sycl {
    enum class stream_manipulator {
        flush,
        dec,
        hex,
        oct,
        noshowbase,
        showbase,
        noshowpos,
        showpos,
        endl,
        fixed,
        scientific,
        hexfloat,
        defaultfloat
    };

    const stream_manipulator flush = stream_manipulator::flush;
    const stream_manipulator dec = stream_manipulator::dec;
    const stream_manipulator hex = stream_manipulator::hex;
    const stream_manipulator oct = stream_manipulator::oct;
    const stream_manipulator noshowbase = stream_manipulator::noshowbase;
    const stream_manipulator showbase = stream_manipulator::showbase;
    const stream_manipulator noshowpos = stream_manipulator::noshowpos;
    const stream_manipulator showpos = stream_manipulator::showpos;
    const stream_manipulator endl = stream_manipulator::endl;
};
```
const stream_manipulator fixed = stream_manipulator::fixed;

const stream_manipulator scientific = stream_manipulator::scientific;

const stream_manipulator hexfloat = stream_manipulator::hexfloat;

const stream_manipulator defaultfloat = stream_manipulator::defaultfloat;

__precision_manipulator__ setprecision(int precision);

__width_manipulator__ setw(int width);

class stream {
    public:
        stream(size_t totalBufferSize, size_t workItemBufferSize, handler& cgh);

        /* -- common interface members -- */

        size_t get_size() const;

        size_t get_work_item_buffer_size() const;

        /* get_max_statement_size() has the same functionality as get_work_item_buffer_size(),
        and is provided for backward compatibility. get_max_statement_size() is a deprecated
        query. */

        size_t get_max_statement_size() const;
    };

    template <typename T>
    const stream& operator<<(const stream& os, const T &rhs);
}; // namespace sycl

<table>
<thead>
<tr>
<th>Stream operand type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>char, signed char, unsigned char, int,</td>
<td>Outputs the value as a stream of characters.</td>
</tr>
<tr>
<td>short, unsigned short, long int,</td>
<td></td>
</tr>
<tr>
<td>long long int</td>
<td></td>
</tr>
<tr>
<td>float, double, half</td>
<td>Outputs the value according to the precision</td>
</tr>
<tr>
<td>of the current statement as a stream of</td>
<td>characters.</td>
</tr>
<tr>
<td>characters.</td>
<td></td>
</tr>
<tr>
<td>char *, const char *</td>
<td>Outputs the string.</td>
</tr>
<tr>
<td>T *, const T *, multi_ptr</td>
<td>Outputs the address of the pointer as a stream</td>
</tr>
<tr>
<td></td>
<td>of characters.</td>
</tr>
<tr>
<td>vec</td>
<td>Outputs the value of each component of the</td>
</tr>
<tr>
<td></td>
<td>vector as a stream of characters.</td>
</tr>
<tr>
<td>id, range, item, nd_item, group,</td>
<td>Outputs the value of each component of each id</td>
</tr>
<tr>
<td>nd_range, h_item</td>
<td>or range as a stream of characters.</td>
</tr>
</tbody>
</table>

Table 4.123: Operand types supported by the stream class.
4.18. STREAM CLASS

### Table 4.124: Manipulators supported by the `stream` class.

<table>
<thead>
<tr>
<th>Stream manipulator</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>flush</code></td>
<td>Triggers a flush operation, which synchronizes the work item stream buffer with the global stream buffer, and then empties the work item stream buffer. After a flush, the full <code>workItemBufferSize</code> is available again for subsequent streaming within the work item.</td>
</tr>
<tr>
<td><code>endl</code></td>
<td>Outputs a new-line character and then triggers a flush operation.</td>
</tr>
<tr>
<td><code>dec</code></td>
<td>Outputs any subsequent values in the current statement in decimal base.</td>
</tr>
<tr>
<td><code>hex</code></td>
<td>Outputs any subsequent values in the current statement in hexadecimal base.</td>
</tr>
<tr>
<td><code>oct</code></td>
<td>Outputs any subsequent values in the current statement in octal base.</td>
</tr>
<tr>
<td><code>noshowbase</code></td>
<td>Outputs any subsequent values without the base prefix.</td>
</tr>
<tr>
<td><code>showbase</code></td>
<td>Outputs any subsequent values with the base prefix.</td>
</tr>
<tr>
<td><code>noshowpos</code></td>
<td>Outputs any subsequent values without a plus sign if the value is positive.</td>
</tr>
<tr>
<td><code>showpos</code></td>
<td>Outputs any subsequent values with a plus sign if the value is positive.</td>
</tr>
<tr>
<td><code>setw(int)</code></td>
<td>Sets the field width of any subsequent values in the current statement.</td>
</tr>
<tr>
<td><code>setprecision(int)</code></td>
<td>Sets the precision of any subsequent values in the current statement.</td>
</tr>
<tr>
<td><code>fixed</code></td>
<td>Outputs any subsequent floating-point values in the current statement in fixed notation.</td>
</tr>
<tr>
<td><code>scientific</code></td>
<td>Outputs any subsequent floating-point values in the current statement in scientific notation.</td>
</tr>
<tr>
<td><code>hexfloat</code></td>
<td>Outputs any subsequent floating-point values in the current statement in hexadecimal notation.</td>
</tr>
<tr>
<td><code>defaultfloat</code></td>
<td>Outputs any subsequent floating-point values in the current statement in the default notation.</td>
</tr>
</tbody>
</table>

End of table
### 4.18. Stream Class

#### 4.18.2 Synchronization

An instance of the SYCL stream class is required to synchronize with the host, and must output everything that is streamed to it via the `operator<<()` operator before a flush operation (that doesn’t exceed the `workItemBufferSize` or `totalBufferSize` limits) within a SYCL kernel function by the time that the event associated with a command group submission enters the completed state. The point at which this synchronization occurs and the member function by which this synchronization is performed are implementation defined. For example it is valid for an implementation to use `printf()`.

The SYCL stream class is required to output the content of each stream, between flushes (up to `workItemBufferSize`), without mixing with content from the same stream in other work items. There are no other output order guarantees between work items or between streams. The stream flush operation therefore delimits the unit of output that is guaranteed to be displayed without mixing with other work items, with respect to a single stream.

---

**Table 4.125: Constructors of the stream class.**

<table>
<thead>
<tr>
<th>Constructor</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>stream(size_t totalBufferSize, size_t workItemBufferSize, handler&amp; cgh)</code></td>
<td>Constructs a SYCL stream instance associated with the command group specified by cgh, with a maximum buffer size in bytes per kernel invocation specified by the parameter <code>totalBufferSize</code>, and a maximum stream size that can be buffered by a work item between stream flushes specified by the parameter <code>workItemBufferSize</code>.</td>
</tr>
</tbody>
</table>

**Table 4.126: Member functions of the stream class.**

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>size_t get_size() const</code></td>
<td>Returns the total buffer size, in bytes.</td>
</tr>
<tr>
<td><code>size_t get_work_item_buffer_size() const</code></td>
<td>Returns the buffer size per work item, in bytes.</td>
</tr>
<tr>
<td><code>size_t get_max_statement_size() const</code></td>
<td>Deprecated query with same functionality as <code>get_work_item_buffer_size()</code>.</td>
</tr>
</tbody>
</table>

**Table 4.127: Global functions of the stream class.**

<table>
<thead>
<tr>
<th>Global function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename T&gt; const stream&amp; operator&lt;&lt;(const stream&amp; os, const T &amp;rhs)</code></td>
<td>Outputs any valid values (see 4.123) as a stream of characters and applies any valid manipulator (see 4.124) to the current stream.</td>
</tr>
</tbody>
</table>
4.19.3 Implicit flush

There is guaranteed to be an implicit flush of each stream used by a kernel, at the end of kernel execution, from the perspective of each work item. There is also an implicit flush when the endl stream manipulator is executed. No other implicit flushes are permitted in an implementation.

4.18.4 Performance note

The usage of the stream class is designed for debugging purposes and is therefore not recommended for performance critical applications.

4.19 SYCL built-in functions for SYCL host and device

SYCL kernels may execute on any SYCL device, which requires the functions used in the kernels to be compiled and linked for both device and host. In the SYCL programming model, the built-ins are available for the entire SYCL application within the sycl namespace, although their semantics may be different. This section follows the OpenCL 1.2 specification document [1, ch. 6.12] - except that for SYCL, all functions are located within the sycl namespace - and describes the behavior of these functions for SYCL host and device. The expected precision and any other semantic requirements are defined in the backend specification.

The SYCL built-in functions are available throughout the SYCL application, and depending on where they execute, they are either implemented using their host implementation or the device implementation. The SYCL system guarantees that all of the built-in functions fulfill the same requirements for both host and device.

4.19.1 Description of the built-in types available for SYCL host and device

All of the OpenCL built-in types are available in the namespace sycl. For the purposes of this document we use generic type names for describing sets of valid SYCL types. The generic type names themselves are not valid SYCL types, but they represent a set of valid types, as defined in Tables 4.128. Each generic type within a section is comprised of a combination of scalar, SYCL vec and/or marray class specializations. The letters n and N define valid sizes for class specializations, where n means 2,3,4,8,16 and N means any positive value of size t type. Note that any reference to the base type refers to the type of a scalar or the element type of a SYCL vec or marray specialization.

<table>
<thead>
<tr>
<th>Generic type name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>floatn</td>
<td>floatn, mfloatn, marray&lt;N,float&gt;</td>
</tr>
<tr>
<td>genfloatf</td>
<td>float, floatn</td>
</tr>
<tr>
<td>doublen</td>
<td>doublen, mdouble, marray&lt;N,double&gt;</td>
</tr>
<tr>
<td>genfloatd</td>
<td>double, doublen</td>
</tr>
<tr>
<td>halfn</td>
<td>halfn, mhalfn, marray&lt;N,half&gt;</td>
</tr>
<tr>
<td>genfloath</td>
<td>half, halfn</td>
</tr>
<tr>
<td>genfloat</td>
<td>genfloatf, genfloatd, genfloath</td>
</tr>
<tr>
<td>sgenfloat</td>
<td>float, double, half</td>
</tr>
<tr>
<td>gengeofloat</td>
<td>float, float2, float3, float4, mfloat2, mfloat3, mfloat4</td>
</tr>
</tbody>
</table>

Table 4.128: Generic type name description, which serves as a description for all valid types of parameters to kernel functions [1].
<table>
<thead>
<tr>
<th>Generic type name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>gengeodouble</td>
<td>double, double2, double3, double4, mdouble2, mdouble3, mdouble4</td>
</tr>
<tr>
<td>charn</td>
<td>charn, mcharn, marray&lt;N,char&gt;</td>
</tr>
<tr>
<td>scharn</td>
<td>scharn, mscharn, marray&lt;N,signed char&gt;</td>
</tr>
<tr>
<td>ucharn</td>
<td>ucharn, mucharn, marray&lt;N,unsigned char&gt;</td>
</tr>
<tr>
<td>igenchar</td>
<td>signed char, scharn</td>
</tr>
<tr>
<td>ugenchar</td>
<td>unsigned char, ucharn</td>
</tr>
<tr>
<td>genchar</td>
<td>char, charn, igenchar, ugenchar</td>
</tr>
<tr>
<td>shortn</td>
<td>shortn, mshortn, marray&lt;N,short&gt;</td>
</tr>
<tr>
<td>genshort</td>
<td>short, shortn</td>
</tr>
<tr>
<td>ushortn</td>
<td>ushortn, mushortn, marray&lt;N,unsigned short&gt;</td>
</tr>
<tr>
<td>ugenshort</td>
<td>unsigned short, ushortn</td>
</tr>
<tr>
<td>uintn</td>
<td>uintn, muintn, marray&lt;N,unsigned int&gt;</td>
</tr>
<tr>
<td>ugenint</td>
<td>unsigned int, uintn</td>
</tr>
<tr>
<td>intn</td>
<td>intn, mshortn, marray&lt;N,int&gt;</td>
</tr>
<tr>
<td>genint</td>
<td>int, intn</td>
</tr>
<tr>
<td>ulongn</td>
<td>ulongn, mulongn, marray&lt;N,unsigned long int&gt;</td>
</tr>
<tr>
<td>ugenlong</td>
<td>unsigned long int, ulongn</td>
</tr>
<tr>
<td>longn</td>
<td>longn, mlongn, marray&lt;N,long int&gt;</td>
</tr>
<tr>
<td>genlong</td>
<td>long int, longn</td>
</tr>
<tr>
<td>ulonglongn</td>
<td>ulonglongn, mulonglongn, marray&lt;N,unsigned long long int&gt;</td>
</tr>
<tr>
<td>ugenlonglong</td>
<td>unsigned long long int, ulonglongn</td>
</tr>
<tr>
<td>longlongn</td>
<td>longlongn, mlonglongn, marray&lt;N,long long int&gt;</td>
</tr>
<tr>
<td>genlonglong</td>
<td>long long int, longlongn</td>
</tr>
<tr>
<td>igenousinteger</td>
<td>genlong, genlonglong</td>
</tr>
<tr>
<td>uigenousinteger</td>
<td>ugenlong, ugenlonglong</td>
</tr>
<tr>
<td>geninteger</td>
<td>genchar, genshort, ugenshort, genint, ugenint, igenlonginteger, uigenousinteger</td>
</tr>
<tr>
<td>genintegerNbit</td>
<td>All types within geninteger whose base type are N bits in size, where N = 8, 16, 32, 64.</td>
</tr>
<tr>
<td>igeninteger</td>
<td>igenchar, genshort, genint, igenlonginteger</td>
</tr>
<tr>
<td>igenintegerNbit</td>
<td>All types within igeninteger whose base type are N bits in size, where N = 8, 16, 32, 64.</td>
</tr>
<tr>
<td>uigenousinteger</td>
<td>ugenchar, ugenshort, ugenint, uigenousinteger</td>
</tr>
</tbody>
</table>

Table 4.128: Generic type name description, which serves as a description for all valid types of parameters to kernel functions [1].
### Generic type name 

| Description | 
|-----------------|-----------------|
| All types within `ugeninteger` whose base type are N bits in size, where N = 8, 16, 32, 64. | 
| `ugenintegerNbit` | 
| `sgeninteger` | `char, signed char, unsigned char, short, unsigned short, int, unsigned int, long int, unsigned long int, long long int, unsigned long long int` |
| `gentype` | `genfloat, geninteger` |
| `genfloatptr` | All permutations of `multi_ptr<dataT, addressSpace, IsDecorated>` where `dataT` is all types within `genfloat`, `addressSpace` is `access::address_space::global_space, access::address_space::local_space` and `IsDecorated` is `access::decorated::yes and access::decorated::no`. |
| `genintptr` | All permutations of `multi_ptr<dataT, addressSpace, IsDecorated>` where `dataT` is all types within `genint`, `addressSpace` is `access::address_space::global_space, access::address_space::local_space` and `IsDecorated` is `access::decorated::yes and access::decorated::no`. |
| `booln` | `marray<N, bool>` |
| `genbool` | `bool, booln` |

Table 4.128: Generic type name description, which serves as a description for all valid types of parameters to kernel functions [1].

### 4.19.2 Work-item functions

In the OpenCL 1.2 specification document [1, ch. 6.12.1] in Table 6.7 the work-item functions are defined where they provide the size of the enqueued kernel NDRange. These functions are available in SYCL through the item and group classes see sections 4.10.1.4, 4.10.1.5 and 4.10.1.7.

### 4.19.3 Function objects

SYCL provides a number of function objects in the `sycl` namespace on host and device. All function objects obey C++ conversion and promotion rules. Each function object is additionally specialized for `void` as a `transparent` function object that deduces its parameter types and return type.

SYCL function objects can be identified using the `sycl::is_native_function_object` and `sycl::is_native_function_object_v` traits classes.
namespace sycl {

  template<typename T=void>
  struct plus {
    T operator()(const T& x, const T& y) const;
  };

  template<typename T=void>
  struct multiplies {
    T operator()(const T& x, const T& y) const;
  };

  template<typename T=void>
  struct bit_and {
    T operator()(const T& x, const T& y) const;
  };

  template<typename T=void>
  struct bit_or {
    T operator()(const T& x, const T& y) const;
  };

  template<typename T=void>
  struct bit_xor {
    T operator()(const T& x, const T& y) const;
  };

  template<typename T=void>
  struct logical_and {
    T operator()(const T& x, const T& y) const;
  };

  template<typename T=void>
  struct logical_or {
    T operator()(const T& x, const T& y) const;
  };

  template<typename T=void>
  struct minimum {
    T operator()(const T& x, const T& y) const;
  };

  template<typename T=void>
  struct maximum {
    T operator()(const T& x, const T& y) const;
  }
}

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>T operator()(const T&amp; x, const T&amp; y)</td>
<td>Returns the sum of its arguments, equivalent to ( x + y ).</td>
</tr>
</tbody>
</table>

Table 4.129: Member functions for the `plus` function object.
### Table 4.130: Member functions for the `multiplies` function object.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T operator()(const T&amp; x, const T&amp; y)const</code></td>
<td>Returns the product of its arguments, equivalent to <code>x * y</code>.</td>
</tr>
</tbody>
</table>

### Table 4.131: Member functions for the `bit_and` function object.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T operator()(const T&amp; x, const T&amp; y)const</code></td>
<td>Returns the bitwise AND of its arguments, equivalent to <code>x &amp; y</code>.</td>
</tr>
</tbody>
</table>

### Table 4.132: Member functions for the `bit_or` function object.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T operator()(const T&amp; x, const T&amp; y)const</code></td>
<td>Returns the bitwise OR of its arguments, equivalent to `x</td>
</tr>
</tbody>
</table>

### Table 4.133: Member functions for the `bit_xor` function object.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T operator()(const T&amp; x, const T&amp; y)const</code></td>
<td>Returns the bitwise XOR of its arguments, equivalent to <code>x ^ y</code>.</td>
</tr>
</tbody>
</table>

### Table 4.134: Member functions for the `logical_and` function object.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T operator()(const T&amp; x, const T&amp; y)const</code></td>
<td>Returns the logical AND of its arguments, equivalent to <code>x &amp;&amp; y</code>.</td>
</tr>
</tbody>
</table>

### Table 4.135: Member functions for the `logical_or` function object.

<table>
<thead>
<tr>
<th>Member function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>T operator()(const T&amp; x, const T&amp; y)const</code></td>
<td>Returns the logical OR of its arguments, equivalent to `x</td>
</tr>
</tbody>
</table>


### Algorithms library

SYCL provides an algorithms library based on the functions described in Section 28 of the C++17 specification. The first argument to each function is an execution policy, and data ranges are described using instances of `multi_ptr` (in place of more general iterators) in order to guarantee that address space information is visible to the compiler. The functions defined in this section are free functions available in the `sycl` namespace.

Any restrictions from the standard algorithms library apply. Some of the functions in the SYCL algorithms library introduce additional restrictions in order to maximize portability across different devices and to minimize the chances of encountering unexpected behavior.

All algorithms are supported for the fundamental scalar types supported by SYCL (see Table 5.1) and instances of the SYCL `vec` and `marray` classes. Functions with arguments of type `vec<T,N>` or `marray<T,N>` are applied component-wise and are semantically equivalent to \( N \) calls to a scalar algorithm with type `T`.

The execution policy `sycl::execution::group` denotes that an algorithm should be performed collaboratively by the work-items in the specified `group`. All algorithms using this execution policy therefore act as group functions (as defined in Section 4.19.5), inheriting all restrictions of group functions. Unless the description of a function says otherwise, how the elements of a range are processed by the work-items in a group is undefined.

#### 4.19.4.1 any_of, all_of and none_of

The `any_of`, `all_of` and `none_of` functions test whether Boolean conditions hold for any of, all of or none of the values in a range, respectively.
4.19. SYCL BUILT-IN FUNCTIONS FOR SYCL HOST AND DEVICE

4.19.4.2 reduce

The `reduce` function combines values in an unspecified order using a binary operator. The result of a call to `reduce` is non-deterministic if the binary operator is not commutative and associative. Only the binary operators defined in Section 4.19.3 are supported by `reduce` in SYCL 2020, but the standard C++ syntax is used for forward compatibility with future SYCL versions.
4.19.4.3 **exclusive_scan** and **inclusive_scan**

The scan functions compute a generalized prefix sum using a binary operator. The result of a call to a scan is non-deterministic if the binary operator is not associative. Only the binary operators defined in Section 4.19.3 are supported by the scan functions in SYCL 2020, but the standard C++ syntax is used for forward compatibility with future SYCL versions.

A scan operation can be exclusive or inclusive. For a scan of elements \([x_0, ..., x_n]\), the \(i\)th result in an exclusive scan excludes \(x_i\), whereas the \(i\)th result in an inclusive scan includes \(x_i\).
### Function and Description

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
</table>
| template <typename ExecutionPolicy, typename InPtr, typename OutPtr, typename BinaryOperation>  
  OutPtr exclusive_scan(ExecutionPolicy&& policy, InPtr first, InPtr last, OutPtr result, BinaryOperation binary_op) | Perform an exclusive scan over the values in the range \([first, last]\) using the operator \(binary\_op\) which must be an instance of a SYCL function object. \(binary\_op(*first, *first)\) must return a value of type \(OutPtr::value\_type\). The value written to \(result + i\) is the exclusive scan of the first \(i\) values in the range and the identity value of \(binary\_op\). Returns a pointer to the end of the output range. If \(policy\) is \(sycl::execution::group\), \(first\), \(last\), \(result\) and the type of \(binary\_op\) must be the same for all work-items in the group. |
| template <typename ExecutionPolicy, typename InPtr, typename OutPtr, typename T, typename BinaryOperation>  
  OutPtr exclusive_scan(ExecutionPolicy&& policy, InPtr first, InPtr last, OutPtr result, T init, BinaryOperation binary_op) | Perform an exclusive scan over the values in the range \([first, last]\) using the operator \(binary\_op\), which must be an instance of a SYCL function object. \(binary\_op(init, *first)\) must return a value of type \(T\). The value written to \(result + i\) is the exclusive scan of the first \(i\) values in the range and an initial value specified by \(init\). Returns a pointer to the end of the output range. If \(policy\) is \(sycl::execution::group\), \(first\), \(last\), \(result\), \(init\) and the type of \(binary\_op\) must be the same for all work-items in the group. |

Table 4.142: Overloads of the `exclusive_scan` function.
### 4.19.5 Group functions

SYCL provides a number of functions that expose functionality tied to groups of work-items (such as group barriers and collective operations). These group functions act as synchronization points and must be encountered in converged control flow by all work-items in the group — if one work-item in the group reaches the function, then all work-items in the group must reach the function. Additionally, restrictions may be placed on the arguments passed to each function in order to ensure that all work-items in the group agree on the operation that is being performed. Any such restrictions on the arguments passed to a function are defined within the descriptions of those functions. Violating these restrictions results in undefined behavior.

The functions defined in this section are free functions available in the `sycl` namespace, and they all accept a templated parameter of type `Group`. It is valid to pass an argument of type `group` or of type `sub_group` for this parameter.
All group functions are supported for the fundamental scalar types supported by SYCL (see Table 5.1) and instances of the SYCL `vec` and `marray` classes. Functions with arguments of type `vec<T,N>` or `marray<T,N>` are applied component-wise and are semantically equivalent to \( N \) calls to a scalar function with type \( T \).

Using a group function inside of a kernel may introduce additional limits on the resources available to user code inside the same kernel. The behavior of these limits is implementation-defined, but must be reflected by calls to kernel querying functions (such as `kernel::get_info`) as described in Section 4.12.

It is undefined behavior for any group function to be invoked within a `parallel_for_work_group` or `parallel_for_work_item` context.

### 4.19.5.1 group_broadcast

The `group_broadcast` function communicates a value held by one work-item to all other work-items in the group.

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename Group, typename T&gt; T group_broadcast(Group g, T x)</code></td>
<td>Broadcast the value of ( x ) from the work-item with the smallest linear id to all work-items within the group.</td>
</tr>
</tbody>
</table>
| `template <typename Group, typename T> T group_broadcast(Group g, T x, Group::linear_id_type local_linear_id)` | Broadcast the value of \( x \) from the work-item with the specified linear id to all work-items within the group.  
The value of `local_linear_id` must be the same for all work-items in the group. |
| `template <typename Group, typename T> T group_broadcast(Group g, T x, Group::id_type local_id)` | Broadcast the value of \( x \) from the work-item with the specified id to all work-items within the group.  
The value of `local_id` must be the same for all work-items in the group, and its dimensionality must match the dimensionality of the group. |

Table 4.144: Overloads of the `group_broadcast` function.

### 4.19.5.2 group_barrier

The `group_barrier` function synchronizes all work-items in a group, using a `group barrier`. 

---

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Function | Description
--- | ---
\texttt{template <typename Group> \n    void group\_barrier\(\text{Group}\ g, \text{memory\_scope}\ \fence\_scope = \text{Group::fence\_scope}\)\n} | Synchronizes all work-items in the group. The current work-item will wait at the barrier until all work-items in the group have reached the barrier. In addition, the barrier performs a \texttt{group mem-fence} operation ensuring that all memory accesses issued before the barrier complete before those issued after the barrier: all work-items in the group execute a release fence prior to synchronizing at the barrier, and all work-items in the group execute an acquire fence afterwards.

By default, the scope of these fences is set to the narrowest scope including all work-items in the group (as reported by \texttt{Group::fence\_scope}). This scope may be optionally overridden with a broader scope, specified by the \texttt{fence\_scope} argument.

End of table

Table 4.145: Overloads for the \texttt{group\_barrier} function.

### 4.19.5.3 \texttt{group\_any\_of}, \texttt{group\_all\_of} and \texttt{group\_none\_of}

The \texttt{group\_any\_of}, \texttt{group\_all\_of} and \texttt{group\_none\_of} functions correspond to the \texttt{any\_of}, \texttt{all\_of} and \texttt{none\_of} functions from the algorithm library in Section 4.19.4, respectively. The \texttt{group\_} variants of the functions perform the same operations, but apply directly to values supplied by the work-items in a group instead of a range of values stored in memory.

Function | Description
--- | ---
\texttt{template <typename Group> \n    bool group\_any\_of\(\text{Group}\ g, \text{bool}\ \text{pred}\)\n} | Return true if \texttt{pred} is true for any work-item in the group.

\texttt{template <typename Group, typename T, typename Predicate> \n    bool group\_any\_of\(\text{Group}\ g, T x, \text{Predicate}\ \text{pred}\)\n} | Return true if \texttt{pred}(\texttt{x}) is true for any work-item in the group. \texttt{pred} must be an immutable callable with the same type and state for all work-items in the group.

End of table

Table 4.146: Overloads for the \texttt{group\_any\_of} function.

Function | Description
--- | ---
\texttt{template <typename Group> \n    bool group\_all\_of\(\text{Group}\ g, \text{bool}\ \text{pred}\)\n} | Return true if \texttt{pred} is true for all work-items in the group.

Continued on next page

Table 4.147: Overloads for the \texttt{group\_all\_of} function.
### 4.19. SYCL BUILT-IN FUNCTIONS FOR SYCL HOST AND DEVICE

#### 4.19. SYCL BUILT-IN FUNCTIONS FOR SYCL HOST AND DEVICE

**SYCL 2020 provisional**

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>```template &lt;typename Group, typename T, typename Predicate&gt;</td>
<td>Return true if <code>pred(x)</code> is true for all work-items in the group.</td>
</tr>
<tr>
<td>bool group_all_of(Group g, T x, Predicate pred)</td>
<td><code>pred</code> must be an immutable callable with the same type and state for all work-items in the group.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>```template &lt;typename Group&gt;</td>
<td>Return true if <code>pred</code> is true for no work-item in the group.</td>
</tr>
<tr>
<td>bool group_none_of(Group g, bool pred)</td>
<td><code>pred</code> must be an immutable callable with the same type and state for all work-items in the group.</td>
</tr>
</tbody>
</table>

#### Table 4.147: Overloads for the `group_all_of` function.

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>```template &lt;typename Group, typename T, typename Predicate&gt;</td>
<td>Return true if <code>pred(x)</code> is true for no work-item in the group.</td>
</tr>
<tr>
<td>bool group_none_of(Group g, T x, Predicate pred)</td>
<td><code>pred</code> must be an immutable callable with the same type and state for all work-items in the group.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>```template &lt;typename Group, typename T, typename BinaryOperation&gt;</td>
<td>Combine the values of <code>x</code> from all work-items in the group using the operator <code>binary_op</code>, which must</td>
</tr>
<tr>
<td>T group_reduce(Group g, T x, BinaryOperation binary_op)</td>
<td>be an instance of a SYCL function object. <code>binary_op(x, x)</code> must return a value of type <code>T</code>.</td>
</tr>
<tr>
<td></td>
<td>The type of <code>binary_op</code> must be the same for all work-items in the group.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>```template &lt;typename Group, typename V, typename T, typename BinaryOperation&gt;</td>
<td>Combine the values of <code>x</code> from all work-items in the group using an initial value of <code>init</code> and the</td>
</tr>
<tr>
<td>T group_reduce(Group g, V x, T init, BinaryOperation binary_op)</td>
<td>operator <code>binary_op</code>, which must be an instance of a SYCL function object. <code>binary_op(init, x)</code> must</td>
</tr>
<tr>
<td></td>
<td>be an instance of a SYCL function object. <code>binary_op(init, x)</code> must return a value of type <code>T</code>.</td>
</tr>
<tr>
<td></td>
<td>The type of <code>binary_op</code> must be the same for all work-items in the group.</td>
</tr>
</tbody>
</table>

#### Table 4.148: Overloads for the `group_none_of` function.

#### 4.19.5.4 group_reduce

The `group_reduce` function corresponds to the `reduce` function from the algorithms library in Section 4.19.4. The `group_` variant of the function performs the same operation, but applies directly to values supplied by the work-items in a group instead of a range of values stored in memory.

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>```template &lt;typename Group, typename T, typename BinaryOperation&gt;</td>
<td>Combine the values of <code>x</code> from all work-items in the group using the operator <code>binary_op</code>, which must</td>
</tr>
<tr>
<td>T group_reduce(Group g, T x, BinaryOperation binary_op)</td>
<td>be an instance of a SYCL function object. <code>binary_op(x, x)</code> must return a value of type <code>T</code>.</td>
</tr>
<tr>
<td></td>
<td>The type of <code>binary_op</code> must be the same for all work-items in the group.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>```template &lt;typename Group, typename V, typename T, typename BinaryOperation&gt;</td>
<td>Combine the values of <code>x</code> from all work-items in the group using an initial value of <code>init</code> and the</td>
</tr>
<tr>
<td>T group_reduce(Group g, V x, T init, BinaryOperation binary_op)</td>
<td>operator <code>binary_op</code>, which must be an instance of a SYCL function object. <code>binary_op(init, x)</code> must</td>
</tr>
<tr>
<td></td>
<td>be an instance of a SYCL function object. <code>binary_op(init, x)</code> must return a value of type <code>T</code>.</td>
</tr>
<tr>
<td></td>
<td>The type of <code>binary_op</code> must be the same for all work-items in the group.</td>
</tr>
</tbody>
</table>

#### Table 4.149: Overloads of the `group_reduce` function.
4.19.5.5 group_exclusive_scan and group_inclusive_scan

The `group_exclusive_scan` and `group_inclusive_scan` functions correspond to the `exclusive_scan` and `inclusive_scan` functions from the algorithm library in Section 4.19.4, respectively. The `group_` variants of the functions perform the same operations, but apply directly to values supplied by the work-items in a group instead of a range of values stored in memory.

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename Group, typename T, typename BinaryOperation&gt; T group_exclusive_scan(Group g, T x, BinaryOperation binary_op)</code></td>
<td>Perform an exclusive scan over the values of <code>x</code> from all work-items in the group using the operator <code>binary_op</code>, which must be an instance of a SYCL function object. <code>binary_op(x, x)</code> must return a value of type <code>T</code>. The value returned on work-item <code>i</code> is the exclusive scan of the first <code>i</code> work-items in the group and the identity value of <code>binary_op</code>. For multi-dimensional groups, the order of work-items in the group is determined by their linear id. The type of <code>binary_op</code> must be the same for all work-items in the group.</td>
</tr>
<tr>
<td><code>template &lt;typename Group, typename V, typename T, typename BinaryOperation&gt; T group_exclusive_scan(Group g, V x, T init, BinaryOperation binary_op)</code></td>
<td>Perform an exclusive scan over the values of <code>x</code> from all work-items in the group using the operator <code>binary_op</code>, which must be an instance of a SYCL function object. <code>binary_op(init, x)</code> must return a value of type <code>T</code>. The value returned on work-item <code>i</code> is the exclusive scan of the first <code>i</code> work items in the group and an initial value specified by <code>init</code>. For multi-dimensional groups, the order of work-items in the group is determined by their linear id. <code>init</code> and the type of <code>binary_op</code> must be the same for all work-items in the group.</td>
</tr>
</tbody>
</table>

Table 4.150: Overloads of the `group_exclusive_scan` function.
4.19. SYCL BUILT-IN FUNCTIONS FOR SYCL HOST AND DEVICE

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>template &lt;typename Group, typename T, typename BinaryOperation&gt;</td>
<td>Perform an inclusive scan over the values of x from all work-items in the group using the</td>
</tr>
<tr>
<td></td>
<td>operator binary_op, which must be an instance of a SYCL function object. binary_op(x, x)</td>
</tr>
<tr>
<td></td>
<td>must return a value of type T.</td>
</tr>
<tr>
<td>T group_inclusive_scan(Group g, T x, BinaryOperation binary_op)</td>
<td>The value returned on work-item i is the inclusive scan of the first i work items in the group.</td>
</tr>
<tr>
<td></td>
<td>For multi-dimensional groups, the order of work-items in the group is determined by their</td>
</tr>
<tr>
<td></td>
<td>linear id.</td>
</tr>
<tr>
<td></td>
<td>The type of binary_op must be the same for all work-items in the group.</td>
</tr>
<tr>
<td>template &lt;typename Group, typename V, typename BinaryOperation, typename T&gt;</td>
<td>Perform an inclusive scan over the values of x from all work-items in the group using the</td>
</tr>
<tr>
<td></td>
<td>operator binary_op, which must be an instance of a SYCL function object. binary_op(init, x)</td>
</tr>
<tr>
<td></td>
<td>must return a value of type T.</td>
</tr>
<tr>
<td>T group_inclusive_scan(Group g, V x, BinaryOperation binary_op, T init)</td>
<td>The value returned on work-item i is the inclusive scan of the first i work items in the group</td>
</tr>
<tr>
<td></td>
<td>and an initial value specified by init. For multi-dimensional groups, the order of work-items</td>
</tr>
<tr>
<td></td>
<td>in the group is determined by their linear id.</td>
</tr>
<tr>
<td></td>
<td>init and the type of binary_op must be the same for all work-items in the group.</td>
</tr>
</tbody>
</table>

Table 4.151: Overloads of the `group_inclusive_scan` function.

4.19.6 Math functions

In SYCL the OpenCL math functions are available in the namespace `sycl` on host and device with the same precision guarantees as defined in the OpenCL 1.2 specification document [1, ch. 7] for host and device. For a SYCL platform the numerical requirements for host need to match the numerical requirements of the OpenCL math built-in functions. The built-in functions can take as input float or optionally double and their `vec` and `marray` counterparts, for all supported dimensions including dimension 1.

The built-in functions available for SYCL host and device, with the same precision requirements for both host and device, are described in Table 4.152.
<table>
<thead>
<tr>
<th>Math Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>genfloat acos (genfloat x)</td>
<td>Inverse cosine function.</td>
</tr>
<tr>
<td>genfloat acosh (genfloat x)</td>
<td>Inverse hyperbolic cosine.</td>
</tr>
<tr>
<td>genfloat acospi (genfloat x)</td>
<td>Compute arccos(x)/π</td>
</tr>
<tr>
<td>genfloat asin (genfloat x)</td>
<td>Inverse sine function.</td>
</tr>
<tr>
<td>genfloat asinh (genfloat x)</td>
<td>Inverse hyperbolic sine.</td>
</tr>
<tr>
<td>genfloat asinpi (genfloat x)</td>
<td>Compute arcsin(x)/π</td>
</tr>
<tr>
<td>genfloat atan (genfloat y_over_x)</td>
<td>Inverse tangent function.</td>
</tr>
<tr>
<td>genfloat atan2 (genfloat y, genfloat x)</td>
<td>Compute arctan(y/x).</td>
</tr>
<tr>
<td>genfloat atanh (genfloat x)</td>
<td>Hyperbolic inverse tangent.</td>
</tr>
<tr>
<td>genfloat atanpi (genfloat x)</td>
<td>Compute arctan(x)/π.</td>
</tr>
<tr>
<td>genfloat atan2pi (genfloat y, genfloat x)</td>
<td>Compute arctan2(y, x)/π.</td>
</tr>
<tr>
<td>genfloat cbrt (genfloat x)</td>
<td>Compute cube-root.</td>
</tr>
<tr>
<td>genfloat ceil (genfloat x)</td>
<td>Round to integral value using the round to positive infinity rounding mode.</td>
</tr>
<tr>
<td>genfloat copysign (genfloat x, genfloat y)</td>
<td>Returns x with its sign changed to match the sign of y.</td>
</tr>
<tr>
<td>genfloat cos (genfloat x)</td>
<td>Compute cosine.</td>
</tr>
<tr>
<td>genfloat cosh (genfloat x)</td>
<td>Compute hyperbolic cosine.</td>
</tr>
<tr>
<td>genfloat cospi (genfloat x)</td>
<td>Compute cos(πx).</td>
</tr>
<tr>
<td>genfloat erfc (genfloat x)</td>
<td>Error function encountered in integrating the normal distribution.</td>
</tr>
<tr>
<td>genfloat erf (genfloat x)</td>
<td>Error function encountered in integrating the normal distribution.</td>
</tr>
<tr>
<td>genfloat exp (genfloat x)</td>
<td>Compute the base-e exponential of x.</td>
</tr>
<tr>
<td>genfloat exp2 (genfloat x)</td>
<td>Exponential base 2 function.</td>
</tr>
<tr>
<td>genfloat exp10 (genfloat x)</td>
<td>Exponential base 10 function.</td>
</tr>
<tr>
<td>genfloat expm1 (genfloat x)</td>
<td>Compute e^x – 1.0.</td>
</tr>
<tr>
<td>genfloat fabs (genfloat x)</td>
<td>Compute absolute value of a floating-point number.</td>
</tr>
<tr>
<td>genfloat fdim (genfloat x, genfloat y)</td>
<td>x – y if x &gt; y, +0 if x is less than or equal to y.</td>
</tr>
<tr>
<td>genfloat floor (genfloat x)</td>
<td>Round to integral value using the round to negative infinity rounding mode.</td>
</tr>
<tr>
<td>genfloat fma (genfloat a, genfloat b, genfloat c)</td>
<td>Returns the correctly rounded floating-point representation of the sum of c with the infinitely precise product of a and b. Rounding of intermediate products shall not occur. Edge case behavior is per the IEEE 754-2008 standard.</td>
</tr>
<tr>
<td>genfloat fmax (genfloat x, genfloat y)</td>
<td>Returns y if x &lt; y, otherwise it returns x. If one argument is a NaN, fmax() returns the other argument. If both arguments are NaNs, fmax() returns a NaN.</td>
</tr>
<tr>
<td>genfloat fmax (genfloat x, sgenfloat y)</td>
<td>Returns y if x &lt; y, otherwise it returns x. If one argument is a NaN, fmax() returns the other argument. If both arguments are NaNs, fmax() returns a NaN.</td>
</tr>
<tr>
<td>genfloat fmin (genfloat x, genfloat y)</td>
<td>Returns y if y &lt; x, otherwise it returns x. If one argument is a NaN, fmin() returns the other argument. If both arguments are NaNs, fmin() returns a NaN.</td>
</tr>
<tr>
<td>genfloat fmin (genfloat x, sgenfloat y)</td>
<td>Returns y if y &lt; x, otherwise it returns x. If one argument is a NaN, fmin() returns the other argument. If both arguments are NaNs, fmin() returns a NaN.</td>
</tr>
<tr>
<td>genfloat fmod (genfloat x, genfloat y)</td>
<td>Modulus. Returns x–y · trunc(x/y).</td>
</tr>
</tbody>
</table>

Table 4.152: Math functions which work on SYCL host and device. They correspond to Table 6.8 of the OpenCL 1.2 specification [1].
### Math Function

<table>
<thead>
<tr>
<th><strong>Math Function</strong></th>
<th><strong>Description</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td><code>genfloat fract (genfloat x, genfloatptr iptr)</code></td>
<td>Returns ( \text{fmin}(x - \text{floor}(x), \text{nextafter}(\text{genfloat}(1.0), \text{genfloat}(0.0))) ). ( \text{floor}(x) ) is returned in ( \text{iptr} ).</td>
</tr>
<tr>
<td><code>genfloat frexp (genfloat x, genintptr exp)</code></td>
<td>Extract mantissa and exponent from ( x ). For each component the mantissa returned is a float with magnitude in the interval ([1/2, 1)) or 0. Each component of ( x ) equals mantissa returned ( \times 2^{\text{exp}} ).</td>
</tr>
<tr>
<td><code>genfloat hypot (genfloat x, genfloat y)</code></td>
<td>Compute the value of the square root of ( x^2 + y^2 ) without undue overflow or underflow.</td>
</tr>
<tr>
<td><code>genint ilogb (genfloat x)</code></td>
<td>Return the exponent as an integer value.</td>
</tr>
<tr>
<td><code>genfloat ldexp (genfloat x, genint k)</code></td>
<td>Multiply ( x ) by 2 to the power ( k ).</td>
</tr>
<tr>
<td><code>genfloat lgamma (genfloat x)</code></td>
<td>Log gamma function. Returns the natural logarithm of the absolute value of the gamma function.</td>
</tr>
<tr>
<td><code>genfloat lgamma_r (genfloat x, genintptr signp)</code></td>
<td>Log gamma function. Returns the natural logarithm of the absolute value of the gamma function. The sign of the gamma function is returned in the signp argument of ( lgamma_r ).</td>
</tr>
<tr>
<td><code>genfloat log (genfloat x)</code></td>
<td>Compute natural logarithm.</td>
</tr>
<tr>
<td><code>genfloat log2 (genfloat x)</code></td>
<td>Compute a base 2 logarithm.</td>
</tr>
<tr>
<td><code>genfloat log10 (genfloat x)</code></td>
<td>Compute a base 10 logarithm.</td>
</tr>
<tr>
<td><code>genfloat log1p (genfloat x)</code></td>
<td>Compute ( \log_e (1.0 + x) ).</td>
</tr>
<tr>
<td><code>genfloat logb (genfloat x)</code></td>
<td>Compute the exponent of ( x ), which is the integral part of ( \log_r (</td>
</tr>
<tr>
<td><code>genfloat mad (genfloat a,genfloat b, genfloat c)</code></td>
<td>mad approximates ( a \times b + c ). Whether or how the product of ( a \times b ) is rounded and how supernormal or subnormal intermediate products are handled is not defined. mad is intended to be used where speed is preferred over accuracy.</td>
</tr>
<tr>
<td><code>genfloat maxmag (genfloat x, genfloat y)</code></td>
<td>Returns ( x ) if (</td>
</tr>
<tr>
<td><code>genfloat minmag (genfloat x, genfloat y)</code></td>
<td>Returns ( x ) if (</td>
</tr>
<tr>
<td><code>genfloat modf (genfloat x, genfloatptr iptr)</code></td>
<td>Decompose a floating-point number. The modf function breaks the argument ( x ) into integral and fractional parts, each of which has the same sign as the argument. It stores the integral part in the object pointed to by ( \text{iptr} ).</td>
</tr>
<tr>
<td><code>genfloat nan (ugenint nancode)</code></td>
<td>Returns a quiet NaN. The nancode may be placed in the significand of the resulting NaN.</td>
</tr>
</tbody>
</table>

Table 4.152: Math functions which work on SYCL host and device. They correspond to Table 6.8 of the OpenCL 1.2 specification [1].
<table>
<thead>
<tr>
<th>Math Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>genfloat nextafter (genfloat x, genfloat y)</td>
<td>Computes the next representable single-precision floating-point value following x in the direction of y. Thus, if y is less than x, nextafter() returns the largest representable floating-point number less than x.</td>
</tr>
<tr>
<td>genfloat pow (genfloat x, genfloat y)</td>
<td>Compute x to the power y.</td>
</tr>
<tr>
<td>genfloat pown (genfloat x, genint y)</td>
<td>Compute x to the power y, where y is an integer.</td>
</tr>
<tr>
<td>genfloat powr (genfloat x, genfloat y)</td>
<td>Compute the value r such that r = x - n*y, where n is the integer nearest the exact value of x/y. If there are two integers closest to x/y, n shall be the even one. If r is zero, it is given the same sign as x.</td>
</tr>
<tr>
<td>genfloat remainder (genfloat x, genfloat y)</td>
<td>Compute the value r such that r = x - n*y, where n is the integer nearest the exact value of x/y. If there are two integers closest to x/y, n shall be the even one. If r is zero, it is given the same sign as x.</td>
</tr>
<tr>
<td>genfloat remquo (genfloat x, genfloat y, genintptr quo)</td>
<td>The remquo function computes the value r such that r = x - k*y, where k is the integer nearest the exact value of x/y. If there are two integers closest to x/y, k shall be the even one. If r is zero, it is given the same sign as x. This is the same value that is returned by the remainder function. remquo also calculates the lower seven bits of the integral quotient x/y, and gives that value the same sign as x/y. It stores this signed value in the object pointed to by quo.</td>
</tr>
<tr>
<td>genfloat rint (genfloat x)</td>
<td>Round to integral value (using round to nearest even rounding mode) in floating-point format. Refer to section 7.1 of the OpenCL 1.2 specification document [1] for description of rounding modes.</td>
</tr>
<tr>
<td>genfloat rootn (genfloat x, genint y)</td>
<td>Compute x to the power 1/y.</td>
</tr>
<tr>
<td>genfloat round (genfloat x)</td>
<td>Return the integral value nearest to x rounding halfway cases away from zero, regardless of the current rounding direction.</td>
</tr>
<tr>
<td>genfloat rsqrt (genfloat x)</td>
<td>Compute inverse square root.</td>
</tr>
<tr>
<td>genfloat sin (genfloat x)</td>
<td>Compute sine.</td>
</tr>
<tr>
<td>genfloat sincos (genfloat x, genfloatptr cosval)</td>
<td>Compute sine and cosine of x. The computed sine is the return value and computed cosine is returned in cosval.</td>
</tr>
<tr>
<td>genfloat sinh (genfloat x)</td>
<td>Compute hyperbolic sine.</td>
</tr>
<tr>
<td>genfloat sinpi (genfloat x)</td>
<td>Compute sin (π x).</td>
</tr>
<tr>
<td>genfloat sqrt (genfloat x)</td>
<td>Compute square root.</td>
</tr>
<tr>
<td>genfloat tan (genfloat x)</td>
<td>Compute tangent.</td>
</tr>
<tr>
<td>genfloat tanh (genfloat x)</td>
<td>Compute hyperbolic tangent.</td>
</tr>
<tr>
<td>genfloat tanpi (genfloat x)</td>
<td>Compute tan (π x).</td>
</tr>
<tr>
<td>genfloat tgamma (genfloat x)</td>
<td>Compute the gamma function.</td>
</tr>
</tbody>
</table>

Table 4.152: Math functions which work on SYCL host and device. They correspond to Table 6.8 of the OpenCL 1.2 specification [1].

CHAPTER 4. SYCL PROGRAMMING INTERFACE
### Math Function | Description
--- | ---
`genfloat trunc (genfloat x)` | Round to integral value using the round to zero rounding mode.

End of table

Table 4.152: Math functions which work on SYCL host and device. They correspond to Table 6.8 of the OpenCL 1.2 specification [1].

In SYCL the implementation defined precision math functions are defined in the namespace `sycl::native`. The functions that are available within this namespace are specified in Tables 4.153.

<table>
<thead>
<tr>
<th>Native Math Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>genfloatf cos (genfloatf x)</code></td>
<td>Compute cosine over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf divide (genfloatf x, genfloatf y)</code></td>
<td>Compute x / y over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf exp (genfloatf x)</code></td>
<td>Compute the base- e exponential of x over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf exp2 (genfloatf x)</code></td>
<td>Compute the base- 2 exponential of x over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf exp10 (genfloatf x)</code></td>
<td>Compute the base- 10 exponential of x over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf log (genfloatf x)</code></td>
<td>Compute natural logarithm over an implementation defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf log2 (genfloatf x)</code></td>
<td>Compute a base 2 logarithm over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf log10 (genfloatf x)</code></td>
<td>Compute a base 10 logarithm over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf powr (genfloatf x, genfloatf y)</code></td>
<td>Compute x to the power y, where ( x \geq 0 ). The range of x and y are implementation-defined. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf recip (genfloatf x)</code></td>
<td>Compute reciprocal over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
<tr>
<td><code>genfloatf rsqrt (genfloatf x)</code></td>
<td>Compute inverse square root over an implementation-defined range. The maximum error is implementation-defined.</td>
</tr>
</tbody>
</table>

Continued on next page

Table 4.153: Native math functions.
Native Math Function | Description
--- | ---
genfloatf sin (genfloatf x) | Compute sine over an implementation-defined range. The maximum error is implementation-defined.
genfloatf sqrt (genfloatf x) | Compute square root over an implementation-defined range. The maximum error is implementation-defined.
genfloatf tan (genfloatf x) | Compute tangent over an implementation-defined range. The maximum error is implementation-defined.

End of table

Table 4.153: Native math functions.

In SYCL the half precision math functions are defined in `sycl::half_precision`. The functions that are available within this namespace are specified in Tables 4.154. These functions are implemented with a minimum of 10-bits of accuracy i.e. an ULP value is less than or equal to 8192 ulp.

Half Math function | Description
--- | ---
genfloatf cos (genfloatf x) | Compute cosine. x must be in the range -216 to +216.
genfloatf divide (genfloatf x, genfloatf y) | Compute x / y.
genfloatf exp (genfloatf x) | Compute the base-e exponential of x.
genfloatf exp2 (genfloatf x) | Compute the base-2 exponential of x.
genfloatf exp10 (genfloatf x) | Compute the base-10 exponential of x.
genfloatf log (genfloatf x) | Compute natural logarithm.
genfloatf log2 (genfloatf x) | Compute a base 2 logarithm.
genfloatf log10 (genfloatf x) | Compute a base 10 logarithm.
genfloatf powr (genfloatf x, genfloatf y) | Compute x to the power y, where x >= 0.
genfloatf recip (genfloatf x) | Compute reciprocal.
genfloatf rsqrt (genfloatf x) | Compute inverse square root.
genfloatf sin (genfloatf x) | Compute sine. x must be in the range -216 to +216.
genfloatf sqrt (genfloatf x) | Compute square root.
genfloatf tan (genfloatf x) | Compute tangent. x must be in the range -216 to +216.

End of table

Table 4.154: Half precision math functions.

### 4.19.7 Integer functions

Integer math functions are available in SYCL in the namespace `sycl` on host and device. The built-in functions can take as input `char`, `unsigned char`, `short`, `unsigned short`, `int`, `unsigned int`, `long`, `long int`, `unsigned long`, `long long int` and their `vec` and `marray` counterparts. The supported integer math functions are described in Table 4.155.
### Integer Functions

<table>
<thead>
<tr>
<th>Integer Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ugeninteger abs (geninteger x)</td>
<td>Returns $</td>
</tr>
<tr>
<td>ugeninteger abs_diff (geninteger x, geninteger y)</td>
<td>Returns $</td>
</tr>
<tr>
<td>geninteger add_sat (geninteger x, geninteger y)</td>
<td>Returns $x + y$ and saturates the result.</td>
</tr>
<tr>
<td>geninteger hadd (geninteger x, geninteger y)</td>
<td>Returns $(x+y) &gt;&gt; 1$. The intermediate sum does not modulo overflow.</td>
</tr>
<tr>
<td>geninteger rhadd (geninteger x, geninteger y)</td>
<td>Returns $(x + y + 1) &gt;&gt; 1$. The intermediate sum does not modulo overflow.</td>
</tr>
<tr>
<td>geninteger clamp (geninteger x, geninteger minval, geninteger maxval)</td>
<td>Returns min(max(x, minval), maxval). Results are undefined if minval &gt; maxval.</td>
</tr>
<tr>
<td>geninteger clz (geninteger x)</td>
<td>Returns the number of leading 0-bits in x, starting at the most significant bit position. If x is 0, returns the size in bits of the type of x or component type of x, if x is a vector type.</td>
</tr>
<tr>
<td>geninteger ctz (geninteger x)</td>
<td>Returns the count of trailing 0-bits in x. If x is 0, returns the size in bits of the type of x or component type of x, if x is a vector type.</td>
</tr>
<tr>
<td>geninteger mad_hi (geninteger a, geninteger b, geninteger c)</td>
<td>Returns $\text{mul}_h(a, b)+c$.</td>
</tr>
<tr>
<td>geninteger mad_sat (geninteger a, geninteger b, geninteger c)</td>
<td>Returns $a \times b + c$ and saturates the result.</td>
</tr>
<tr>
<td>geninteger max (geninteger x, geninteger y)</td>
<td>Returns y if $x &lt; y$, otherwise it returns x.</td>
</tr>
<tr>
<td>geninteger min (geninteger x, sgeninteger y)</td>
<td>Returns y if $y &lt; x$, otherwise it returns x.</td>
</tr>
<tr>
<td>geninteger mul_hi (geninteger x, geninteger y)</td>
<td>Computes $x \times y$ and returns the high half of the product of x and y.</td>
</tr>
<tr>
<td>geninteger rotate (geninteger v, geninteger i)</td>
<td>For each element in v, the bits are shifted left by the number of bits given by the corresponding element in i (subject to usual shift modulo rules described in section 6.3). Bits shifted off the left side of the element are shifted back in from the right.</td>
</tr>
<tr>
<td>geninteger sub_sat (geninteger x, geninteger y)</td>
<td>Returns $x - y$ and saturates the result.</td>
</tr>
<tr>
<td>ugeninteger16bit upsample (ugeninteger8bit hi, ugeninteger8bit lo)</td>
<td>$\text{result}[i] = ((\text{ushort})\text{hi}[i] &lt;&lt; 8)</td>
</tr>
<tr>
<td>iegeninteger16bit upsample (igeninteger8bit hi, iegeninteger8bit lo)</td>
<td>$\text{result}[i] = ((\text{short})\text{hi}[i] &lt;&lt; 8)</td>
</tr>
<tr>
<td>ugeninteger32bit upsample (ugeninteger16bit hi, ugeninteger16bit lo)</td>
<td>$\text{result}[i] = ((\text{uint})\text{hi}[i] &lt;&lt; 16)</td>
</tr>
<tr>
<td>iegeninteger32bit upsample (igeninteger16bit hi, iegeninteger16bit lo)</td>
<td>$\text{result}[i] = ((\text{int})\text{hi}[i] &lt;&lt; 16)</td>
</tr>
<tr>
<td>ugeninteger64bit upsample (ugeninteger32bit hi, ugeninteger32bit lo)</td>
<td>$\text{result}[i] = ((\text{ulonglong})\text{hi}[i] &lt;&lt; 32)</td>
</tr>
</tbody>
</table>

Table 4.155: Integer functions which work on SYCL host and device, are available in the sycl namespace.
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### Integer Function

<table>
<thead>
<tr>
<th>Integer Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>igeninteger64bit upsample (igeninteger32bit hi, ugeninteger32bit lo)</code></td>
<td>`result[i] = ((longlong)hi[i] &lt;&lt; 32)</td>
</tr>
<tr>
<td><code>geninteger popcount (geninteger x)</code></td>
<td>Returns the number of non-zero bits in x.</td>
</tr>
<tr>
<td><code>geninteger32bit mad24 (geninteger32bit x, geninteger32bit y, geninteger32bit z)</code></td>
<td>Multiply two 24-bit integer values x and y and add the 32-bit integer result to the 32-bit integer z. Refer to definition of <code>mul24</code> to see how the 24-bit integer multiplication is performed.</td>
</tr>
<tr>
<td><code>geninteger32bit mul24 (geninteger32bit x, geninteger32bit y)</code></td>
<td>Multiply two 24-bit integer values x and y. x and y are 32-bit integers but only the low 24-bits are used to perform the multiplication. <code>mul24</code> should only be used when values in x and y are in the range $[−2^{23}, 2^{23} − 1]$ if x and y are signed integers and in the range $[0, 2^{24} − 1]$ if x and y are unsigned integers. If x and y are not in this range, the multiplication result is implementation-defined.</td>
</tr>
</tbody>
</table>

Table 4.155: Integer functions which work on SYCL host and device, are available in the `sycl` namespace.

### 4.19.8 Common functions

In SYCL the OpenCL common functions are available in the namespace `sycl` on host and device as defined in the OpenCL 1.2 specification document [1, par. 6.12.4]. They are described here in Table 4.156. The built-in functions can take as input `float` or optionally `double` and their `vec` and `marray` counterparts.

<table>
<thead>
<tr>
<th>Common Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>genfloat clamp (genfloat x, genfloat minval, genfloat maxval)</code></td>
<td><code>genfloatf clamp (genfloatf x, float minval, float maxval)</code></td>
</tr>
<tr>
<td><code>genfloat max (genfloat x, genfloat y)</code></td>
<td><code>genfloatf max (genfloatf x, float y)</code></td>
</tr>
<tr>
<td><code>genfloat min (genfloat x, genfloat y)</code></td>
<td><code>genfloatf min (genfloatf x, float y)</code></td>
</tr>
<tr>
<td><code>genfloat mix (genfloat x, genfloat y, genfloat a)</code></td>
<td><code>genfloatf mix (genfloatf x, genfloatf y, float a)</code></td>
</tr>
</tbody>
</table>

Table 4.156: Common functions which work on SYCL host and device, are available in the `sycl` namespace. They correspond to Table 6.12 of the OpenCL 1.2 specification [1].

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### 4.19 SYCL Built-in Functions for SYCL Host and Device

#### 4.19.9 Geometric functions

In SYCL the OpenCL geometric functions are available in the namespace `sycl` on host and device as defined in the OpenCL 1.2 specification document [1, par. 6.12.5]. The built-in functions can take as input float or optionally double and their vec and codeinlinemarray counterparts, for dimensions 2, 3 and 4. On the host the vector types use the `vec` class and on an SYCL device use the corresponding native SYCL backend vector types. All of the geometric functions use round-to-nearest-even rounding mode. Table 4.157 contains the definitions of supported geometric functions.

<table>
<thead>
<tr>
<th>Geometric Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>float4 cross (float4 p0, float4 p1)</td>
<td>Returns the cross product of p0.xyz and p1.xyz. The w component of float4 result returned will be 0.0.</td>
</tr>
<tr>
<td>double4 cross (double4 p0, double4 p1)</td>
<td>Returns the cross product of first 3 components of p0 and p1. The 4th component of result returned will be 0.0.</td>
</tr>
<tr>
<td>float dot (gengeofloat p0, gengeofloat p1)</td>
<td>Compute dot product.</td>
</tr>
</tbody>
</table>

Continued on next page
<table>
<thead>
<tr>
<th>Geometric Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>float distance (gengeofloat p0, gengeofloat p1)</code></td>
<td>Returns the distance between p0 and p1. This is calculated as <code>length(p0 - p1)</code>.</td>
</tr>
<tr>
<td><code>double distance (gengeodouble p0, gengeodouble p1)</code></td>
<td>Returns the length of vector p, i.e., <code>\sqrt{p.x^2 + p.y^2 + ...}</code>.</td>
</tr>
<tr>
<td><code>gengeofloat normalize (gengeofloat p)</code></td>
<td>Returns a vector in the same direction as p but with a length of 1.</td>
</tr>
<tr>
<td><code>gengeodouble normalize (gengeodouble p)</code></td>
<td></td>
</tr>
<tr>
<td><code>float fast_distance (gengeofloat p0, gengeofloat p1)</code></td>
<td>Returns <code>fast_length(p0 - p1)</code>.</td>
</tr>
<tr>
<td><code>float fast_length (gengeofloat p)</code></td>
<td>Returns the length of vector p computed as: <code>sqrt((half)(pow(p.x,2)+ pow(p.y,2) + ...))</code>.</td>
</tr>
<tr>
<td><code>gengeofloat fast_normalize (gengeofloat p)</code></td>
<td>Returns a vector in the same direction as p but with a length of 1. fast_normalize is computed as: <code>p * rsqrt((half)(pow(p.x,2)+ pow(p.y,2)+ ... ))</code>. The result shall be within 8192 ulps error from the infinitely precise result of <code>if (all (p == 0.0f)) result = p; else result = p / sqrt (pow(p.x,2)+ pow(p.y,2)+ ... );</code> with the following exceptions: 1. If the sum of squares is greater than <code>FLT_MAX</code> then the value of the floating-point values in the result vector are undefined. 2. If the sum of squares is less than <code>FLT_MIN</code> then the implementation may return back p. 3. If the device is in “denorms are flushed to zero” mode, individual operand elements with magnitude less than <code>sqrt(FLT_MIN)</code> may be flushed to zero before proceeding with the calculation.</td>
</tr>
</tbody>
</table>

Table 4.157: Geometric functions which work on SYCL host and device, are available in the `sycl` namespace. They correspond to Table 6.13 of the OpenCL 1.2 specification [1].

### 4.19.10 Relational functions

In SYCL the OpenCL `relational` functions are available in the namespace `sycl` on host and device as defined in the OpenCL 1.2 specification document [1, par. 6.12.6]. The built-in functions can take as input `char`, `unsigned char`, `short`, `unsigned short`, `int`, `unsigned int`, `long`, `unsigned long`, `float` or optionally `double` and their `vec` and `marray` counterparts. The relational functions are provided in addition to the the operators.

The available built-in functions for `vec` template class are described in Tables 4.158.
### Relational Function

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>igeninteger32bit isequal (genfloatf x, genfloatf y)</strong></td>
<td>Returns the component-wise compare of ( x == y ).</td>
</tr>
<tr>
<td><strong>igeninteger64bit isequal (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isnotequal (genfloatf x, genfloatf y)</strong></td>
<td>Returns the component-wise compare of ( x! = y ).</td>
</tr>
<tr>
<td><strong>igeninteger64bit isnotequal (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isgreater (genfloatf x, genfloatf y)</strong></td>
<td>Returns the component-wise compare of ( x &gt; y ).</td>
</tr>
<tr>
<td><strong>igeninteger64bit isgreater (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isgreaterequal (genfloatf x, genfloatf y)</strong></td>
<td>Returns the component-wise compare of ( x &gt;= y ).</td>
</tr>
<tr>
<td><strong>igeninteger64bit isgreaterequal (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isless (genfloatf x, genfloatf y)</strong></td>
<td>Returns the component-wise compare of ( x &lt; y ).</td>
</tr>
<tr>
<td><strong>igeninteger64bit isless (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit islessequal (genfloatf x, genfloatf y)</strong></td>
<td>Returns the component-wise compare of ( x &lt;= y ).</td>
</tr>
<tr>
<td><strong>igeninteger64bit islessequal (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit islessgreater (genfloatf x, genfloatf y)</strong></td>
<td>Returns the component-wise compare of ( (x &lt; y)</td>
</tr>
<tr>
<td><strong>igeninteger64bit islessgreater (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isfinite (genfloatf x)</strong></td>
<td>Test for finite value.</td>
</tr>
<tr>
<td><strong>igeninteger64bit isfinite (genfloatd x)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isinf (genfloatf x)</strong></td>
<td>Test for infinity value (positive or negative).</td>
</tr>
<tr>
<td><strong>igeninteger64bit isinf (genfloatd x)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isnan (genfloatf x)</strong></td>
<td>Test for a NaN.</td>
</tr>
<tr>
<td><strong>igeninteger64bit isnan (genfloatd x)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isnormal (genfloatf x)</strong></td>
<td>Test for a normal value.</td>
</tr>
<tr>
<td><strong>igeninteger64bit isnormal (genfloatd x)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isordered (genfloatf x, genfloatf y)</strong></td>
<td>Test if arguments are ordered. isordered() takes arguments x and y, and returns the result isequal(x, x) &amp;&amp; isequal(y, y).</td>
</tr>
<tr>
<td><strong>igeninteger64bit isordered (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit isunordered (genfloatf x, genfloatf y)</strong></td>
<td>Test if arguments are unordered. isunordered() takes arguments x and y, returning non-zero if x or y is NaN, and zero otherwise.</td>
</tr>
<tr>
<td><strong>igeninteger64bit isunordered (genfloatd x, genfloatd y)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>igeninteger32bit signbit (genfloatf x)</strong></td>
<td>Test for sign bit. The scalar version of the function returns a 1 if the sign bit in the float is set else returns 0. The vector version of the function returns the following for each component in floatn: -1 (i.e all bits set) if the sign bit in the float is set else returns 0.</td>
</tr>
<tr>
<td><strong>igeninteger64bit signbit (genfloatd x)</strong></td>
<td></td>
</tr>
</tbody>
</table>

Table 4.158: Relational functions for vec template class which work on SYCL host and device, are available in the sycl namespace. They correspond to Table 6.14 of the OpenCL 1.2 specification [1].
### Relational Function Description

<table>
<thead>
<tr>
<th>Relational Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>int any (igeninteger x)</code></td>
<td>Returns 1 if the most significant bit in any component of x is set; otherwise returns 0.</td>
</tr>
<tr>
<td><code>int all (igeninteger x)</code></td>
<td>Returns 1 if the most significant bit in all components of x is set; otherwise returns 0.</td>
</tr>
<tr>
<td><code>gentype bitselect (gentype a, gentype b, gentype c)</code></td>
<td>Each bit of the result is the corresponding bit of a if the corresponding bit of c is 0. Otherwise it is the corresponding bit of b.</td>
</tr>
<tr>
<td><code>geninteger select (geninteger a, geninteger b, igeninteger c)</code></td>
<td>For each component of a vector type: result[i] = (MSB of c[i] is set)? b[i] : a[i]. For a scalar type: result = c ? b : a. geninteger must have the same number of elements and bits as gentype.</td>
</tr>
</tbody>
</table>

Table 4.158: Relational functions for vec template class which work on SYCL host and device, are available in the sycl namespace. They correspond to Table 6.14 of the OpenCL 1.2 specification [1].

The available built-in functions for marray template class are described in Tables 4.159

<table>
<thead>
<tr>
<th>Relational Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>genbool isequal (genfloatf x, genfloatf y)</code></td>
<td>Returns the component-wise compare of x == y.</td>
</tr>
<tr>
<td><code>genbool isequal (genfloatd x, genfloatd y)</code></td>
<td>Returns the component-wise compare of x == y.</td>
</tr>
<tr>
<td><code>genbool isnotequal (genfloatf x, genfloatf y)</code></td>
<td>Returns the component-wise compare of x != y.</td>
</tr>
<tr>
<td><code>genbool isnotequal (genfloatd x, genfloatd y)</code></td>
<td>Returns the component-wise compare of x != y.</td>
</tr>
<tr>
<td><code>genbool isgreater (genfloatf x, genfloatf y)</code></td>
<td>Returns the component-wise compare of x &gt; y.</td>
</tr>
<tr>
<td><code>genbool isgreater (genfloatd x, genfloatd y)</code></td>
<td>Returns the component-wise compare of x &gt; y.</td>
</tr>
<tr>
<td><code>genbool isgreaterequal (genfloatf x, genfloatf y)</code></td>
<td>Returns the component-wise compare of x &gt;= y.</td>
</tr>
<tr>
<td><code>genbool isgreaterequal (genfloatd x, genfloatd y)</code></td>
<td>Returns the component-wise compare of x &gt;= y.</td>
</tr>
<tr>
<td><code>genbool islesst (genfloatf x, genfloatf y)</code></td>
<td>Returns the component-wise compare of x &lt; y.</td>
</tr>
<tr>
<td><code>genbool isless (genfloatd x, genfloatd y)</code></td>
<td>Returns the component-wise compare of x &lt; y.</td>
</tr>
<tr>
<td><code>genbool islessequall (genfloatf x, genfloatf y)</code></td>
<td>Returns the component-wise compare of x &lt;= y.</td>
</tr>
<tr>
<td><code>genbool islessequal (genfloatd x, genfloatd y)</code></td>
<td>Returns the component-wise compare of x &lt;= y.</td>
</tr>
<tr>
<td><code>genbool islessgreater (genfloatf x, genfloatf y)</code></td>
<td>Returns the component-wise compare of (x &lt; y) &amp;&amp; (x &gt; y).</td>
</tr>
<tr>
<td><code>genbool islessgreater (genfloatd x, genfloatd y)</code></td>
<td>Returns the component-wise compare of (x &lt; y) &amp;&amp; (x &gt; y).</td>
</tr>
<tr>
<td><code>genbool isfinite (genfloatf x)</code></td>
<td>Test for finite value.</td>
</tr>
<tr>
<td><code>genbool isfinite (genfloatd x)</code></td>
<td>Test for finite value.</td>
</tr>
<tr>
<td><code>genbool isinf (genfloatf x)</code></td>
<td>Test for infinity value (positive or negative) .</td>
</tr>
<tr>
<td><code>genbool isinf (genfloatd x)</code></td>
<td>Test for infinity value (positive or negative) .</td>
</tr>
<tr>
<td><code>genbool isnan (genfloatf x)</code></td>
<td>Test for a NaN.</td>
</tr>
<tr>
<td><code>genbool isnan (genfloatd x)</code></td>
<td>Test for a NaN.</td>
</tr>
</tbody>
</table>

Table 4.159: Relational functions for scalar data types and marray template class which work on SYCL host and device, are available in the sycl namespace.
### Relational Functions

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>genbool isnormal (genfloat f x)</td>
<td>Test for a normal value.</td>
</tr>
<tr>
<td>genbool isnormal (genfloat d x)</td>
<td></td>
</tr>
<tr>
<td>genbool isordered (genfloat f x, genfloat f y)</td>
<td>Test if arguments are ordered. isordered() takes arguments x and y, and returns the result isequal(x, x) &amp;&amp; isequal(y, y).</td>
</tr>
<tr>
<td>genbool isordered (genfloat d x, genfloat d y)</td>
<td></td>
</tr>
<tr>
<td>genbool isunordered (genfloat f x, genfloat f y)</td>
<td>Test if arguments are unordered. isunordered() takes arguments x and y, returning true if x or y is NaN, and false otherwise.</td>
</tr>
<tr>
<td>genbool isunordered (genfloat d x, genfloat d y)</td>
<td></td>
</tr>
<tr>
<td>genbool signbit (genfloat f x)</td>
<td>Test for sign bit, returning true if the sign bit in the float is set, and false otherwise.</td>
</tr>
<tr>
<td>genbool signbit (genfloat d x)</td>
<td></td>
</tr>
<tr>
<td>bool any (genbool x)</td>
<td>Returns true if the most significant bit in any component of x is set; otherwise returns false.</td>
</tr>
<tr>
<td>int all (igeninteger x)</td>
<td>Returns true if the most significant bit in all components of x is set; otherwise returns false.</td>
</tr>
<tr>
<td>gentype bitselect (gentype a, gentype b, gentype c)</td>
<td>Each bit of the result is the corresponding bit of a if the corresponding bit of c is 0. Otherwise it is the corresponding bit of b.</td>
</tr>
<tr>
<td>gentype select (gentype a, gentype b, genbool c)</td>
<td>Returns the component-wise result = c ? b : a.</td>
</tr>
</tbody>
</table>

End of table

Table 4.159: Relational functions for scalar data types and marray template class template class which work on SYCL host and device, are available in the sycl namespace.

### 4.19.11 Vector data load and store functions

The functionality from the OpenCL functions as defined in the OpenCL 1.2 specification document [1, par. 6.12.7] is available in SYCL through the vec class in section 4.16.2.

### 4.19.12 Synchronization functions

In SYCL the OpenCL synchronization functions are available through the nd_item class 4.10.1.5, as they are applied to work-items for local or global address spaces. Please see 4.83.

### 4.19.13 printf function

The functionality of the printf function is covered by the stream class 4.18, which has the capability to print to standard output all of the SYCL classes and primitives, and covers the capabilities defined in the OpenCL 1.2 specification document [1, par. 6.12.13].
5. SYCL Device Compiler

This section specifies the requirements of the SYCL device compiler. Most features described in this section relate to underlying SYCL backend capabilities of target devices and limiting the requirements of device code to ensure portability.

5.1 Offline compilation of SYCL source files

There are two alternatives for a SYCL device compiler: a single-source device compiler and a device compiler that supports the technique of SMCP.

A SYCL device compiler takes in a C++ source file, extracts only the SYCL kernels and outputs the device code in a form that can be enqueued from host code by the associated SYCL runtime. How the SYCL runtime invokes the kernels is implementation defined, but a typical approach is for a device compiler to produce a header file with the compiled kernel contained within it. By providing a command-line option to the host compiler, it would cause the implementation’s SYCL header files to include the generated header file. The SYCL specification has been written to allow this as an implementation approach in order to allow SMCP. However, any of the mechanisms needed from the SYCL compiler, the SYCL runtime and build system are implementation defined, as they can vary depending on the platform and approach.

A SYCL single-source device compiler takes in a C++ source file and compiles both host and device code at the same time. This specification specifies how a SYCL single-source device compiler sees and outputs device code for kernels, but does not specify the host compilation.

5.2 Naming of kernels

SYCL kernels are extracted from C++ source files and stored in an implementation-defined format. In the case of the shared-source compilation model, the kernels have to be uniquely identified by both host and device compiler. This is required in order for the host runtime to be able to load the kernel by using the OpenCL host runtime interface.

From this requirement the following rules apply for naming the kernels:

- The kernel name is a C++ typename.
- The kernel name type may not be forward declared other than in namespace scope (including global namespace scope). If it isn’t forward declared but is specified as a template argument in a kernel invoking interface, as described in 4.10.7, then it may not conflict with a name in any enclosing namespace scope.
- If the kernel is defined as a named function object type, the name can be the typename of the function object as long as it is either declared at namespace scope, or does not conflict with any name in an enclosing namespace scope.
- If the kernel is defined as a lambda, a typename can optionally be provided to the kernel invoking interface as described in 4.10.7, so that the developer can control the kernel name for purposes such as debugging or...
referring to the kernel when applying build options.

In both single-source and shared-source implementations, a device compiler should detect the kernel invocations (e.g. \texttt{parallel\_for\langle\texttt{kernelname}\rangle}) in the source code and compile the enclosed kernels, storing them with their associated type name.

The format of the kernel and the compilation techniques are implementation defined. The interface between the compiler and the runtime for extracting and executing SYCL kernels on the device is implementation defined.

## 5.3 Compilation of functions

The SYCL device compiler parses an entire C++ source file supplied by the user, including any header files referenced via \texttt{#include} directives. From this source file, the SYCL device compiler must compile kernels for the device, as well as any functions that the kernels call.

The device compiler identifies kernels by looking for calls to kernel invocation commands such as \texttt{parallel\_for}. One of the parameters is a function object which is known as a SYCL kernel function, and this function must always return \texttt{void}. Any function called by the SYCL kernel function is also compiled for the device, and these functions together with the SYCL kernel functions are known as device functions. The device compiler searches recursively for any functions called from a device function, and these functions are also compiled for the device and known as device functions.

To illustrate, the following source code shows three functions and a kernel invoke with comments explaining which functions need to be compiled for the device.

```cpp
1  void f(handler& cgh) {
2   // Function "f" is not compiled for device
3   cgh.single_task([=] {
4     // This code is compiled for device
5     g(); // This line forces "g" to be compiled for device
6   });
7  }
8
9  void g() {  // Called from kernel, so "g" is compiled for device
10   }
11 }
12
13 void h() {  // Not called from a device function, so not compiled for device
14   }
15 }
```

In order for the SYCL device compiler to correctly compile device functions, all functions in the source file, whether device functions or not, must be syntactically correct functions according to this specification. A syntactically correct function adheres to at least the minimum required C++ version defined in Section 3.8.1.

## 5.4 Language restrictions for device functions

Device functions must abide by certain restrictions. The full set of C++ features are not available to these functions. Following is a list of these restrictions:
• Structures containing pointers may be shared. However, when a pointer is passed between SYCL devices or between the host and a SYCL device, dereferencing that pointer on the device produces undefined behavior unless the device supports USM and the pointer is an address within a USM memory region (see Section 4.8).

• Memory storage allocation is not allowed in kernels. All memory allocation for the device is done on the host using accessor classes or using USM as explained in Section 4.8. Consequently, the default allocation operator new overloads that allocate storage are disallowed in a SYCL kernel. The placement new operator and any user-defined overloads that do not allocate storage are permitted.

• Kernel functions must always have a void return type. A kernel lambda trailing-return-type that is not void is therefore illegal, as is a return statement (that would return from the kernel function) with an expression that does not convert to void.

• The odr-use of polymorphic classes and classes with virtual inheritance is allowed. However, no virtual member functions are allowed to be called in a SYCL kernel or any functions called by the kernel.

• No function pointers or references are allowed to be called in a SYCL kernel or any functions called by the kernel.

• RTTI is disabled inside kernels.

• No variadic functions are allowed to be called in a SYCL kernel or any functions called by the kernel.

• Exception-handling cannot be used inside a SYCL kernel or any code called from the kernel. But of course noexcept is allowed.

• Recursion is not allowed in a SYCL kernel or any code called from the kernel.

• Variables with thread storage duration (thread_local storage class specifier) are not allowed to be odr-used in kernel code.

• Variables with static storage duration that are odr-used inside a kernel must be const or constexpr and zero-initialized or constant-initialized.

• The rules for kernels apply to both the kernel function objects themselves and all functions, operators, member functions, constructors and destructors called by the kernel. This means that kernels can only use library functions that have been adapted to work with SYCL. Implementations are not required to support any library routines in kernels beyond those explicitly mentioned as usable in kernels in this spec. Developers should refer to the SYCL built-in functions in 4.19 to find functions that are specified to be usable in kernels.

• Interacting with a special SYCL runtime class (i.e. SYCL accessor, sampler or stream) that is stored within a C++ union is undefined behavior.

5.5 Built-in scalar data types

In a SYCL device compiler, the device definition of all standard C++ fundamental types from Table 5.1 must match the host definition of those types, in both size and alignment. A device compiler may have this preconfigured so that it can match them based on the definitions of those types on the platform, or there may be a necessity for a device compiler command-line option to ensure the types are the same.
The standard C++ fixed width types, e.g. `int8_t`, `int16_t`, `int32_t`, `int64_t`, should have the same size as defined by the C++ standard for host and device.

<table>
<thead>
<tr>
<th>Fundamental data type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>bool</code></td>
<td>A conditional data type which can be either true or false. The value true expands to the integer constant 1 and the value false expands to the integer constant 0.</td>
</tr>
<tr>
<td><code>char</code></td>
<td>A signed or unsigned 8-bit integer, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>signed char</code></td>
<td>A signed 8-bit integer, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>unsigned char</code></td>
<td>An unsigned 8-bit integer, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>short int</code></td>
<td>A signed integer of at least 16-bits, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>unsigned short int</code></td>
<td>An unsigned integer of at least 16-bits, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>int</code></td>
<td>A signed integer of at least 16-bits, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>unsigned int</code></td>
<td>An unsigned integer of at least 16-bits, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>long int</code></td>
<td>A signed integer of at least 32-bits, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>unsigned long int</code></td>
<td>An unsigned integer of at least 32-bits, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>long long int</code></td>
<td>An integer of at least 64-bits, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>unsigned long long int</code></td>
<td>An unsigned integer of at least 64-bits, as defined by the C++ core language</td>
</tr>
<tr>
<td><code>float</code></td>
<td>A 32-bit floating-point. The float data type must conform to the IEEE 754 single precision storage format.</td>
</tr>
<tr>
<td><code>double</code></td>
<td>A 64-bit floating-point. The double data type must conform to the IEEE 754 double precision storage format.</td>
</tr>
</tbody>
</table>

Table 5.1: Fundamental data types supported by SYCL.

5.6 Preprocessor directives and macros

The standard C++ preprocessing directives and macros are supported. The following preprocessor macros must be defined by all conformant implementations:

- `SYCL_LANGUAGE_VERSION` substitutes an integer reflecting the version number and revision of the SYCL language being supported by the implementation. The version of SYCL defined in this document will have `SYCL_LANGUAGE_VERSION` substitute the integer 202001, composed with the general SYCL version followed by 2 digits representing the revision number;
SYCL 2020 provisional

5.7. KERNEL ATTRIBUTES

- \_SYCL\_DEVICE\_ONLY\_ is defined to 1 if the source file is being compiled with a SYCL device compiler which does not produce host binary;

- \_SYCL\_SINGLE\_SOURCE\_ is defined to 1 if the source file is being compiled with a SYCL single-source compiler which produces host as well as device binary;

- SYCL\_EXTERNAL is an optional macro which enables external linkage of SYCL functions and member functions to be included in a SYCL kernel. The macro is only defined if the implementation supports external linkage. For more details see 5.9.1

In addition, for each SYCL backend supported, the preprocessor macros described in the backend section 4.1 must be defined by all conformant implementations.

5.7 Kernel attributes

The SYCL general programming interface defines attributes that augment the information available while generating the device code for a particular platform.

5.7.1 Core kernel attributes

The attributes in Table 5.2 are defined in the \[[sycl:\:]] namespace and are applied to the function-type of kernel function declarations using C++ attribute specifier syntax.

A given attribute-token shall appear at most once in each attribute-list. The first declaration of a function shall specify an attribute if any declaration of that function specifies the same attribute. If a function is declared with an attribute in one translation unit and the same function is declared without the same attribute in another translation unit, the program is ill-formed and no diagnostic is required.

If there are any conflicts between different kernel attributes, then the behavior is undefined. The attributes have an effect when applied to a kernel function and no effect otherwise (i.e. no effect on non-kernel functions and on anything other than a function). If an attribute is applied to a device function that is not a kernel function (but that is potentially called from a kernel function), then the effect is implementation defined. It is implementation defined whether any diagnostic is produced when an attribute is applied to anything other than the function-type of a kernel function declaration.
<table>
<thead>
<tr>
<th>SYCL attribute</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>reqd_work_group_size(dim0)</code></td>
<td>Indicates that the kernel must be launched with the specified work-group size. The order of the arguments matches the constructor of the <code>group</code> class. Each argument to the attribute must be an integral constant expression. The dimensionality of the attribute variant used must match the dimensionality of the work-group used to invoke the kernel.</td>
</tr>
<tr>
<td><code>reqd_work_group_size(dim0, dim1)</code></td>
<td>SYCL device compilers should give a compilation error if the required work-group size is unsupported. If the kernel is submitted for execution using an incompatible work-group size, the SYCL runtime must throw an exception with the <code>errc::nd_range_error</code> error code.</td>
</tr>
<tr>
<td><code>reqd_work_group_size(dim0, dim1, dim2)</code></td>
<td></td>
</tr>
<tr>
<td><code>work_group_size_hint(dim0)</code></td>
<td>Hint to the compiler on the work-group size most likely to be used when launching the kernel at runtime. Each argument must be an integral constant expression, and the number of dimensional values defined provide additional information to the compiler on the dimensionality most likely to be used when launching the kernel at runtime. The effect of this attribute, if any, is implementation defined.</td>
</tr>
<tr>
<td><code>work_group_size_hint(dim0, dim1)</code></td>
<td></td>
</tr>
<tr>
<td><code>work_group_size_hint(dim0, dim1, dim2)</code></td>
<td></td>
</tr>
<tr>
<td><code>vec_type_hint(&lt;type&gt;)</code></td>
<td>Hint to the compiler on the vector computational width of the kernel. The argument must be one of the vector types defined in section 4.10.2. The effect of this attribute, if any, is implementation defined.</td>
</tr>
<tr>
<td></td>
<td>This attribute is deprecated (available for use, but will likely be removed in a future version of the specification and is not recommended for use in new code).</td>
</tr>
</tbody>
</table>

Table 5.2: Attributes supported by the SYCL General programming interface.
SYCL 2020 provisional

<table>
<thead>
<tr>
<th>SYCL attribute</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>reqd_sub_group_size(dim)</td>
<td>Indicates that the kernel must be compiled and executed with the specified sub-group size. The argument to the attribute must be an integral constant expression. SYCL device compilers should give a compilation error if the required sub-group size is unsupported by the device or incompatible with any language feature used by the kernel. The set of valid sub-group sizes for a kernel can be queried as described in Table 4.19 and Table 4.99.</td>
</tr>
</tbody>
</table>

Table 5.2: Attributes supported by the SYCL General programming interface.

Other attributes may be provided as part of SYCL backend-interop functionality.

### 5.7.2 Example attribute syntax

Using [[sycl::reqd_work_group_size(16)]] as an example attribute, but applying equally to all attributes in Table 5.2 and to attributes that are part of SYCL backend-interop or extensions, the following code examples demonstrate how to apply the attributes to the function-type of a kernel function.

```cpp
// Kernel defined as a lambda
myQueue.submit([&](handler &h) {
    h.parallel_for( range<1>(16),
    [=] (item<1> it) [[sycl::reqd_work_group_size(16)]] {
        //kernel code
    });
});

// Kernel defined as a functor to be invoked later
class KernelFunctor {
    public:
    void operator()(item<1> it) const [[sycl::reqd_work_group_size(16)]] {
        //kernel code
    }
};
```

### 5.7.3 Deprecated attribute syntax

The SYCL 1.2.1 specification (superseded by this version) defined two mechanisms for kernel attributes to be specified, which are deprecated in this version of SYCL. The old syntaxes are supported but will be removed in a future version, and are therefore not recommended for use. Specifically, the following two attribute syntaxes defined by the SYCL 1.2.1 specification are deprecated:

1. The attribute syntax defined by the OpenCL C specification within device code (__attribute__((attrib)) ).
2. The C++ attribute specifier syntax in the \([\text{cl}::]\) namespace applied to device functions (not the function-type of a kernel function), including automatic propagation of the attribute to any caller of such device functions.

5.8 Address-space deduction

C++ has no type-level support to represent address spaces. As a consequence, the SYCL generic programming model does not directly affect the C++ type of unannotated pointers and references.

Source level guarantees about address spaces in the SYCL generic programming model can only be achieved using pointer classes (instances of \texttt{multi_ptr}), which are regular classes that represent pointers to data stored in the corresponding address spaces.

In SYCL, the address space of pointer and references are derived from:

- Accessors that give access to shared data. They can be bound to a memory object in a command group and passed into a kernel. Accessors are used in scheduling of kernels to define ordering. Accessors to buffers have a compile-time address space based on their access mode.

- Explicit pointer classes (e.g. \texttt{global_ptr}) holds a pointer which is known to be addressing the address space represented by the \texttt{access::address_space}. This allows the compiler to determine whether the pointer references global, local, constant or private memory and generate code accordingly.

- Raw C++ pointer and reference types (e.g. \texttt{int*}) are allowed within SYCL kernels. They can be constructed from the address of local variables, explicit pointer classes, or accessors.

5.8.1 Address space assignment

In order to understand where data lives, the device compiler is expected to assign address spaces while lowering types for the underlying target based on the context. The address space deducing rules differ slightly depending on the target device of the SYCL backend.

If the target of the SYCL backend can represent the generic address space, then the “common address space deduction rules” (section 5.8.2) and the “generic as default address space rules” (section 5.8.3) apply. If the target of the SYCL backend cannot represent the generic address space, then the “common address space deduction rules” (section 5.8.2) and the “inferred address space rules” (section 5.8.4) apply.

[Note: SYCL address space does not affect the type, address space shall be understood as memory segment in which data is allocated. For instance, if \texttt{int i;} is allocated to the global address space, then \texttt{decltype(\&i)} shall evaluate to \texttt{int*}.
— end note]

5.8.2 Common address space deduction rules

The variable declarations get assigned to an address space depending on their scope and storage class:

- Namespace scope
  - The declaration is assigned to global address space if the type is not \texttt{const}
  - The declaration is assigned to constant address space if the type is \texttt{const}

- Block scope and function parameter scope
– Declarations with static storage are treated the same way as variables in namespace scope
– Otherwise the declaration is assigned to the local address space if declared in a hierarchical context
– Otherwise the declaration is assigned to the private address space

• Class scope:
  – Static data members are treated the same way as for variable in namespace scope

The result of a prvalue-to-xvalue conversion is assigned to the local address space if it happens in a hierarchical context or to the private address space otherwise.

### 5.8.3 Generic as default address space

Unannotated pointers and references are considered to be pointing to the generic address space.

### 5.8.4 Inferred address space

[Note for this provisional version: The address space deduction feature described next is being reworked to better align with addition of generic address space and generic as default address space. — end note]

Inside kernels, the SYCL device compiler will need to auto-deduce the memory region of unannotated pointer and reference types during the lowering of types from C++ to the underlying representation.

If a kernel function or device function contains a pointer or reference type, then the address space deduction must be attempted using the following rules:

• If an explicit pointer class is converted into a C++ pointer value, then the C++ pointer value will point to same address space as the one represented by the explicit pointer class.

• If a variable is declared as a pointer type, but initialized in its declaration to a pointer value with an already-deduced address space, then that variable will have the same address space as its initializer.

• If a function parameter is declared as a pointer type, and the argument is a pointer value with a deduced address space, then the function will be compiled as if the parameter had the same address space as its argument. It is legal for a function to be called in different places with different address spaces for its arguments: in this case the function is said to be “duplicated” and compiled multiple times. Each duplicated instance of the function must compile legally in order to have defined behavior.

• If a function return type is declared as a pointer type and return statements use address space deduced expressions, then the function will be compiled as if the return type had the same address space. To compile legally, all return expressions must deduce to the same address space.

• The rules for pointer types also apply to reference types. i.e. a reference variable takes its address space from its initializer. A function with a reference parameter takes its address space from its argument.

• If no other rule above can be applied to a declaration of a pointer, then it is assumed to be in the private address space.

It is illegal to assign a pointer value addressing one address space to a pointer variable addressing a different address space.
5.9 SYCL offline linking

5.9.1 SYCL functions and member functions linkage

The default behavior in SYCL applications is that all the definitions and declarations of the functions and member functions are available to the SYCL compiler, in the same translation unit. When this is not the case, all the symbols that need to be exported to a SYCL library or from a C++ library to a SYCL application need to be defined using the macro: \texttt{SYCL\_EXTERNAL}.

The \texttt{SYCL\_EXTERNAL} macro will only be defined if the implementation supports offline linking. The macro is implementation-defined, but the following restrictions apply:

- \texttt{SYCL\_EXTERNAL} can only be used on functions;
- if the SYCL backend does not support the generic address space then the function cannot use raw pointers as parameter or return types. Explicit pointer classes must be used instead;
- externally defined functions cannot call a \texttt{sycl::parallel\_for\_work\_item} member function;
- externally defined functions cannot be called from a \texttt{sycl::parallel\_for\_work\_group} scope.

The SYCL linkage mechanism is optional and implementation defined.
6. SYCL Extensions

This section describes the mechanism by which the SYCL core specification can be extended. An extension can be either of two flavors: an extension ratified by the Khronos SYCL group or a vendor supplied extension. In both cases, an extension is an optional feature set which an implementation need not implement in order to be conformant with the core SYCL specification.

Vendors may choose to define extensions in order to expose custom features or to gather feedback on an API that is not yet ready for inclusion in the core specification. Since the APIs for extensions may change as feedback is gathered, the extension mechanism includes a way for application developers to test for the API version of each extension. Once a vendor extension has stabilized, vendors are encouraged to promote it to a future version of the core SYCL specification. Thus, extensions can be viewed as a pipeline of features for consideration in future SYCL versions.

This section does not describe any particular extension to SYCL. Rather, it describes the mechanism for defining an extension. Each extension is defined by its own separate document. If an extension is ratified by the Khronos SYCL group, that group will release a document describing the extension. If a vendor defines an extension, the vendor is responsible for releasing its documentation.

6.1 Definition of an extension

An extension can be implemented by adding new types or free functions in a specific namespace, by adding functionality to an existing class that is defined in the core SYCL specification, or through a combination of the two.

New types or free functions for Khronos ratified extensions are defined in the namespace ::sycl::khr::<extensionname>. For example, ::sycl::khr::fancy could be the namespace for a Khronos extension named “fancy”.

If a vendor specific extension adds new types or free functions, the vendor is encouraged to define them in the namespace ::sycl::ext::<vendorname> and they are encouraged to add another namespace layer according to the name of the extension. For example, ::sycl::ext::acme::fancier could be the namespace for an extension from the Acme vendor. However, vendors may also choose to define new types and free functions in another top-level namespace that is outside of ::sycl. This might be more appropriate, for example, when an extension integrates features from an existing non-SYCL API. A vendor may not define new types or free functions underneath ::sycl, unless they are in ::sycl::ext::<vendorname>.

[Note: Vendors are discouraged from defining top level namespaces that start with the word “sycl” because we believe that application developers may want to use namespaces like this as namespace aliases. — end note]

Extensions may only add functionality to existing SYCL classes in a limited way. When a Khronos ratified extension needs to add functionality to an existing class, it does so by adding a member function named khr() to that class. For example, an extension on the device class would add a member function like this:

```cpp
class device {
```

```cpp

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```
The `khr()` member function returns an object, and that object provides member functions that are part of the extension.

Likewise, a vendor specific extension may add functionality to an existing SYCL class by adding a member function named `ext_<vendorname>()` (e.g. `ext_acme()` for the Acme vendor) like this:

```cpp
class device {
  // ...
  sycl::ext::acme::device ext_acme();
};
```

One motivation for this pattern is to reduce verbosity of application code that uses an extension and to facilitate application migration when an extension is promoted to the core SYCL specification. Consider the following application code:

```cpp
void foo(sycl::device dev) {
  dev.ext_acme().fancy();
}
```

If the extension “fancy” is later promoted to the core SYCL specification, the application need only remove the call to `ext_acme()` in order to migrate the application.

Extensions may also add C++ attributes. The attribute namespace `sycl::` is reserved for attributes in the core SYCL specification and for Khronos ratified extensions. Vendor defined extensions should use a different attribute namespace.

Applications must include a special header file in order to get declarations for the types and free functions of an extension. Each Khronos ratified extension has an associated header named "SYCL/khr/<extensionname>.hpp". The include path "SYCL/ext/<vendorname>" is reserved for vendor extensions. Vendors can choose to provide a single header for all extensions or to provide separate headers for each extension. For example, the Acme vendor could provide the header "SYCL/ext/acme/extensions.hpp" for access to all of its extensions. As with namespaces, vendors are encouraged to define header files in "SYCL/ext/<vendorname>", but a vendor may also define header files in another file system path that is outside of the "SYCL" directory. Vendors may not define header files in the "SYCL" path unless they are underneath "SYCL/ext/<vendorname>".

## 6.2 Predefined macros

Each Khronos ratified extension has a corresponding feature test macro of the form `SYCL_KHR_<extensionname>` whose value follows the C++20 pattern for language feature test macros. The value is a number with 6 decimal digits in YYYYMM format identifying the year and month the extension was first adopted or the date the extension was last updated. An implementation must predefined this macro only if it implements the extension, so applications can use the macro in order to determine if the extension is available.

If an implementation provides a vendor specific extension, it should also predefined a feature test macro of the form `SYCL_EXT_<vendorname>_<extensionname>` (e.g. `SYCL_EXT_ACME_FANCY`). The value of the macro must be
an integer that monotonically increases for each version of the extension, and vendors are encouraged to use the
same YYYYMM format described above.

[Note: The feature test macros are defined uniformly across all parts of a SYCL application, just like any macro. If an implementation uses SMCP, all compiler passes predefine a particular feature test macro the same way, regardless of whether that compiler pass’s device supports the feature. Thus, the feature test macros cannot be used to determine whether any particular device supports a feature. If the feature is device-specific, the application must use device::has() or platform::has() to test the feature’s aspect in order to determine whether a particular device supports the feature. — end note]

Each vendor’s implementation must also predefine a macro of the form SYCL_VENDOR_<vendorname> (e.g.
SYCL_VENDOR_ACME), which applications can use to determine whether they are being compiled by that vendor’s
toolchain.

An implementation, of course, is allowed to predefine additional macros too. However, an implementation may
not predefine a macro whose name starts with SYCL unless it starts with SYCL_EXT_<vendorname> or SYCL_VENDOR_<vendorname>.

### 6.3 Device aspects and conditional features

An extension may define additional device aspects and it may provide features which are only available on de-
vices with certain aspects. If it does so, the extension documentation must describe which aspects enable these
conditional features. If an extension provides a new enumerated aspect value, the type of the new value must be:
::sycl::aspect but the enumerated value must be in the extension’s namespace scope. For example, a Khronos
ratified extension could add a new aspect value like this:

```cpp
namespace sycl {
namespace khr {
namespace aspect {
  static constexpr auto foo = static_cast<sycl::aspect>(1000);
}
}
}
```

A vendor extension could add an aspect value in a similar way:

```cpp
namespace sycl {
namespace ext {
namespace acme {
namespace aspect {
  static constexpr auto bar = static_cast<sycl::aspect>(-1);
}
}
}
```
In the examples above, the vendor has decided to implement aspects from Khronos ratified extensions starting at 1000 and to implement vendor specific aspects as negative integers. However, these are just example implementation details. The SYCL specification does not prescribe the numerical value of any aspect.

### 6.4 Backends

A vendor extension may define a new SYCL backend. If it does so, the enumerated value for the backend should be defined in the extension’s namespace, similar to the way an extended aspect is defined:

```cpp
namespace sycl {
    namespace ext {
        namespace acme {
            namespace backend {
                static constexpr auto foo = static_cast<sycl::backend>(-1);
            }
        }
    }
}
```

The backend’s interoperability API should be made available through a header named “SYCL/ext/<vendorname>/backend/<backendname>.hpp” and it should be defined in the namespace ::sycl::ext::<vendorname>::<backendname>. The implementation should also predefine a macro of the form SYCL_EXT_<vendorname>_BACKEND_<backendname> when the backend is active.

### 6.5 Conditional features and compilation errors

SYCL applications are allowed to contain kernels for heterogeneous devices and those kernels, of course, are allowed to use features that are available only on certain devices. Applications are responsible for ensuring that a kernel using such a feature is never submitted to a device that does not support the feature and is never compiled for a device that does not support the feature (e.g. via the module build() or compile() functions). If an application fails to adhere to this requirement, the implementation raises a feature_not_supported exception.

[Note: If an implementation defines a compiler flag that causes some kernels to be pre-compiled for some devices, the vendor is responsible for defining the semantics about when errors are reported for kernels that use device specific extensions. — end note]

An implementation may not raise a spurious error as a result of speculative compilation of a kernel for a device when the application did not specifically ask to submit the kernel to that device or to compile the kernel for that device. To clarify, consider the following example. An application with kernels K1 and K2 runs on devices D1 and D2. Kernel K1 uses extensions specific to D1, and kernel K2 uses extensions specific to D2. The application is coded to ensure that K1 is only submitted to D1 and that K2 is only submitted to D2. An implementation may not raise errors due to speculative compilation of K1 for device D2 or for compilation of K2 for device D1.
An implementation is required, however, to raise an error for a kernel that is not valid for any device. Therefore an implementation must raise an error for a kernel K that is invalid for all devices, even if the application is coded such that kernel K is never submitted to any device.
A. Information descriptors

The purpose of this chapter is to include all the header of the memory object descriptors, which are described in detail in Chapter 4, for platform, context, device, and queue.

A.1 Platform information descriptors

The following interface includes all the information descriptors for the platform class as described in Table 4.12.

```cpp
namespace sycl {
namespace info {
enum class platform : unsigned int {
  profile,
  version,
  name,
  vendor,
  extensions // Deprecated
};
}
}
```

A.2 Context information descriptors

The following interface includes all the information descriptors for the context class as described in Table 4.15.

```cpp
namespace sycl {
namespace info {
enum class context : int {
  platform,
  devices,
  atomic_memory_order_capabilities,
  atomic_fence_order_capabilities
  atomic_memory_scope_capabilities,
  atomic_fence_scope_capabilities
};
}
}
```

A.3 Device information descriptors

The following interface includes all the information descriptors for the device class as described in Table 4.19.
namespace sycl {
namespace info {
enum class device : int {
  device_type, 
  vendor_id, 
  max_compute_units, 
  max_work_item_dimensions, 
  max_work_item_sizes, 
  max_work_group_size, 
  preferred_vector_width_char, 
  preferred_vector_width_short, 
  preferred_vector_width_int, 
  preferred_vector_width_long, 
  preferred_vector_width_float, 
  preferred_vector_width_double, 
  preferred_vector_width_half, 
  native_vector_width_char, 
  native_vector_width_short, 
  native_vector_width_int, 
  native_vector_width_long, 
  native_vector_width_float, 
  native_vector_width_double, 
  native_vector_width_half, 
  max_clock_frequency, 
  address_bits, 
  max_mem_alloc_size, 
  image_support, // Deprecated 
  max_read_image_args, 
  max_write_image_args, 
  image2d_max_height, 
  image2d_max_width, 
  image3d_max_height, 
  image3d_max_width, 
  image3d_max_depth, 
  image_max_buffer_size, 
  image_max_array_size, 
  max_samplers, 
  max_parameter_size, 
  mem_base_addr_align, 
  half_fp_config, 
  single_fp_config, 
  double_fp_config, 
  global_mem_cache_type, 
  global_mem_cache_line_size, 
  global_mem_cache_size, 
  global_mem_size, 
  max_constant_buffer_size, 
  max_constant_args, 
  local_mem_type, 
  local_mem_size, 
  error_correction_support, 
  host_unified_memory, 
  atomic_memory_order_capabilities, 
  atomic_fence_order_capabilities,
};
atomic_memory_scope_capabilities,
atomic_fence_scope_capabilities,
profiling_timer_resolution,
is_endian_little,
is_available,
is_compiler_available, // Deprecated
is_linker_available, // Deprecated
execution_capabilities,
queue_profiling, // Deprecated
built_in_kernels,
platform,
name,
vendor,
driver_version,
profile,
version,
backend_version,
aspects,
extensions, // Deprecated
printf_buffer_size,
prefixed interoper user sync,
parent_device,
partition_max_sub_devices,
partition_properties,
partition_affinity_domains,
partition_type_property,
partition_type_affinity_domain,
reference_count
};

enum class device_type : unsigned int {
cpu,     // Maps to OpenCL CL DEVICE TYPE_CPU
gpu,     // Maps to OpenCL CL DEVICE TYPE_GPU
accelerator, // Maps to OpenCL CL DEVICE TYPE ACCELERATOR
custom,  // Maps to OpenCL CL DEVICE TYPE CUSTOM
automatic, // Maps to OpenCL CL DEVICE TYPE DEFAULT
host,
all     // Maps to OpenCL CL DEVICE TYPE ALL
};

enum class partition_property : int {
no_partition,
partition_equally,
partition_by_counts,
partition_by_affinity_domain
};

enum class partition_affinity_domain : int {
not_applicable,
uma,
L4_cache,
L3_cache,
L2_cache,
L1_cache,
next_partitionable
Queue information descriptors

The following interface includes all the information descriptors for the `queue` class as described in Table 4.23.

A.5 Kernel information descriptors

The following interface includes all the information descriptors for the `kernel` class as described in Table 4.98.
### A.6 Event information descriptors

The following interface includes all the information descriptors for the `event` class as described in Table 4.28 and Table 4.29.

```cpp
namespace sycl {
namespace info {
enum class event: int {
    command_execution_status
};
enum class event_command_status : int {
    submitted,
    running,
    complete
};
enum class event_profiling : int {
    command_submit,
    command_start,
    command_end
};
} // namespace info
} // namespace sycl
```
B. Feature sets

As of SYCL 2020 there are now two distinct feature sets which a SYCL implementation can conform to, in order to better fit the requirements of different domains, such as embedded, mobile, and safety critical, which may have limitations because of the toolchains used.

A SYCL implementation can choose to conform to either the full feature set or the reduced feature set.

B.1 Full feature set

The full feature set includes all features specified in the core SYCL specification with no exceptions.

B.2 Reduced feature set

The reduced feature set makes certain features optional or restricted to specific forms. The following list defines all the differences between the reduced feature set and the full feature set.

1. **Un-named SYCL kernel functions**: SYCL kernel functions which are defined using a lambda expression and therefore have no standard name are required to be provided a name via the kernel name template parameter of kernel invocation functions such as `parallel_for`. This overrides the core SYCL specification rules for SYCL kernel function naming as specified in Section 4.10.7.

B.3 Compatibility

In order to avoid introducing any kind of divergence the reduced and full feature sets are defined such that the full feature set is a subsumption of the reduced feature set. This means that any applications which are developed for the reduced feature will be compatible with both a SYCL reduced implementation and a SYCL full implementation.

B.4 Conformance

One of the reasons for having this be defined in the specification is that hardware vendors which wish to support SYCL on their platform(s) want to be able to demonstrate their support for it by passing conformance. However, if passing conformance means adopting features which they do not believe to be necessary at an additional development effort then this may deter them.

Each feature set has its own route for passing conformance allowing adopters of SYCL to specify the feature set they wish to test conformance against. The conformance test suite would then alter or disable the tests within the test suite according to how the feature sets are differentiated above.
C. Host backend specification

This chapter describes how SYCL is mapped on the SYCL host backend. The SYCL host backend exposes the host where the SYCL application is executing as a platform to dispatch SYCL kernels. The SYCL host backend exposes at least one SYCL host device.

C.1 Mapping of the SYCL programming model on the host

The SYCL host device implements all functionality required to execute the SYCL kernels directly on the host, without relying on a third party API. It has full SYCL capabilities and reports them through the SYCL information retrieval interface. At least one SYCL host device must be exposed in the SYCL host backend in all SYCL implementations, and it must always be available. Any C++ application debugger, if available on the system, can be used for debugging SYCL kernels executing on a SYCL host device.

When a SYCL implementation executes kernels on the host device, it is free to use whatever parallel execution facilities available on the host, as long as it executes within the semantics of the kernel execution model defined by the SYCL kernel execution model.

Kernel math library functions on the host must conform to OpenCL math precision requirements. The SYCL host device needs to be queried for the capabilities it provides. This ensures consistency when executing any SYCL general application.

The SYCL host device must report as supporting images and therefore support the minimum image formats.

The range of image formats supported by the host device is implementation-defined, but must match the minimum requirements of the OpenCL specification.

SYCL implementors can provide extensions on the host-device to match any other backend-specific extension. This allows developers to rely on the host device to execute their programs when said backend is not available.

C.1.1 SYCL memory model on the host

All SYCL device memories are available on devices from the host backend.

<table>
<thead>
<tr>
<th>SYCL</th>
<th>Host</th>
</tr>
</thead>
<tbody>
<tr>
<td>Global</td>
<td>System memory</td>
</tr>
<tr>
<td>Constant</td>
<td>System memory</td>
</tr>
<tr>
<td>Local</td>
<td>System memory</td>
</tr>
<tr>
<td>Private</td>
<td>Stack</td>
</tr>
</tbody>
</table>

Table C.1: Mapping of SYCL memory regions into host memory regions.
C.2 Interoperability with the host application

The host backend must ensure all functionality of the SYCL generic programming model is always available to developers. However, since there is no heterogeneous API behind the host backend (it directly targets the host platform), there are no native types for SYCL objects to map to in the SYCL application.

Inside SYCL kernels, the host backend must ensure interoperability with existing host code, so that existing host libraries can be used inside SYCL kernels executing on the host. In particular, when retrieving a raw pointer from a multi pointer object, the pointer returned must be usable by any library accessible by the SYCL application.
D. OpenCL backend specification

This chapter describes how the SYCL general programming model is mapped on top of OpenCL, and how the SYCL generic interoperability interface must be implemented by vendors providing SYCL for OpenCL implementations to ensure SYCL applications written for the OpenCL backend are interoperable.

D.1 SYCL for OpenCL framework

The SYCL framework allows applications to use a host and one or more OpenCL devices as a single heterogeneous parallel computer system. The framework contains the following components:

- **SYCL C++ template library**: The template library provides a set of C++ templates and classes which provide the programming model to the user. It enables the creation of runtime classes such as SYCL queues, buffers and images, as well as access to some underlying OpenCL runtime object, such as contexts, platforms, devices and program objects.

- **SYCL runtime**: The SYCL runtime interfaces with the underlying OpenCL implementations and handles scheduling of commands in queues, moving of data between host and devices, manages contexts, programs, kernel compilation and memory management.

- **OpenCL Implementation(s)**: The SYCL system assumes the existence of one or more OpenCL implementations available on the host machine. If no OpenCL implementation is available, then the SYCL implementation provides only the SYCL host device to run kernels on.

- **SYCL device compilers**: The SYCL device compilers compile SYCL C++ kernels into a format which can be executed on an OpenCL device at runtime. There may be more than one SYCL device compiler in a SYCL implementation. The format of the compiled SYCL kernels is not defined. A SYCL device compiler may, or may not, also compile the host parts of the program.

The OpenCL backend is enabled using the `sycl::backend::opencl` value of `enum class backend`. That means that when the OpenCL backend is active, the value of `sycl::is_backend_active<sycl::backend::opencl>::value` will be `true`.

D.2 Mapping of SYCL programming model on top of OpenCL

The SYCL programming model was originally designed as a high-level model for the OpenCL API, hence the mapping of SYCL on the OpenCL API is mostly straightforward.

When the OpenCL backend is active on a SYCL application, all visible OpenCL platforms are exported as SYCL platforms.

When a SYCL implementation executes kernels on an OpenCL device, it achieves this by enqueuing OpenCL commands to execute computations on the processing elements within a device. The processing elements within an OpenCL compute unit may execute a single stream of instructions as ALUs within a SIMD unit (which execute
D.2. MAPPING OF SYCL PROGRAMMING MODEL ON TOP OF OPENCL SYCL 2020 provisional

<table>
<thead>
<tr>
<th>SYCL</th>
<th>OpenCL</th>
</tr>
</thead>
<tbody>
<tr>
<td>Global</td>
<td>Global memory</td>
</tr>
<tr>
<td>Constant</td>
<td>Constant memory</td>
</tr>
<tr>
<td>Local</td>
<td>Local memory</td>
</tr>
<tr>
<td>Private</td>
<td>Private memory</td>
</tr>
</tbody>
</table>

Table D.1: Mapping of SYCL memory regions into OpenCL memory regions.

in lockstep with a single stream of instructions), as independent SPMD units (where each PE maintains its own program counter) or as some combination of the two.

D.2.1 Platform mixed version support

The SYCL system presents the user with a set of devices, grouped into some number of platforms. The device version is an indication of the device’s capabilities, as represented by the device information returned by the `sycl::device::get_info()` member function. Examples of attributes associated with the device version are resource limits and information about functionality beyond the core SYCL specification’s requirements. The version returned corresponds to the highest version of the OpenCL specification for which the device is conformant, but is not higher than the version of the device’s platform which bounds the overall capabilities of the runtime operating the device.

D.2.2 OpenCL memory model

The memory model for SYCL devices running on OpenCL platforms follows the memory model of the OpenCL version they conform to. Work-items executing in a kernel have access to four distinct memory regions, with the mapping between SYCL and OpenCL described in table D.1.

D.2.3 OpenCL resources managed by SYCL application

In OpenCL, a developer must create a context to be able to execute commands on a device. Creating a context involves choosing a platform and a list of devices. In SYCL, contexts, platforms and devices all exist, but the user can choose whether to specify them or have the SYCL implementation create them automatically. The minimum required object for submitting work to devices in SYCL is the queue, which contains references to a platform, device and context internally.

The resources managed by SYCL are:

1. **Platforms:** all features of OpenCL are implemented by platforms. A platform can be viewed as a given hardware vendor’s runtime and the devices accessible through it. Some devices will only be accessible to one vendor’s runtime and hence multiple platforms may be present. SYCL manages the different platforms for the user. In SYCL, a platform resource is accessible through a `sycl::platform` object. SYCL also provides a host platform object, which only contains a single host device.

2. **Contexts:** any OpenCL resource that is acquired by the user is attached to a context. A context contains a collection of devices that the host can use and manages memory objects that can be shared between the devices. Data movement between devices within a context may be efficient and hidden by the underlying OpenCL runtime while data movement between contexts may involve the host. A given context can only wrap devices owned by a single platform. In SYCL, a context resource is accessible through a `sycl::context` object.
3. **Devices**: platforms provide one or more devices for executing kernels. In SYCL, a device is accessible through a `sycl::device` object.

4. **Kernels**: the SYCL functions that run on SYCL devices (i.e. either an OpenCL device, or the host device) are defined as C++ function objects (a named function object type or a lambda function).

5. **Modules**: OpenCL objects that store implementation data for the SYCL kernels. These objects are only required for advanced use in SYCL and are encapsulated in the `sycl::module` class.

6. **Queues**: SYCL kernels execute in command queues. The user must create a queue, which references an associated context, platform and device. The context, platform and device may be chosen automatically, or specified by the user. In SYCL, command queues are accessible through `sycl::queue` objects.

### D.3 Interoperability with the OpenCL API

The OpenCL backend for SYCL ensures maximum compatibility between SYCL and OpenCL kernels and API. This includes supporting devices with different capabilities and support for different versions of the OpenCL C language, in addition to supporting SYCL kernels written in C++.

**SYCL runtime** classes which encapsulate an OpenCL opaque type such as SYCL context or SYCL queue must provide an interoperability constructor taking an instance of the OpenCL opaque type. These constructors must retain that instance to increase the reference count of the OpenCL resource.

The destructor for the **SYCL runtime** classes which encapsulate an OpenCL opaque type must release that instance to decrease the reference count of the OpenCL resource.

Note that an instance of a **SYCL runtime** class which encapsulates an OpenCL opaque type can encapsulate any number of instances of the OpenCL type, unless it was constructed via the interoperability constructor, in which case it can encapsulate only a single instance of the OpenCL type.

The lifetime of a **SYCL runtime** class that encapsulates an OpenCL opaque type and the instance of that opaque type retrieved via the `get()` member function are not tied in either direction given correct usage of OpenCL reference counting. For example if a user were to retrieve a `cl_command_queue` instance from a SYCL queue instance and then immediately destroy the SYCL queue instance, the `cl_command_queue` instance is still valid. Or if a user were to construct a SYCL queue instance from a `cl_command_queue` instance and then immediately release the `cl_command_queue` instance, the SYCL queue instance is still valid.

Note that a **SYCL runtime** class that encapsulates an OpenCL opaque type is not responsible for any incorrect use of OpenCL reference counting outside of the **SYCL runtime**. For example if a user were to retrieve a `cl_command_queue` instance from a SYCL queue instance and then release the `cl_command_queue` instance more than once without any prior retain then the SYCL queue instance that the `cl_command_queue` instance was retrieved from is now undefined.

Note that an instance of the SYCL buffer or SYCL image class templates constructed via the interoperability constructor is free to copy from the `cl_mem` into another memory allocation within the **SYCL runtime** to achieve normal SYCL semantics, for as long as the SYCL buffer or SYCL image instance is alive.

Table D.2 relates SYCL objects to their OpenCL native type in the SYCL application.
### OpenCL backend native types

<table>
<thead>
<tr>
<th>Description</th>
<th>Native Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>A SYCL device object encapsulates an OpenCL device object.</td>
<td>cl_device_id</td>
</tr>
<tr>
<td>A SYCL context object encapsulates an OpenCL context object.</td>
<td>cl_context</td>
</tr>
<tr>
<td>When a SYCL program is constructed for the OpenCL backend, this maps directly to an OpenCL program object.</td>
<td>cl_program</td>
</tr>
<tr>
<td>The SYCL implementation will produce OpenCL programs from the SYCL device kernels. They are dispatched on the OpenCL interface as OpenCL kernel objects. This also apply to built-in kernels.</td>
<td>cl_kernel</td>
</tr>
<tr>
<td>A SYCL event can encapsulate one or multiple OpenCL events, representing a number of dependencies in the same or different contexts, that must be satisfied for the SYCL event to be complete.</td>
<td>std::vector&lt;cl_event&gt;</td>
</tr>
<tr>
<td>SYCL buffers containing OpenCL memory objects can handle multiple cl_mem objects in the same or different context. The interoperability interface will return a list of active buffers in the SYCL runtime.</td>
<td>std::vector&lt;cl_mem&gt;</td>
</tr>
<tr>
<td>SYCL images containing OpenCL image objects can handle multiple underlying cl_mem objects at the same time in the same or different OpenCL contexts. The interoperability interface will return a list of active images in the SYCL runtime.</td>
<td>std::vector&lt;cl_mem&gt;</td>
</tr>
</tbody>
</table>

Table D.2: List of native types per SYCL object in the OpenCL backend.

The user can also extract OpenCL cl_kernel and cl_program objects for kernels by providing the type name of the kernel.

Inside the SYCL kernel, the SYCL API offers interoperability with OpenCL device types. The table D.3 describes the mapping of kernel types.

<table>
<thead>
<tr>
<th>SYCL kernel native types in OpenCL</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Returns a pointer in the OpenCL address space corresponding to the type of multi pointer object</td>
<td>multi_ptr::get()</td>
</tr>
<tr>
<td>Returns an event_t object, which can be used to identify copies from global to local memory and vice-versa</td>
<td>device_event::get()</td>
</tr>
</tbody>
</table>

Table D.3: List of native types per SYCL object on kernel code.
When a buffer or image is allocated on more than one OpenCL device, if these devices are on separate contexts then multiple cl_mem objects may be allocated for the memory object, depending on whether the object has actively been used on these devices yet or not.

Some types in SYCL vary according to pointer size or vary on the host according to the host ABI, such as size_t or long. In order for the the SYCL device compiler to ensure that the sizes of these types match the sizes on the host and to enable data of these types to be shared between host and device, the OpenCL interoperability types are defined, sycl::cl_int and sycl::cl_size_t.

The OpenCL C function qualifier __kernel and the access qualifiers: __read_only, __write_only and __read_write are not exposed in SYCL via keywords, but are instead encapsulated in SYCL's parameter passing system inside accessors. Users wishing to achieve the OpenCL equivalent of these qualifiers in SYCL should instead use SYCL accessors with equivalent semantics.

Any OpenCL C function included in a pre-built OpenCL library can be defined as an extern "C" function and the OpenCL program has to be linked against any SYCL program that contains kernels using the external function. In this case, the data types used have to comply with the interoperability aliases defined in D.7.

### D.4 Programming interface

The following section describes the OpenCL-specific API. All free functions are available in the sycl::opencl namespace.

<table>
<thead>
<tr>
<th>OpenCL interoperability functions</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>sycl::context make_context (cl_context &amp;clContext,</td>
<td>Constructs a SYCL context instance from an OpenCL cl_context in accordance</td>
</tr>
<tr>
<td>const sycl::context &amp;syclContext,</td>
<td>with the requirements described in 4.5.2.</td>
</tr>
<tr>
<td>const sycl::async_handler &amp;asyncHandler = {})</td>
<td></td>
</tr>
<tr>
<td>cl_context get_native<a href="">sycl::context</a>(const</td>
<td>Returns a valid cl_context instance in accordance with the requirements</td>
</tr>
<tr>
<td>sycl::context &amp;syclContext)</td>
<td>described in 4.5.2.</td>
</tr>
<tr>
<td>sycl::event make_event (const cl_event &amp;clEvent,</td>
<td>Constructs a SYCL event instance from an OpenCL cl_event in accordance</td>
</tr>
<tr>
<td>const sycl::context &amp;syclContext)</td>
<td>with the requirements described in 4.5.2.</td>
</tr>
<tr>
<td>cl_event get_native<a href="">sycl::event</a>(const sycl::</td>
<td>Returns a valid cl_event instance in accordance with the requirements</td>
</tr>
<tr>
<td>event &amp;syclEvent)</td>
<td>described in 4.5.2.</td>
</tr>
<tr>
<td>sycl::device make_device(const cl_device_id &amp;clDevice)</td>
<td>Constructs a SYCL device instance from an OpenCL cl_device_id in accordance</td>
</tr>
<tr>
<td>cl_device_id get_native<a href="">sycl::device</a>(const</td>
<td>with the requirements described in 4.5.2.</td>
</tr>
<tr>
<td>sycl::device &amp;syclDevice)</td>
<td></td>
</tr>
<tr>
<td>sycl::platform make_platform(const cl_platform_id</td>
<td>Constructs a SYCL platform instance from an OpenCL cl_platform_id in</td>
</tr>
<tr>
<td>&amp;clPlatformId)</td>
<td>accordance with the requirements described in 4.5.2.</td>
</tr>
<tr>
<td>cl_platform_id get_native<a href="">sycl::platform</a>(const</td>
<td>Returns a valid cl_platform_id instance in accordance with the requirements</td>
</tr>
<tr>
<td>sycl::platform &amp;syclPlatform)</td>
<td>described in 4.5.2.</td>
</tr>
</tbody>
</table>
### OpenCL interoperability functions

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>sycl::queue make_queue</code>&lt;br&gt;  <code>const cl_command_queue &amp;clQueue,</code>&lt;br&gt;  <code>const sycl::context &amp;syclContext,</code>&lt;br&gt;  <code>const sycl::async_handler &amp;asyncHandler = {}</code>)</td>
<td>Constructs a SYCL queue instance with an optional async_handler from an OpenCL cl_command_queue in accordance with the requirements described in 4.5.2.</td>
</tr>
<tr>
<td><code>cl_command_queue get_native&lt;sycl::queue&gt;</code>&lt;br&gt;  <code>const sycl::queue &amp;syclQueue</code>)</td>
<td>Returns a valid cl_command_queue instance in accordance with the requirements described in 4.5.2.</td>
</tr>
<tr>
<td><code>sycl::buffer make_buffer</code>&lt;br&gt;  <code>const cl_mem &amp;clMemObject,</code>&lt;br&gt;  <code>const sycl::context &amp;syclContext,</code>&lt;br&gt;  <code>sycl::event availableEvent = {}</code>)</td>
<td>Available only when: dimensions == 1. Constructs a SYCL buffer instance from an OpenCL cl_mem in accordance with the requirements described in 4.5.2. The instance of the SYCL buffer class template being constructed must wait for the SYCL event parameter, if one is provided, availableEvent to signal that the cl_mem instance is ready to be used. The SYCL context parameter syclContext is the context associated with the memory object.</td>
</tr>
<tr>
<td><code>sycl::sampled_image make_sampled_image</code>&lt;br&gt;  <code>const cl_mem &amp;clMemObject,</code>&lt;br&gt;  <code>const context &amp;syclContext,</code>&lt;br&gt;  <code>event availableEvent = {}</code>)</td>
<td>Constructs a SYCL sampled_image instance from an OpenCL cl_mem in accordance with the requirements described in 4.5.2. The instance of the SYCL image class template being constructed must wait for the SYCL event parameter, if one is provided, availableEvent to signal that the cl_mem instance is ready to be used. The SYCL context parameter syclContext is the context associated with the memory object.</td>
</tr>
<tr>
<td><code>sycl::unsampled_image make_unsampled_image</code>&lt;br&gt;  <code>const cl_mem &amp;clMemObject,</code>&lt;br&gt;  <code>const sycl::context &amp;syclContext,</code>&lt;br&gt;  <code>sycl::image_sampler syclImageSampler,</code>&lt;br&gt;  <code>event availableEvent = {}</code>)</td>
<td>Constructs a SYCL unsampled_image instance from an OpenCL cl_mem in accordance with the requirements described in 4.5.2. The instance of the SYCL image class template being constructed must wait for the SYCL event parameter, if one is provided, availableEvent to signal that the cl_mem instance is ready to be used. The SYCL context parameter syclContext is the context associated with the memory object.</td>
</tr>
<tr>
<td><code>sycl::image_sampler make_image_sampler</code>&lt;br&gt;  <code>const cl_sampler &amp;clSampler,</code>&lt;br&gt;  <code>const sycl::context &amp;syclContext,</code>&lt;br&gt;  <code>sycl::context &amp;syclContext,</code>&lt;br&gt;  <code>sycl::event availableEvent = {}</code>)</td>
<td>Constructs a SYCL image_sampler instance from an OpenCL cl_sampler in accordance with the requirements described in 4.5.2. The SYCL context parameter syclContext is the context associated with the sampler object.</td>
</tr>
</tbody>
</table>
### OpenCL interoperability functions

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>sycl::kernel make_kernel(const cl_kernel &amp;clKernel, const sycl::context&amp; syclContext)</code></td>
<td>Constructs a SYCL kernel instance from an OpenCL cl_kernel in accordance with the requirements described in 4.5.2. The SYCL context must represent the same underlying OpenCL context associated with the OpenCL kernel object.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>cl_kernel get_native&lt;sycl::kernel&gt;(const sycl::kernel &amp;syclKernel)</code></td>
<td>Returns a valid cl_kernel instance in accordance with the requirements described in 4.5.2.</td>
</tr>
</tbody>
</table>

### D.4.1 Reference counting

All OpenCL objects are reference counted. The SYCL general programming model doesn’t require that native objects are reference counted. However, for convenience, the following function is provided in the `sycl::opencl` namespace.

<table>
<thead>
<tr>
<th>Reference counting</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>template &lt;typename openCLT&gt; cl_uint get_reference_count(openCLT obj)</code></td>
<td>Returns the reference counting of the given object</td>
</tr>
</tbody>
</table>

### D.4.2 Errors and limitations

If there is an OpenCL error associated with an exception triggered, then the OpenCL error code can be obtained by the free function `cl_int sycl::opencl::get_error_code(sycl::exception&)`. In the case where there is no OpenCL error associated with the exception triggered, the OpenCL error code will be `CL_SUCCESS`.

### D.4.3 Interoperability with modules

In OpenCL [1] any kernel function that is enqueued over an nd-range is represented by a cl_kernel and must be compiled and linked via a cl_program using `clBuildProgram`, `clCompileProgram` and `clLinkProgram`.

For OpenCL SYCL backend this detail is abstracted away by modules and a module object containing all SYCL kernel functions in a translation is retrieved by calling the free function `this_module::get`.

However, there are cases where it is useful to be able to manually create a module from an input specific to the OpenCL SYCL backend such as OpenCL C source, and intermediate representation/language such as SPIR-V. This can be useful for interoperability with existing OpenCL kernels or libraries or binaries generated by another tool which need to be linked at runtime.

The OpenCL SYCL backend specification provides additional free functions which provide the above functionality, each resulting in an input module which can then be built, compiled and linked as described in 4.13.7.

```cpp
namespace sycl {
namespace opencl {
using binary_blob_t = std::pair<const char*, size_t>;
module<module_state::input> create_module_with_source (context ctx, std::string source);
```

---

1 namespace sycl {
2 namespace opencl {
3 
4 using binary_blob_t = std::pair<const char*, size_t>;
5 
6 module<module_state::input> create_module_with_source (context ctx, std::string source);
```
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module<module_state::input> create_module_with_binary(context ctx, binary_blob_t binary); // (1)

module<module_state::input> create_module_with_il (context ctx, binary_blob_t il);

module<module_state::input> create_module_with_builtin_kernels (context ctx,
                   std::vector<std::string> kernelNames);

D.4.3.1 Free functions

module<module_state::input> create_module_with_source (context ctx, std::string source); // (1)

1. Preconditions: The context specified by ctx must be associated with the OpenCL SYCL backend. The
   OpenCL C source specified by source must not be an empty string.

   Effects: Constructs a module from the provided OpenCL C source specified by source and associated with
   the context specified by ctx by invoking the necessary OpenCL APIs.

   Returns: A module of module_state::input containing the kernels defined in the OpenCL C source specified
   by source.

   Throws: invalid_object_error if any error is produced by invoking the OpenCL APIs.

module<module_state::input> create_module_with_binary(context ctx, binary_blob_t binary); // (1)

1. Preconditions: The context specified by ctx must be associated with the OpenCL SYCL backend. The
   binary blob specified by binary must not contain a null pointer or zero size.

   Effects: Constructs a module from the provided binary blob specified by binary and associated with the
   context specified by ctx by invoking the necessary OpenCL APIs.

   Returns: A module of module_state::input containing the kernels defined in the binary blob specified by
   binary.

   Throws: invalid_object_error if any error is produced by invoking the OpenCL APIs.

module<module_state::input> create_module_with_il (context ctx, binary_blob_t il); // (1)

1. Preconditions: The context specified by ctx must be associated with the OpenCL SYCL backend. The
   intermediate language specified by il must not contain a null pointer or zero size.

   Effects: Constructs a module from the provided intermediate language specified by il and associated with
   the context specified by ctx by invoking the necessary OpenCL APIs.

   Returns: A module of module_state::input containing the kernels defined in the binary intermediate
   language by il.

   Throws: invalid_object_error if any error is produced by invoking the OpenCL APIs.
```cpp
module<module_state::input> create_module_with_builtin_kernels (context ctx, //(1)
std::vector<std::string> kernelNames);
```

1. **Preconditions:** The `context` specified by `ctx` must be associated with the OpenCL SYCL backend. The list of names specified by `kernelNames` must not be empty.

   **Effects:** Constructs a module from the provided builtin kernel names specified by `kernelNames` and associated with the `context` specified by `ctx` by invoking the necessary OpenCL APIs.

   **Returns:** A module of `module_state::input` containing the built-in kernels defined by the list of kernel names specified by `kernelNames`.

   **Throws:** `invalid_object_error` if any error is produced by invoking the OpenCL APIs.

### D.4.4 Interoperability with kernels

It is possible to construct a kernel from a previously created OpenCL `cl_kernel` by calling the interop free function `make_kernel` defined in 4.5.2.3.

This will create a kernel object which can be invoked by any of kernel invocation commands such as `parallel_for` which take a kernel but not SYCL kernel function. Calling `make_kernel` must trigger a call to `clRetainKernel` and the resulting kernel object must call `clReleaseKernel` on destruction.

The kernel arguments for the OpenCL C kernel kernel can either be set prior to creating the kernel object or by calling the `set_arg` member function of the `handler` class.

If kernel arguments are set prior to creating the kernel object the SYCL runtime is not responsible for managing the data of these arguments.

### D.4.5 OpenCL kernel conventions and SYCL

OpenCL and SYCL use opposite conventions for the unit stride dimension. SYCL aligns with C++ conventions, which is important to understand from a performance perspective when porting code to SYCL. The unit stride dimension, at least for data, is implicit in the linearization equations in SYCL (Equation 4.3) and OpenCL. SYCL aligns with C++ array subscript ordering `arr[a][b][c]`, in that range constructor dimension ordering used to launch a kernel (e.g. `range<3> R(a,b,c)`) and range and ID queries within a kernel, are ordered in the same way as the C++ multi-dimensional subscript operators (unit stride on the right).

When specifying a range as the global or local size in a `parallel_for` that invokes an OpenCL interop kernel (through `cl_kernel` interop or `compile_with_source/ build_with_source`), the highest dimension of the range in SYCL will map to the lowest dimension within the OpenCL kernel. That statement applies to both an underlying enqueue operation such as `clEnqueueNDRangeKernel` in OpenCL, and also ID and size queries within the OpenCL kernel. For example, a 3D global range specified in SYCL as:

```cpp
range<3> R{r0,r1,r2};
```

maps to an `clEnqueueNDRangeKernel global_work_size argument of:

```cpp
size_t cl_interop_range[3] = {r2,r1,r0};
```
Likewise, a 2D global range specified in SYCL as:

```c
range<2> R{r0, r1};
```

maps to a `clEnqueueNDRangeKernel` `global_work_size` argument of:

```c
size_t cl_interop_range[2] = {r1, r0};
```

The mapping of highest dimension in SYCL to lowest dimension in OpenCL applies to all operations where a multi-dimensional construct must be mapped, such as when mapping SYCL explicit memory operations to OpenCL APIs like `clEnqueueCopyBufferRect`.

Work-item and work-group ID and range queries have the same reversed convention for unit stride dimension between SYCL and OpenCL. For example, with three, two, or one dimensional SYCL global ranges, OpenCL and SYCL kernel code queries relate to the range as shown in Table D.6. The “SYCL kernel query” column applies for SYCL-defined kernels, and the “OpenCL kernel query” column applies for kernels defined through OpenCL interop.

<table>
<thead>
<tr>
<th>SYCL kernel query</th>
<th>OpenCL kernel query</th>
<th>Returned Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>With enqueued 3D SYCL global <code>range</code> of <code>range&lt;3&gt; R{r0, r1, r2}</code></td>
<td><code>item::get_range(2)</code></td>
<td><code>get_global_size(2)</code> r0</td>
</tr>
<tr>
<td></td>
<td><code>item::get_range(1)</code></td>
<td><code>get_global_size(1)</code> r1</td>
</tr>
<tr>
<td></td>
<td><code>item::get_range(0)</code></td>
<td><code>get_global_size(0)</code> r2</td>
</tr>
<tr>
<td></td>
<td><code>item::get_id(2)</code></td>
<td><code>get_global_id(2)</code> Value in range 0..(r2-1)</td>
</tr>
<tr>
<td></td>
<td><code>item::get_id(1)</code></td>
<td><code>get_global_id(1)</code> Value in range 0..(r1-1)</td>
</tr>
<tr>
<td></td>
<td><code>item::get_id(0)</code></td>
<td><code>get_global_id(0)</code> Value in range 0..(r0-1)</td>
</tr>
</tbody>
</table>

| With enqueued 2D SYCL global `range` of `range<2> R{r0, r1}` | `item::get_range(0)`     | `get_global_size(0)` r0 |
| `item::get_range(1)` | `get_global_size(1)` r1 | `item::get_id(1)` Value in range 0..(r1-1) |
| `item::get_id(0)` | `get_global_id(0)` Value in range 0..(r0-1) |

| With enqueued 1D SYCL global `range` of `range<1> R{r0}` | `item::get_range(0)`     | `get_global_size(0)` r0 |
| `item::get_id(0)` | `get_global_id(0)` Value in range 0..(r0-1) |

Table D.6: Example range mapping from SYCL enqueued three dimensional global `range` to OpenCL and SYCL queries.

## D.4.6 Data types

The OpenCL C language standard [1, Section 6.11] defines its own built-in scalar data types, and these have additional requirements in terms of size and signedness on top of what is guaranteed by ISO C++. For the purpose of interoperability and portability, SYCL defines a set of aliases to C++ types within the `sycl::opencl` namespace using the `cl_` prefix. These aliases are described in Table D.7.
### Table D.7: Scalar data type aliases supported by SYCL OpenCL backend.

<table>
<thead>
<tr>
<th>Scalar data type alias</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>cl_bool</td>
<td>Alias to a conditional data type which can be either true or false. The value true expands to the integer constant 1 and the value false expands to the integer constant 0.</td>
</tr>
<tr>
<td>cl_char</td>
<td>Alias to a signed 8-bit integer, as defined by the C++ core language.</td>
</tr>
<tr>
<td>cl_uchar</td>
<td>Alias to an unsigned 8-bit integer, as defined by the C++ core language.</td>
</tr>
<tr>
<td>cl_short</td>
<td>Alias to a signed 16-bit integer, as defined by the C++ core language.</td>
</tr>
<tr>
<td>cl_ushort</td>
<td>Alias to an unsigned 16-bit integer, as defined by the C++ core language.</td>
</tr>
<tr>
<td>cl_int</td>
<td>Alias to a signed 32-bit integer, as defined by the C++ core language.</td>
</tr>
<tr>
<td>cl_uint</td>
<td>Alias to an unsigned 32-bit integer, as defined by the C++ core language.</td>
</tr>
<tr>
<td>cl_long</td>
<td>Alias to a signed 64-bit integer, as defined by the C++ core language.</td>
</tr>
<tr>
<td>cl_ulong</td>
<td>Alias to an unsigned 64-bit integer, as defined by the C++ core language.</td>
</tr>
<tr>
<td>cl_float</td>
<td>Alias to a 32-bit floating-point. The float data type must conform to the IEEE 754 single precision storage format.</td>
</tr>
<tr>
<td>cl_double</td>
<td>Alias to a 64-bit floating-point. The double data type must conform to the IEEE 754 double precision storage format.</td>
</tr>
<tr>
<td>cl_half</td>
<td>Alias to a 16-bit floating-point. The half data type must conform to the IEEE 754-2008 half precision storage format. An exception with the errc::feature_not_supported error code must be thrown if the half type is used in a SYCL kernel function which executes on a SYCL device that does not support the extension khr_fp16.</td>
</tr>
</tbody>
</table>

End of table

### D.5 Preprocessor directives and macros

- SYCL_BACKEND_OPENCL substitutes to one if the OpenCL SYCL backend is active while building the SYCL application.

### D.5.1 Offline linking with OpenCL C libraries

SYCL supports linking SYCL kernel functions with OpenCL C libraries during offline compilation or during online compilation by the SYCL runtime within a SYCL application.
D.5. PREPROCESSOR DIRECTIVES AND MACROS

Linking with OpenCL C kernel functions offline is an optional feature and is unspecified. Linking with OpenCL C kernel functions online is performed by using the SYCL module class to compile and link an OpenCL C source; using the compile_with_source or build_with_source member functions.

OpenCL C functions that are linked with, using either offline or online compilation, must be declared as extern "C" function declarations. The function parameters of these function declarations must be defined as the OpenCL C interoperability aliases; pointer of the multi_ptr class template, vector_t of the vec class template and scalar data type aliases described in Table D.7.

For example:

```cpp
extern "C" typename sycl::decorated_global_ptr<std::int32_t>::pointer my_func(
    sycl::float4::vector_t x, double y);
```
D.6 SYCL support of non-core OpenCL features

In addition to the OpenCL core features, SYCL also provides support for OpenCL extensions which provide features in OpenCL via khr extensions.

Some extensions are natively supported within the SYCL interface, however some can only be used via the OpenCL interoperability interface. The SYCL interface required for native extensions must be available. However if the respective extension is not supported by the executing SYCL device, the SYCL runtime must throw an exception with the errc::feature_not_supported error code.

The OpenCL backend exposes khr extensions to SYCL applications through the sycl::aspect enumerated type. Therefore, applications can query for the existence of khr extensions by calling the device::has() or platform::has() member functions.

All OpenCL extensions are available through the OpenCL interoperability interface, but some can also be used through core SYCL APIs. Table D.8 shows which these are. Table D.8 also shows the mapping from each OpenCL extension name to its associated SYCL device aspect. Note that some aspects are part of the core SYCL specification, and these are in namespace sycl::aspect. Other aspects are specific to the OpenCL backend, and these are in namespace sycl::opencl::aspect.

<table>
<thead>
<tr>
<th>SYCL Aspect</th>
<th>OpenCL Extension</th>
<th>Core SYCL API</th>
</tr>
</thead>
<tbody>
<tr>
<td>aspect::int64_base_atomics</td>
<td>cl_khr_int64_base_atomics</td>
<td>Yes</td>
</tr>
<tr>
<td>aspect::int64_extended_atomics</td>
<td>cl_khr_int64_extended_atomics</td>
<td>Yes</td>
</tr>
<tr>
<td>aspect::fp16</td>
<td>cl_khr_fp16</td>
<td>Yes</td>
</tr>
<tr>
<td>opencl::aspect::3d_image_writes</td>
<td>cl_khr_3d_image_writes</td>
<td>Yes</td>
</tr>
<tr>
<td>opencl::aspect::khr_gl_sharing</td>
<td>cl_khr_gl_sharing</td>
<td>No</td>
</tr>
<tr>
<td>opencl::aspect::apple_gl_sharing</td>
<td>cl_apple_gl_sharing</td>
<td>No</td>
</tr>
<tr>
<td>opencl::aspect::d3d10_sharing</td>
<td>cl_khr_d3d10_sharing</td>
<td>No</td>
</tr>
<tr>
<td>opencl::aspect::d3d11_sharing</td>
<td>cl_khr_d3d11_sharing</td>
<td>No</td>
</tr>
<tr>
<td>opencl::aspect::dx9_media_sharing</td>
<td>cl_khr_dx9_media_sharing</td>
<td>No</td>
</tr>
</tbody>
</table>

End of table

Table D.8: SYCL support for OpenCL 1.2 extensions.

D.6.1 Half precision floating-point

The half scalar data type: half and the half vector data types: half1, half2, half3, half4, half8 and half16 must be available at compile-time. However if any of the above types are used in a SYCL kernel function, executing on a device which does not support the extension khr_fp16, the SYCL runtime must throw an exception with the errc::feature_not_supported error code.

The conversion rules for half precision types follow the same rules as in the OpenCL 1.2 extensions specification [5, par. 9.5.1].

The math functions for half precision types follow the same rules as in the OpenCL 1.2 extensions specification [5, par. 9.5.2, 9.5.3, 9.5.4, 9.5.5]. The allowed error in ULP(Unit in the Last Place) is less than 8192, corresponding to Table 6.9 of the OpenCL 1.2 specification [1].
D.6.2 Writing to 3D image memory objects

The accessor class for target access::target::image in SYCL support member functions for writing 3D image memory objects, but this functionality is only allowed on a device if the extension cl_khr_3d_image_writes is supported on that device.

D.6.3 Interoperability with OpenGL

Interoperability between SYCL and OpenGL is not directly provided by the SYCL interface, however can be achieved via the SYCL OpenCL interoperability interface.
E. What has changed from previous versions

E.1 What has changed from SYCL 1.2.1 to SYCL 2020

The SYCL runtime moved from namespace `cl::sycl` provided by `#include "CL/sycl.hpp"` to namespace `sycl` provided by `#include "SYCL/sycl.hpp"` as explained in Section 4.3. The old header file is still available for compatibility with SYCL 1.2.1 applications.

The SYCL specification is now based on the core language of C++17, as described in Section 3.8.1. Features of C++17 are now enabled within the specification, such as deduction guides for class template argument deduction.

Naming of lambda functions passed to kernel invocations is now optional.

The `accessor` class definition and interface have been significantly modified to simplify commonly written SYCL code. Default modes have been defined and modifiers use tag arguments. Host accessors have now their own class `host_accessor` as explained in Section 4.7.6.10.

Kernel attributes have been better described and are now applied to the function type of a kernel function, which allows them to be applied directly to lambdas. This means that propagation of the attribute from a function to the calling kernel is no longer required, and attributes are instead applied directly to the kernel function that they impact.

The list of built-in integer math functions was extended with `ctz()` in Tables 4.15. Specification of `clz()` was extended with the case of argument is 0.

The classes `vector_class`, `string_class`, `function_class`, `mutex_class`, `shared_ptr_class`, `weak_ptr_class`, `hash_class` and `exception_ptr_class` have been removed from the API and the standard classes `std::vector`, `std::string`, `std::function`, `std::mutex`, `std::shared_ptr`, `std::weak_ptr`, `std::hash` and `std::exception_ptr` are used instead.

`operator[]` of SYCL `accessor` for SYCL `buffer` was changed to return `const` reference when `accessMode == access::mode::read`.

The specific `sycl::buffer` API taking `std::unique_ptr` has been removed. The behavior is the same as in SYCL 1.2.1 but with a simplified API. Since there is still the API taking `std::shared_ptr` and there is an implicit conversion from a `std::unique_ptr` prvalue to a `std::shared_ptr`, the API can still be used as before with a `std::unique_ptr` to give away memory ownership.

Unified Shared Memory (USM), in Section 4.8, has been added as a pointer-based strategy for data management. It defines several types of allocations with various accessibility rules for host and devices. USM is meant to complement buffers, not replace them.

The `queue` class received a new `property` that requires in-order semantics for a queue where operations are executed in the order in which they are submitted.

The `queue` class received several new member functions to define kernels directly on a queue objects instead of
inside a command group handler in the `submit` member function.

The `program` class has been replaced with the `module` which provides a type-safe and thread-safe interface for compiling and linking `SYCL kernel function`. The previous member functions of the `program` class are now functions. Modules are now retrieved from the `this_module::get` function which produces a `module` containing the `SYCL kernel functions` of the current translation unit.

The `module` class now supports `specialization constants` which allow SYCL `kernel functions` to define constant variables, whose value is not known until the containing `module` is compiled. The `specialization_constant` class has been introduced to represent a reference to a `specialization constant` both in the SYCL application for setting the value and in a `SYCL kernel function` for retrieving the value.

The `kernel_handler` class has been introduced as an optional parameter to `kernel invocation commands` to provide functionality and queries relating to a `SYCL kernel function` at the `kernel scope`, including getting the value of a `specialization constant`.

The constructors for SYCL `context` and `queue` are made `explicit` to prevent ambiguities in the selected constructor resulting from implicit type conversion.

The requirement for C++ standard layout for data shared between host and devices has been softened and now only C++ trivially copyable is required, as explained mainly in Section 4.14.4.

The concept of a `group` of `work-items` was generalized to include `work-groups` and `sub-groups`. A `work-group` is represented by the `sycl::group` class as in SYCL 1.2.1, and a `sub-group` is represented by the new `sycl::sub_group` class.

The `host_task` member function for the `queue` has been introduced for en-queueing `host tasks` on a `queue` to schedule the `SYCL runtime` to invoke native C++ functions, conforming to the SYCL memory model. `Host tasks` also support interoperability with the native `SYCL backend` objects associated at that point in the DAG using the optional `interop_handle` class.

A library of algorithms based on the C++17 algorithms library was introduced in Section 4.19.4. These algorithms provide a simple way for developers to apply common parallel algorithms using the work-items of a group.

The definition of the `sycl::group` class was modified to support the new group functions in Section 4.19.5. New member types and variables were added to enable generic programming, and member functions were updated to encapsulate all functionality tied to `work-groups` in the `sycl::group` class. See Table 4.85 for details.

The `barrier` and `mem_fence` member functions of the `nd_item` class have been removed. The `barrier` member function has been replaced by the `group_barrier()` function, which can be used to synchronize either `work-groups` or `sub-groups`. The `mem_fence` member function has been replaced by the `atomic_fence` function, which is more closely aligned with `std::atomic_thread_fence` and offers control over memory ordering and scope.

Changes in the SYCL `vec` class described in Section 4.16.2:

- `operator[]` was added;
- unary `operator+()` and `operator-()` were added;
- `get_count()` and `get_size()` were made `static constexpr`.

Buffer and local accessors now meet the C++ requirement of `ContiguousContainer`. This includes (but is not limited to) returning `begin` and `end` iterators, specifying a default constructible accessor that can be passed to a
kernel but not dereferenced, and making them equality comparable within kernel functions. accessor::get_size() has been removed to prevent confusion with accessor::size(), and replaced with accessor::byte_size(). All buffer and local accessor size and iterator queries have been marked noexcept.

The device selection now relies on a simpler API based on ranking functions used as device selectors described in Section 4.6.1.1.

A new reduction library consisting of the reduction function, reducer class and parallel_reduce was introduced to simplify the expression of variables with reduction semantics in SYCL kernels. See Section 4.10.2.

Global and constant accessors can now be constructed as placeholders without specifying the accessor::placeholder template parameter (which has been deprecated). It is allowed to call handler::require on both placeholder and non-placeholder global and constant buffer accessors, and it is allowed to call it multiple times. accessor::is_placeholder is not constexpr anymore.

The image class has been replaced with unsampled_image and sampled_image classes. The sampler class has been modified to support these changes.

Specified the constness semantics of accessors. const dataT and accessor::mode::read are semantically equivalent, and having at least one of those parameters part of the accessor type makes the accessor read-only. Defined implicit conversions based on these semantics. Specified default access mode to be accessor::mode::read for const dataT and accessor::mode::read_write otherwise. Specified default accessor dimensions to be 1. All of these rules enable most buffer accessor code to only need to use accessor<T> for mutable data and accessor<const T> for const data.

The atomic class from SYCL 1.2.1 and accessors using accessor::mode::atomic were deprecated in favor of a new atomic_ref interface.

The SYCL exception class hierarchy has been condensed into a single exception type: exception. exception now derives from std::exception. The variety of errors are now provided via error codes, which aligns with the C++ error code mechanism.

The new error code mechanism now also generalizes the previous get_cl_code interface to provide a generic interface way for querying backend-specific error codes.

Default asynchronous error handling behavior is now defined, so that asynchronous errors will cause abnormal program termination even if a user-defined asynchronous handler function is not defined. This prevents asynchronous errors from being silently lost during early stages of application development.

Kernel invocation functions, such as parallel_for, now take kernel functions by const reference. Kernel functions must now have a const-qualified operator(), and are allowed to be copied zero or more times by an implementation. These clarifications allow implementations to have flexibility for specific devices, and define what users should expect with kernel functors. Specifically, kernel functors cannot be marked as mutable, and sharing of data between work-items should not be attempted through state stored within a kernel functor.

A new concept called device aspects has been added, which tells the set of optional features a device supports. This new mechanism replaces the has_extension() function and some uses of get_info().

There is a new Chapter 6 which describes how extensions to the SYCL language can be added by vendors and by The Khronos Group.

A queue constructor has been added that takes both a device and context, to simplify interfacing with libraries.
The `parallel_for` interface has been simplified in some forms to accept a braced initializer list in place of a `range`, and to always take `item` arguments. Kernel invocation functions have also been modified to accept generic lambda expressions. Some implicit conversions have been defined to allow one-dimensional `item` to convert to scalar types. All of these modifications lead to simpler SYCL code in common use cases.

Some device-specific queries have been renamed to more clearly be “device-specific kernel” `get_info` queries (`info::kernel_device_specific`) instead of “work-group” (`get_workgroup_info`) and sub-group (`get_sub_group_info`) queries.

A new math array type `marray` has been defined to begin disambiguation of the multiple possible interpretations of how `sycl::vec` should be interpreted and implemented.

Changes in SYCL address spaces:

- the address space meaning has been significantly improved;
- the generic address space was introduced;
- behavior of unannotated pointer/reference (raw pointer/reference) is now dependent on the compilation mode. The compiler can either interpret unannotated pointer/reference has addressing the generic address space or to be deduced;
- some ambiguities in the address space deduction were clarified. Notably that deduced type does not affect the user-provided type.

Changes in `multi_ptr` interface:

- addition of `access::address_space::generic_space` to represent the generic address space;
- an extra template parameter to allow to select a flavor of the `multi_ptr` interface. There are now 3 different interfaces:
  - interface exposing undecorated types. Returned pointer and reference are not annotated by an address space;
  - interface exposing decorated types. Returned pointer and reference are annotated by an address space;
  - legacy 1.2.1 interface (deprecated).
- deprecation of the 1.2.1 interface;
- `global_ptr`, `local_ptr`, `constant_ptr` and `private_ptr` alias take the new extra parameter;
- addition of the `address_space_cast` free function to cast undecorated pointer to `multi_pointer`;
- addition of construction/conversion operator for the generic address space;
- removal of the constructor and assignment operator taking an unannotated pointer;
- implicit conversion to a pointer is now deprecated. `get` should be used instead;
- the return type of the member function `get` now depends on the selected interface.
- addition of the member function `get_raw` which returns the underlying pointer as an unannotated pointer;
• addition of the member function `get_decorated` which returns the underlying pointer as an annotated pointer.

The accessor member function `get_pointer` now returns a raw pointer. The `get_multi_ptr` member function was introduced to accessor classes which return the `multi_ptr` class to the appropriate space.

The `cl::sycl::byte` has been deprecated and now the C++17 `std::byte` should be used instead.
References


Glossary

**accessor**  An accessor is a class which allows a SYCL kernel function to access data managed by a buffer or image class. Accessors are used to express the dependencies among the different command groups. For the full description please refer to section [4.7.6]. 30, 31, 37, 38, 109, 110, 124, 142, 144, 263, 265, 266

**SYCL application**  A SYCL application is a C++ application which uses the SYCL programming model in order to execute kernels on devices. 30, 33, 57, 58, 59, 265, 266, 272, 325, 416, 423, 424, 428

**application scope**  The application scope starts with the construction first SYCL runtime class object and finishes with the destruction of the last one. Application refers to the C++ SYCL application and not the SYCL runtime. 30, 33, 159

**aspect**  A characteristic of a device which determines whether it supports some optional feature. Aspects are always boolean, so a device either has or does not have an aspect. 70, 78, 88, 91, 387, 417

**async_handler**  An asynchronous error handler object is a function class instance providing necessary code for handling all the asynchronous errors triggered from the execution of command groups on a queue, within a context or an associated event. For the full description please refer to section [4.15.2]. 7, 94, 100, 104, 106, 107, 285, 286

**asynchronous error**  A SYCL asynchronous error is an error occurring after the host API call invoking the error causing action has returned, such that the error cannot be thrown as a typical C++ exception from a host API call. Such errors are typically generated from device kernel invocations which are executed when SYCL task graph dependencies are satisfied, which occur asynchronously from host code execution. For the full description and associated asynchronous error handling mechanisms, please refer to section [4.15]. 46, 94, 100, 104, 106, 107, 285, 286

**SYCL backend**  An implementation of the SYCL programming model using an heterogeneous programming API. A SYCL backend exposes one or multiple SYCL platforms. For example, the OpenCL backend, via the ICD loader, can expose multiple OpenCL platforms. 3, 19, 29, 31, 32, 33, 34, 37, 38, 45, 46, 47, 48, 49, 51, 55, 56, 57, 58, 61, 64, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 88, 92, 93, 99, 101, 103, 105, 106, 107, 108, 109, 110, 124, 178, 266, 268, 269, 270, 271, 278, 287, 289, 292, 324, 370, 375, 379, 381, 382, 388, 407, 408, 409, 411, 416, 424, 429

**buffer**  The buffer class manages data for the SYCL C++ host application and the SYCL device kernels. The buffer class may acquire ownership of some host pointers passed to its constructors according to the constructor kind.

The buffer class, together with the accessor class, is responsible for tracking memory transfers and guaranteeing data consistency among the different kernels. The SYCL runtime manages the memory allocations on both the host and the device within the lifetime of the buffer object. For the full description please refer to section [4.7.2]. 31, 37, 42, 48, 49, 109, 142, 158, 263, 265, 423, 429

**command**  A request to execute work that is submitted to a queue such as the invocation of a SYCL kernel
function, the invocation of a host task or an asynchronous copy. 243, 244, 263, 267, 425, 429

**command group handler** The command group handler class provides the interface for the commands that can be executed inside the command group scope. It is provided as a scoped object to all of the data access requests within the command group scope. For the full description please refer to section [4.10.4]. 159, 239, 244, 248, 260, 262, 263, 266, 424, 427

**command group function object** A type which is callable with `operator()` that takes a reference to a command group handler, that defines a command group which can be submitted by a queue. The function object can be a named type, lambda function or `std::function`. 31, 32, 34, 46, 47, 48, 100, 243, 244, 286, 424

**command group scope** The command group scope is the function scope defined by the command group function object. The command group command group handler object lifetime is restricted to the command group scope. For more details please see [4.10.3]. 30, 31, 33, 51, 57, 159, 243, 244, 424

**command group** In SYCL, the operations required to process data on a device are represented using a command group function object. Each command group function object is given a unique command group handler object to perform all the necessary work required to correctly process data on a device using a kernel. In this way, the group of commands for transferring and processing data is enqueued as a command group on a device for execution. A command group is submitted atomically to a SYCL queue. 11, 33, 34, 35, 36, 37, 38, 39, 94, 104, 105, 108, 109, 145, 248, 260, 261, 262, 263, 266, 272, 423, 424, 425, 426

**constant memory** A region of global memory that remains constant during the execution of a kernel. The SYCL runtime allocates and initializes memory objects placed into constant memory. 40, 143, 147

**context** A context represents the runtime data structures and state required by a SYCL backend API to interact with a group of devices associated with a platform. The context is defined as the `sycl::context` class, for further details please see [4.6.3]. 31, 32, 34, 48, 72, 73, 74, 247, 262, 263, 264, 266, 271, 272, 276, 278, 279, 280, 281, 402, 408, 409, 424, 427

**device** A SYCL device encapsulates a number of heterogeneous devices exposed by a SYCL platform from a given SYCL backend. SYCL devices can execute SYCL kernel functions. 29, 30, 31, 32, 33, 34, 48, 71, 73, 74, 79, 80, 88, 91, 92, 93, 94, 103, 262, 263, 264, 266, 271, 275, 276, 279, 280, 402, 403, 414, 423, 424, 425, 426, 427, 428, 429

**device function** A device function is any function in a SYCL application that can be run on a device. This includes SYCL kernel functions and, recursively, and functions they call. 376

**device image selection function** A callable object which takes the begin and end iterators of a module pointing to a sequence of device image and returns an iterator to a chosen device image. 262

**device image** A SYCL device image represents an implementation defined file format that encapsulates the relevant functions, symbols and meta-data to represent the SYCL kernel function set of a module. 247, 262, 271, 272, 274, 276, 277, 278, 279, 280, 424, 427

**device selector** A way to select a device used in various places. This is a callable object taking a device reference and returning an integer rank. One of the device with the highest positive value is selected. See Section 4.6.1.1 for more details. 48, 66, 67, 68, 69, 70, 75, 77, 97, 98, 417

**device compiler** A SYCL device compiler is a compiler that produces OpenCL device binaries from a valid SYCL application. For the full description please refer to section [5]. 30, 42, 50, 234, 278, 375, 401
event A SYCL object that represents the status of an operation that is being executed by the SYCL runtime. 105, 108

executable A state which a module can be in, representing SYCL kernel functions as an executable. 274, 276, 425

executable module A module that is of module state executable. 272, 276, 277, 278, 427

SYCL file A SYCL C++ source file that contains SYCL API calls. 428

generic memory Generic memory is a virtual memory region which can represent global, local and private memory region. 41

global memory Global memory is a memory region accessible to all work-items executing on a device. 40, 143, 147, 187

global id As in OpenCL, a global ID is used to uniquely identify a work-item and is derived from the number of global work-items specified when executing a kernel. A global ID is a one, two or three-dimensional value that starts at 0 per dimension. 36, 223, 224, 426, 429

group A group of work-items within the index space of a SYCL kernel execution, such as a work-group or sub-group. 42, 324, 353, 416, 427

group mem-fence A group mem-fence guarantees that any memory access before a group barrier must complete before continuing to process any data after the barrier. All work-items in the group execute a release fence prior to synchronizing at the barrier, and all work-items in the group execute an acquire fence afterwards. The scope of these fences can be specified by a memory_scope. 359, 425, 427

group barrier A synchronization function within a group of work-items. All the work-items of a group must execute the barrier construct for any work-item continues execution beyond the barrier. Additionally a group barrier performs a group mem-fence. 42, 46, 324, 357, 358, 425, 429

host Host is the system that executes the C++ application including the SYCL API. 29, 50, 159, 167, 173, 324, 428, 429

SYCL host device See SYCL host backend. 324, 399, 401, 426

host task command A type of command that can be used inside a command group in order to schedule a native C++ function. 263

host task A command which invokes a native C++ callable, scheduled conforming to SYCL dependency rules. 263, 264, 265, 266, 416, 424

host pointer A pointer to memory on the host. Cannot be accessed directly from a device. 140

SYCL host backend The SYCL host device is a native C++ implementation of a device. It does not have an native handle. It has full SYCL capabilities and reports them through the SYCL information retrieval interface. The SYCL host device is mandatory for every SYCL implementation and is always available, but may not achieve the same performance as a different backend running on the CPU. Any C++ application debugger can be used for debugging SYCL kernels executing on a SYCL host device. 33, 55, 66, 67, 69, 71, 73, 79, 99, 399, 425
**id**  It is a unique identifier of an item in an index space. It can be one, two or three dimensional index space, since the SYCL kernel execution model is an **nd-range**. It is one of the index space classes. For the full description please refer to section 4.10.1.3. 217, 222, 223, 224, 230, 234, 426, 427

**image**  Images in SYCL, like buffers, are abstractions of multidimensional structured arrays. Image can refer to **unsampled_image** and **sampled_image**. For the full description please refer to section [4.7.3]. 31, 37, 48, 49, 109, 142, 263, 265, 423

**index space classes**  The OpenCL kernel execution model defines an **nd-range** index space. The SYCL runtime class that defines an **nd-range** is the **sycl::nd_range**, which takes as input the sizes of global and local work-items, represented using the **sycl::range** class. The kernel library classes for indexing in the defined **nd-range** are the following classes:

- **sycl::id**: The basic index class representing a **id**.
- **sycl::item**: The index class that contains the **global id** and **local id**.
- **sycl::nd_item**: The index class that contains the **global id**, **local id** and the **work-group id**.
- **sycl::group**: The group class that contains the **work-group id** and the member functions on a work-group.

**input**  A state which a **module** can be in, representing SYCL kernel functions as a source or intermediate representation. 274, 275, 426

**input module**  A **module** that is of **module state input**. 272, 275, 278, 279, 407, 427, 428

**item**  An item id is an interface used to retrieve the **global id**, **work-group id** and **local id**. For further details see [4.10.1.4]. 220, 225, 226, 227, 426

**kernel**  A SYCL kernel which can be executed on a **device**, including the SYCL host device. Is created implicitly when defining a **SYCL kernel function** (see 4.10) but can also be created manually in order to pre-compile SYCL kernel functions. 34, 50, 75, 110, 248, 252, 260, 267, 269, 270, 281, 403, 409, 423, 427

**kernel invocation command**  A type of command that can be used inside a **command group** in order to schedule a **SYCL kernel function**, includes **single_task**, all variants of **parallel_for** and **parallel_for_workgroup**. 262, 263, 271, 272, 376, 409, 416, 429

**kernel handler**  A representation of a SYCL kernel function being invoked that is available to the **kernel scope**. 247

**kernel name**  A kernel name is a class type that is used to assign a name to the kernel function, used to link the host system with the kernel object output by the device compiler. For details on naming kernels please see [5.2]. 50, 248, 249, 250, 251, 252, 283, 429

**kernel scope**  The function scope of the **operator()** on a SYCL kernel function. Note that any function or member function called from the kernel is also compiled in kernel scope. The kernel scope allows C++ language extensions as well as restrictions to reflect the capabilities of OpenCL devices. The extensions and restrictions are defined in the SYCL device compiler specification. 30, 33, 247, 416, 426

**SYCL kernel function**  A type which is callable with **operator()** that takes a **id**, **item**, **nd-item** or **work-group**
which can be passed to kernel enqueue member functions of the command group handler. A SYCL kernel function defines an entry point to a kernel. The function object can be a named trivially copyable type or lambda function.

**local memory**  Local memory is a memory region associated with a work-group and accessible only by work-items in that work-group.

**local id**  A unique identifier of a work-item among other work-items of a work-group.

**mem-fence**  A memory fence guarantees that any memory access must complete before continuing to process any data after the fence. The `sycl::atomic_fence` function acts as a fence across all work-items and devices specified by a `memory_scope` argument, and a `group mem-fence` acts as a fence across all work-items in a specific group.

**module**  A SYCL module represents a set of SYCL kernel functions which can be executed on a number of devices associated with a context.

**module state**  A SYCL module state represents an the state abstract state of a module and therefore it’s capabilities in the SYCL programming API. Can be either a input module, object module or an executable module.

**native-specialization constant**  A specialization constant which is natively supported by the device image which contains it.

**native backend object**  An opaque object defined by a specific backend that represents a high-level SYCL object on said backend. There is no guarantee of having native backend objects for all SYCL types.

**nd-item**  A unique identifier representing a single work-item and work-group within the index space of a SYCL kernel execution. Can be one, two or three dimensional. In the SYCL interface a nd-item is represented by the `nd_item` class (see Section 4.10.1.5).

**nd-range**  A representation of the index space of a SYCL kernel execution, the distribution of work-items within into work-groups. Contains a range specifying the number of global work-items, a range specifying the number of local work-items and a id specifying the global offset. Can be one, two or three dimensional. The minimum size of each range within the nd-range is 1 per dimension. In the SYCL interface an nd-range is represented by the `nd_range` class (see Section 4.10.1.2).

**object**  A state which a module can be in, representing SYCL kernel functions as a non-executable object.

**object module**  A module that is of module state object.

**platform**  The host together or a collection of devices managed by the OpenCL framework that allow an application to share resources and execute kernels on devices in the platform. A SYCL application can target one
or multiple OpenCL platforms provided by OpenCL device vendors [1]. 32, 33, 34, 48, 71, 74, 402, 423, 424

**private memory** A region of memory private to a work-item. Variables defined in one work-item’s private memory are not visible to another work-item. [1]. The `sycl::private_memory` class provides access to the work-item’s private memory for the hierarchical API as it is described at [4.10.7.3]. 41, 258

**queue** A SYCL command queue is an object that holds command groups to be executed on a SYCL device. SYCL provides a heterogeneous platform integration using device queue, which is the minimum requirement for a SYCL application to run on a SYCL device. For the full description please refer to section [4.6.5]. 31, 32, 33, 34, 47, 48, 103, 262, 263, 264, 266, 402, 403, 416, 423, 424

**range** A representation of a number of work-items or work-group within the index space of a SYCL kernel execution. Can be one, two or three dimensional. In the SYCL interface a work-group is represented by the `group` class (see Section 4.10.1.7). 427

**reduction** An operation that produces a single value by combining multiple values in an unspecified order using a binary operator. If the operator is non-associative or non-commutative, the behavior of a reduction may be non-deterministic. 44, 234, 235, 237, 417

**rule of zero** For a given class, if the copy constructor, move constructor, copy assignment operator, move assignment operator and destructor would all be inlined, public and defaulted, none of them should be explicitly declared. 62, 63

**rule of five** For a given class, if at least one of the copy constructor, move constructor, copy assignment operator, move assignment operator or destructor is explicitly declared, all of them should be explicitly declared. 62, 63

**SYCL runtime** A SYCL runtime is an implementation of the SYCL API specification. The SYCL runtime manages the different OpenCL platforms, devices, contexts as well as memory handling of data between host and OpenCL contexts to enable semantically correct execution of SYCL programs. 32, 39, 45, 46, 47, 48, 49, 50, 55, 56, 57, 58, 59, 60, 62, 64, 66, 94, 104, 109, 111, 118, 121, 123, 125, 127, 129, 130, 131, 132, 135, 136, 138, 139, 140, 142, 148, 158, 159, 167, 173, 230, 244, 247, 249, 250, 251, 252, 253, 263, 264, 265, 266, 267, 272, 276, 284, 285, 291, 326, 375, 377, 401, 403, 409, 411, 413, 416, 423, 424, 426

**SMCP** The single-source multiple compiler-passes (SMCP) technique allows a single source file to be parsed by multiple compilers for building native programs per compilation target. For example, a standard C++ CPU compiler for targeting host will parse the SYCL file to create the C++ SYCL application which offloads parts of the computation to other devices. A SYCL device compiler will parse the same source file and target only SYCL kernels. 27, 28, 50, 92, 375, 387

**specialization id** An identifier which represents a reference to a specialization constant both in the SYCL application for setting the value prior to the compilation of an input module and in a SYCL kernel function for retrieving the value during invocation. 272, 275

**specialization constant** A constant variable where the value is not known until compilation of the SYCL kernel function. 247, 263, 272, 275, 277, 278, 283, 416, 427, 428

**string kernel name** The name of a SYCL kernel function in string form, this can be the name of a kernel function created via interop or a string form of a type kernel name. 277, 280
sub-group barrier  A group barrier for all work-items in a sub-group. 46

sub-group The SYCL sub-group (sycl::sub_group class) is a representation of a collection of related work-items within a work-group that execute concurrently, and which may make independent forward progress with respect to other sub-groups in the same work-group. For further details for the sycl::sub_group class see [4.10.1.8]. 42, 223, 233, 234, 416, 425, 429

SYCL C++ template library The template library is a set of C++ templated classes which provide the programming interface to the SYCL developer. 401

SYCL backend API The exposed API for writing SYCL code against a given SYCL backend. 3, 29, 34, 35, 48, 50, 57

type kernel name The name of a SYCL kernel function in type form, this can be either a kernel name provided to a kernel invocation command or the type of a function object use as a SYCL kernel function. 280, 428

USM Unified Shared Memory (USM) provides a pointer-based alternative to the buffer programming model. USM enables:

- easier integration into existing code bases by representing allocations as pointers rather than buffers, with full support for pointer arithmetic into allocations
- fine-grain control over ownership and accessibility of allocations, to optimally choose between performance and programmer convenience;
- a simpler programming model, by automatically migrating some allocations between SYCL devices and the host.

See Section 4.8. 27, 42, 196, 197, 377

work-group barrier A group barrier for all work-items in a work-group. 46, 255, 256, 258, 324

work-group id As in OpenCL, SYCL kernels execute in work-groups. The group ID is the ID of the work-group that a work-item is executing within. A group ID is an one, two or three dimensional value that starts at 0 per dimension. 36, 222, 230, 426, 429

work-group The SYCL work-group (sycl::group class) is a representation of a collection of related work-items that execute on a single compute unit. The work-items in the group execute the same kernel-instance and share local memory and work-group functions [1]. For further details for the sycl::group class see [4.10.1.7]. 36, 42, 167, 223, 224, 228, 230, 231, 234, 248, 256, 257, 416, 425, 426, 427, 428, 429

work-item The SYCL work-item is a representation of a work-item among a collection of parallel executions of a kernel invoked on a device by a command. A work-item is executed by one or more processing elements as part of a work-group executing on a compute unit. A work-item is distinguished from other work-items by its global id or the combination of its work-group id and its local id within a work-group [1]. 36, 42, 167, 221, 222, 224, 225, 256, 257, 258, 416, 425, 427, 428, 429